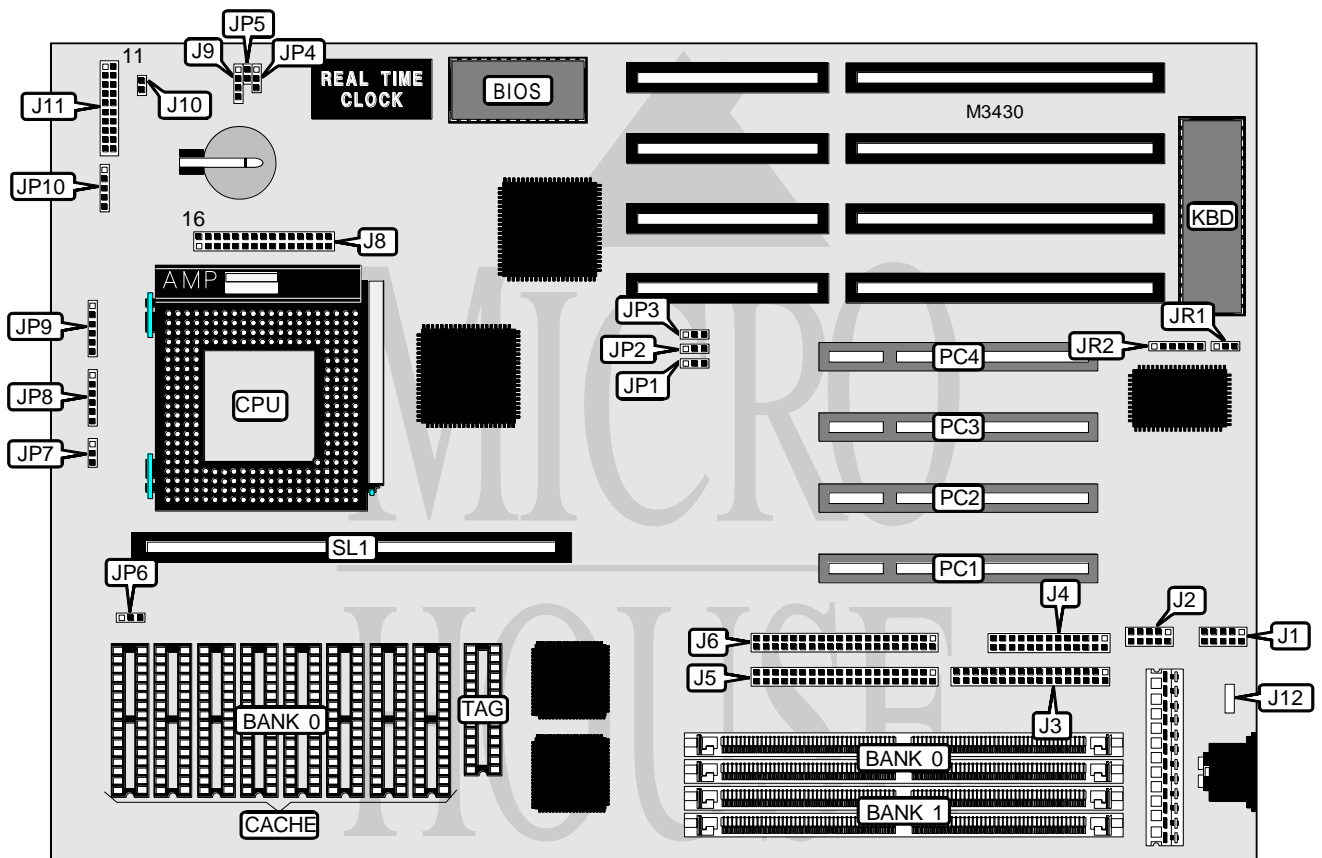


SPRING CIRCLE COMPUTER, INC. ST586 REV. P550 PCI MB

Processor	CX M1/Pentium
Processor Speed	75/90/100/120/133/150/166MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	AMI/Award
Dimensions	280mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, serial ports (2), cache slot, VRM connector
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	J1	Turbo LED	J11 pins 2 & 3
Serial port 1	J2	Green PC connector	J11 pins 4 & 5
Floppy drive interface	J3	Turbo switch	J11 pins 6 & 7
Parallel port	J4	Reset switch	J11 pins 9 & 10
IDE interface 2	J5	Power LED & keylock	J11 pins 11 - 15
IDE interface 1	J6	Speaker	J11 pins 17 - 20
VRM connector	J8	32-bit PCI slots	PC1 - PC4
External battery	J9	Cache slot	SL1
IDE interface LED	J10		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
Jumper information unavailable	J12	Unidentified
í Flash BIOS voltage select 5v	JP4	Pins 1 & 2 closed
Flash BIOS voltage select 12v	JP4	Pins 2 & 3 closed
í CMOS memory normal operation	JP5	Open
CMOS memory clear	JP5	Closed
í Parallel port IRQ select IRQ7	JR1	Pins 1 & 2 closed
Parallel port IRQ select IRQ5	JR1	Pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None

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CACHE CONFIGURATION			
Size	Bank 0	TAG	SL1
256KB (A)	(8) 32K x 8	(1) 8K x 8	Not installed
256KB (B)	None	(1) 8K x 8	256KB module installed
512KB (A)	(8) 64K x 8	(1) 32K x 8	Not installed
512KB (B)	None	(1) 32K x 8	512KB module installed

CACHE JUMPER CONFIGURATION		
Size	JP7	JP9
None	Pins 1 & 2 closed	Pins 1 & 2, 4 & 5 closed
256KB (A)	Pins 2 & 3 closed	Pins 1 & 2, 5 & 6 closed
256KB (B)	Open	Open
512KB (A)	Pins 1 & 2 closed	Pins 2 & 3, 4 & 5 closed
512KB (B)	Open	Open

CACHE VOLTAGE CONFIGURATION	
Size	JP6
3.3v	Pins 2 & 3 closed
5v mixed	Pins 1 & 2 closed

CPU SPEED SELECTION (CYRIX)				
Speed	JP1	JP2	JP3	JP8
120MHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2, 5 & 6 closed
150MHz	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2, 5 & 6 closed
166MHz	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2, 5 & 6 closed

CPU SPEED SELECTION (INTEL)				
Speed	JP1	JP2	JP3	JP8
75MHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2, 4 & 5 closed
90MHz	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2, 4 & 5 closed
100MHz	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2, 4 & 5 closed
120MHz	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2, 5 & 6 closed
133MHz	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2, 5 & 6 closed
150MHz	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3, 5 & 6 closed
166MHz	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3, 5 & 6 closed

CPU VOLTAGE SELECTION	
Voltage	JP10
3.3v	Pins 1 & 2 closed
3.4v	Pins 2 & 3 closed
3.5v	Pins 4 & 5 closed

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CPU VOLTAGE SELECTION	
Setting	J8
Regular CPU voltage	Pins 4 & 5, 6 & 7, 19 & 20, 21 & 22 closed
VRM module for CPU voltage	Open

DMA CHANNEL SELECTION	
Channel	JR2
1	Pins 1 & 2, 4 & 5 closed
3	Pins 2 & 3, 5 & 6 closed