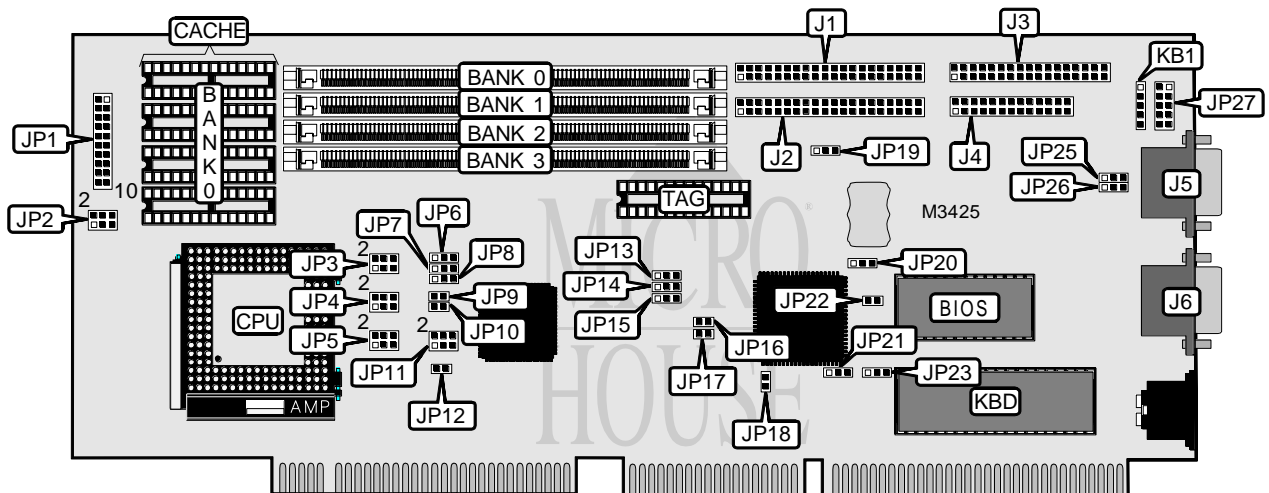


TMC RESEARCH CORPORATION

CAT48AF, CAP48AF

Processor	804846SX/SL80486SX/SL80486SX2/80486DX/SL80486DX/ TI486DX2/AM486/AM486+/80486DX2/SL80486DX2/TI486DX4/ 80486DX4/P24D/P24T/CX5X86
Processor Speed	25/33/40/50(internal)/50/66(internal)/75(internal)/80(internal)/ 100(internal)/120(internal)MHz
Chip Set	ALI
Video Chip Set	None
Maximum Onboard Memory	128MB
Maximum Video Memory	None
Cache	128/256/512KB
BIOS	Unidentified
Dimensions	Unidentified
I/O Options	Floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
IDE interface 1	J1	Turbo switch	JP1 pins 7 & 17
IDE interface 2	J2	Turbo LED	JP1 pins 8 & 18
Floppy drive interface	J3	Reset switch	JP1 pins 9 & 19
Parallel port	J4	IDE interface LED	JP1 pins 10 & 20
Serial port 1	J5	Power LED & keylock	JP1 pins 11 - 15
Serial port 2	J6	PS/2 mouse interface	JP27
Speaker	JP1 pins 1 - 4	Auxiliary keyboard connector	KB1

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TMC RESEARCH CORPORATION

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	JP19	Pins 1 & 2 closed
CMOS memory clear	JP19	Pins 2 & 3 closed
í System reset when watch dog timer times out	JP20	Pins 1 & 2 closed
Activate NMI to CPU when watch dog timer times out	JP20	Pins 2 & 3 closed
Watch dog timer disabled	JP20	Open
í Watch dog timer time out select 1 second	JP22	Open
Watch dog timer time out select 500ms	JP22	Closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 256K x 36	(1) 256K x 36	None	None
2MB	(1) 512K x 36	None	None	None
4MB	(1) 1M x 36	None	None	None
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
4MB	(1) 512K x 36	None	(1) 256K x 36	(1) 256K x 36
4MB	(1) 512K x 36	None	(1) 512K x 36	None
5MB	(1) 256K x 36	(1) 1M x 36	None	None
6MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	None
6MB	(1) 512K x 36	None	(1) 1M x 36	None
8MB	(1) 1M x 36	(1) 1M x 36	None	None
8MB	(1) 2M x 36	None	None	None
10MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36
10MB	(1) 512K x 36	None	(1) 1M x 36	(1) 1M x 36
10MB	(1) 512K x 36	None	(1) 2M x 36	None
12MB	(1) 1M x 36	None	(1) 2M x 36	None
16MB	(1) 4M x 36	None	None	None
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	None	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	None	(1) 2M x 36	None
17MB	(1) 256K x 36	(1) 4M x 36	None	None
20MB	(1) 1M x 36	(1) 4M x 36	None	None
32MB	(1) 4M x 36	(1) 4M x 36	None	None
32MB	(1) 8M x 36	None	None	None
64MB	(1) 16M x 36	None	None	None
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	None	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	None	(1) 8M x 36	None
128MB	(1) 16M x 36	(1) 16M x 36	None	None
128MB	(1) 32M x 36	None	None	None

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TMC RESEARCH CORPORATION

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CACHE CONFIGURATION		
Size	Bank 0	TAG
128KB	(4) 32K x 8	(1) 8K x 8
256KB	(4) 64K x 8	(1) 16K x 8
512KB	(4) 128K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION		
Size	JP13	JP14
128KB	Pins 2 & 3 closed	Pins 2 & 3 closed
256KB	Pins 2 & 3 closed	Pins 1 & 2 closed
512KB	Pins 1 & 2 closed	Pins 1 & 2 closed

CPU SPEED SELECTION				
Speed	JP15	JP16	JP17	JP18
25MHz	Pins 1 & 2 closed	Open	Open	Open
33MHz	Pins 1 & 2 closed	Closed	Closed	Open
40MHz	Pins 1 & 2 closed	Closed	Open	Open
50iMHz	Pins 1 & 2 closed	Open	Open	Open
50MHz	Pins 2 & 3 closed	Open	Closed	Closed
66iMHz	Pins 1 & 2 closed	Closed	Closed	Open
75iMHz	Pins 1 & 2 closed	Open	Open	Open
80iMHz	Pins 1 & 2 closed	Closed	Open	Open
100iMHz	Pins 1 & 2 closed	Closed	Closed	Open
120iMHz	Pins 1 & 2 closed	Closed	Open	Open

CPU VOLTAGE SELECTION	
Voltage	JP2
3.45v	Pins 1 & 2 closed
4v	Pins 3 & 4 closed
5v	Pins 5 & 6 closed

DMA CHANNEL SELECTION		
Channel	JP21	JP23
1	Pins 1 & 2 closed	Pins 1 & 2 closed
3	Pins 2 & 3 closed	Pins 2 & 3 closed

SERIAL PORT 2 SELECTION		
Setting	JP25	JP26
Used as COM2	Pins 1 & 2 closed	Pins 1 & 2 closed
Used as IR connector	Pins 2 & 3 closed	Pins 2 & 3 closed

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CPU TYPE SELECTION						
Type	JP3	JP4	JP5	JP6	JP7	JP8
80486SX	Open	1 & 2	Open	1 & 2	1 & 2	Open
SL80486SX	3 & 4	3 & 4	3 & 4	1 & 2	1 & 2	Open
SL80486SX2	3 & 4	3 & 4	3 & 4	1 & 2	1 & 2	Open
80486DX	Open	1 & 2	Open	2 & 3	1 & 2	2 & 3
SL80486DX	3 & 4	3 & 4	3 & 4	2 & 3	1 & 2	2 & 3
CX486M7	5 & 6	5 & 6	5 & 6	2 & 3	1 & 2	2 & 3
TI486DX2	5 & 6	5 & 6	5 & 6	2 & 3	1 & 2	2 & 3
AM486-66/80	Open	1 & 2	Open	2 & 3	1 & 2	2 & 3
AM486-100	Open	1 & 2	Open	2 & 3	1 & 2	2 & 3
AM486+/66/80	3 & 4	3 & 4	3 & 4	2 & 3	2 & 3	2 & 3
AM486+/100/120	3 & 4	3 & 4	3 & 4	2 & 3	2 & 3	2 & 3
80486DX2	Open	1 & 2	Open	2 & 3	1 & 2	2 & 3
SL80486DX2	3 & 4	3 & 4	3 & 4	2 & 3	1 & 2	2 & 3
TI486DX4	5 & 6	5 & 6	5 & 6	2 & 3	1 & 2	2 & 3
80486DX4	3 & 4	3 & 4	3 & 4	2 & 3	1 & 2	2 & 3
P24D	3 & 4	3 & 4	3 & 4	2 & 3	2 & 3	2 & 3
P24T	3 & 4	3 & 4	3 & 4	2 & 3	2 & 3	1 & 2
CX5X86	3 & 4	3 & 4	3 & 4	2 & 3	1 & 2	2 & 3
AM5X86	3 & 4	3 & 4	3 & 4	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION				
Type	JP9	JP10	JP11	JP12
80486SX	Open	Open	Open	Closed
SL80486SX	Closed	Open	Open	Closed
SL80486SX2	Closed	Open	Open	Closed
80486DX	Open	Open	Open	Closed
SL80486DX	Closed	Open	Open	Closed
CX486M7	Closed	Open	1 & 2	Open
TI486DX2	Closed	Open	1 & 2	Open
AM486-66/80	Open	Closed	Open	Closed
AM486-100	Open	Open	Open	Closed
AM486+/66/80	Closed	Open	1 & 3, 5 & 6	Closed
AM486+/100/120	Closed	Open	5 & 6	Closed
80486DX2	Open	Open	Open	Closed
SL80486DX2	Closed	Open	Open	Closed
TI486DX4	Closed	Open	1 & 2	Open
80486DX4	Closed	Open	Open	Closed
P24D	Closed	Open	5 & 6	Closed
P24T	Closed	Open	Open	Closed
CX5X86	Closed	Open	5 & 6	Closed
AM5X86	Closed	Open	1 & 3, 5 & 6	Closed

Note: Pins designated should be in the closed position.