CX M1/AM K5/Pentium **Processor Processor Speed** 75/90/100/120/133MHz

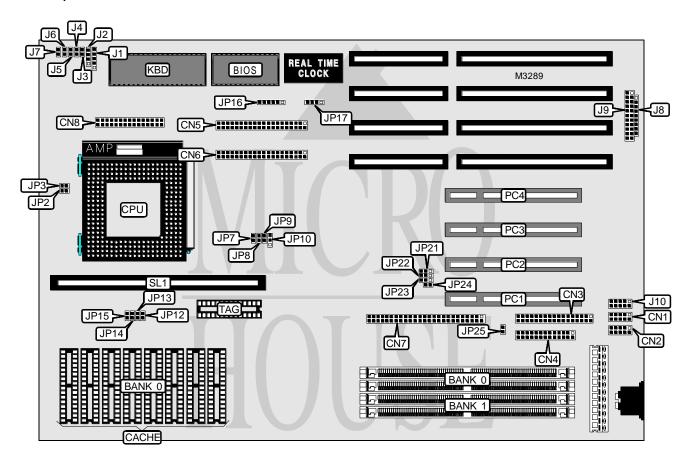
Chip Set Intel **Maximum Onboard Memory** 128MB Cache 256/512KB **BIOS Award**

Dimensions 330mm x 218mm

I/O Options 32-bit PCI slots (4), floppy drive interface, IDE interfaces (2), SCSI interface, parallel

port, serial ports (2), VRM connector, cache slot

NPU Options None



CONNECTIONS				
Function	Label	Function	Label	
Serial port 1	CN1	Speaker	J2	
Serial port 2	CN2	Turbo LED	J3	
Floppy drive interface	CN3	Turbo switch	J4	
Parallel port	CN4	IDE interface LED	J5	
IDE interface	CN5	SCSI interface LED	J6	
IDE interface	CN6	Reset switch	J7	
SCSI interface	CN7	External battery	JP17	
VRM connector	CN8	32-bit PCI slots	PCI - PC4	
Power LED & keylock	J1	Cache slot	SL1	



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USER CONFIGURABLE SETTINGS				
Setting	Label	Position		
í Jumper information unavailable	J8	N/A		
í Jumper information unavailable	J9	N/A		
í Jumper information unavailable	J10	N/A		
í Factory configured - do not alter	JP16	Pins 2 & 3, 5 & 6 closed		
í CMOS memory normal operation	JP17	Pins 1 & 2 closed		
CMOS memory clear	JP17	Pins 2 & 3 closed		
Battery type select external	JP17	Closed		
í SCSI terminator power source select system	JP25	Closed		
SCSI terminator power source select peripheral	JP25	Open		

	DRAM	
Size	Bank 0	Bank 1
4MB	(2) 512K x 32	None
8MB	(2) 1M x 32	None
8MB	(2) 512K x 32	(2) 512K x 32
12MB	(2) 1M x 32	(2) 512K x 32
12MB	(2) 512K x 32	(2) 1M x 32
16MB	(2) 2M x 32	None
16MB	(2) 1M x 32	(2) 1M x 32
20MB	(2) 2M x 32	(2) 512K x 32
20MB	(2) 512K x 32	(2) 2M x 32
24MB	(2) 2M x 32	(2) 1M x 32
24MB	(2) 1M x 32	(2) 2M x 32
32MB	(2) 4M x 32	None
32MB	(2) 2M x 32	(2) 2M x 32
36MB	(2) 4M x 32	(2) 512K x 32
36MB	(2) 512K x 32	(2) 4M x 32
40MB	(2) 4M x 32	(2) 1M x 32
40MB	(2) 1M x 32	(2) 4M x 32
48MB	(2) 4M x 32	(2) 2M x 32
48MB	(2) 2M x 32	(2) 4M x 32
64MB	(2) 8M x 32	None
64MB	(2) 4M x 32	(2) 4M x 32
68MB	(2) 8M x 32	(2) 512K x 32
68MB	(2) 512K x 32	(2) 8M x 32
72MB	(2) 8M x 32	(2) 1M x 32
72MB	(2) 1M x 32	(2) 8M x 32
80MB	(2) 8M x 32	(2) 2M x 32
80MB	(2) 2M x 32	(2) 8M x 32
96MB	(2) 8M x 32	(2) 4M x 32
96MB	(2) 4M x 32	(2) 8M x 32
128MB	(2) 8M x 32	(2) 8M x 32
128MB	(2) 16M x 32	None

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CACHE SIZE					
Size	Bank 0	TAG	SL1		
256KB (A)	(8) 32K x 8	(1) 8K x 8	Not installed		
256KB (B)	None	None	Installed		
512KB (A)	(8) 64K x 8	(1) 16K x 8	Not installed		
512KB (B)	None	None	Installed		

		CACHE JUMPER		
Size	JP12	JP13	JP14	JP15
256KB (A)	Closed	Closed	Open	Open
512KB (A)	Closed	Open	Closed	Closed

CPU SPEED (MX8325)					
CPU speed	Clock speed	JP7	JP8	JP9	JP10
75MHz	50MHz	Open	Closed	Closed	1 & 2
90MHz	60MHz	Closed	Open	Closed	2 & 3
100MHz	50MHz	Open	Closed	Closed	1 & 2
100MHz	66MHz	Closed	Closed	Closed	2 & 3
120MHz	60MHz	Closed	Open	Closed	2 & 3
133MHz	66MHz	Closed	Closed	Closed	2 & 3
Note: Pins design	ated should be in t	the closed position.			

CPU SPEED (IMI464)					
CPU speed	Clock speed	JP7	JP8	JP9	JP10
75MHz	50MHz	Open	Open	Closed	1 & 2
90MHz	60MHz	Closed	Open	Closed	2 & 3
100MHz	50MHz	Open	Open	Closed	1 & 2
100MHz	66MHz	Closed	Closed	Closed	2 & 3
120MHz	60MHz	Closed	Closed Open		2 & 3
133MHz	66MHz	Closed	Closed	Closed	2 & 3
Note: Pins designa	ated should be in	the closed position.			

CPU SPEED (IMI484)					
CPU speed	Clock speed	JP7	JP8	JP9	JP10
75MHz	50MHz	Closed	Open	Open	1 & 2
90MHz	60MHz	Closed	Open	Closed	2 & 3
100MHz	50MHz	Closed	Open	Open	1 & 2
100MHz	66MHz	Closed	Closed	Closed	2 & 3
120MHz	60MHz	Closed	Open	Closed	2 & 3
133MHz	66MHz	Closed	Closed	Closed	2 & 3
Note: Pins design	Note: Pins designated should be in the closed position.				

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CPU MULTIPLIER				
Setting JP2 JP3				
1.5x	Open	Open		
2x	Closed	Open		
3x	Open	Closed		

		ON BOARD SCSI		
Setting	JP21	JP22	JP23	JP24
Enabled	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Open
Disabled	Pins 1 & 2 closed	Pins 1 & 2 closed	Open	Closed