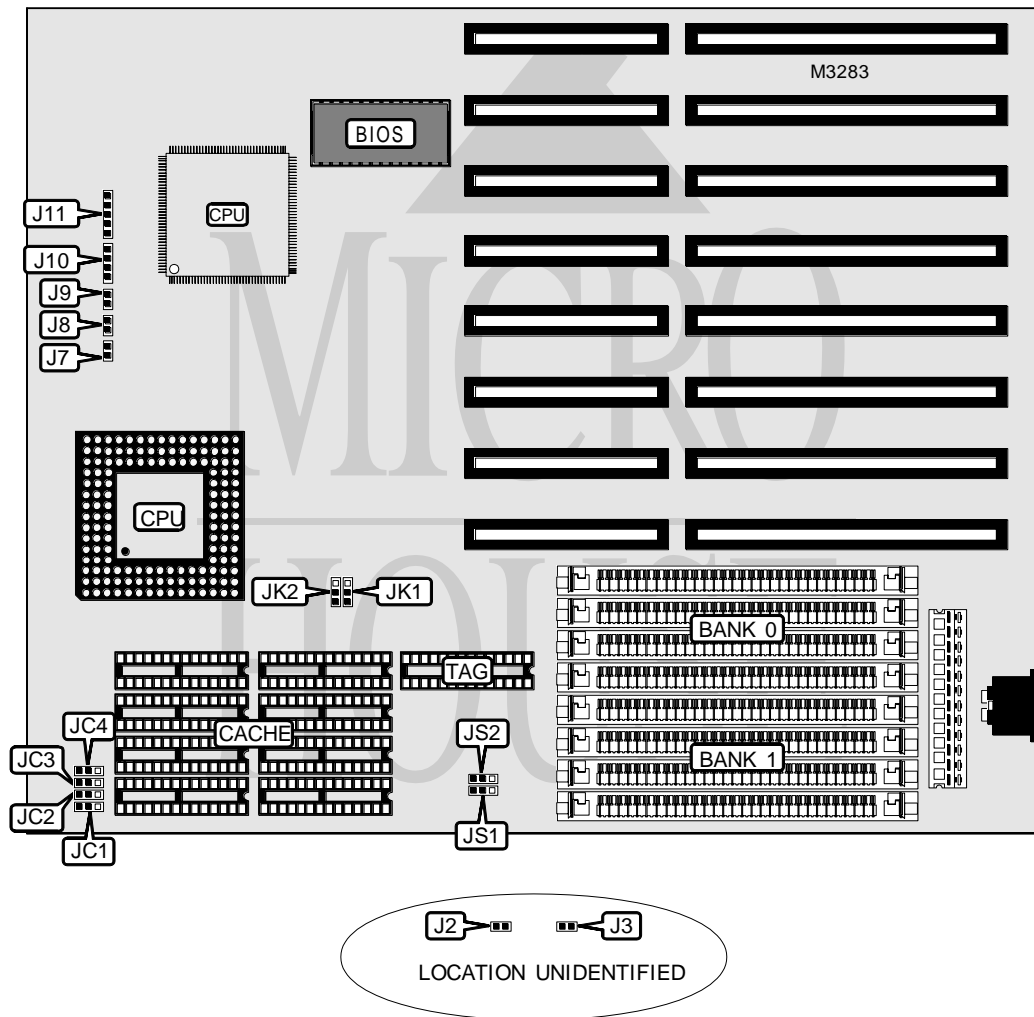


OLIVETTI

PCS30/PCS40 (486-VC)

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	25/33/50(internal)/66(internal)MHz
Chip Set	Unidentified
Maximum Onboard Memory	32MB
Cache	64/128/256KB
BIOS	Unidentified
Dimensions	254mm x 218mm
I/O Options	None
NPU Options	None



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OLIVETTI

PCS30/PCS40 (486-VC)

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CONNECTIONS			
Function	Label	Function	Label
Turbo LED	J7	Speaker	J10
Turbo switch	J8	Power LED & keylock	J11
Reset switch	J9		

USER CONFIGURABLE SETTINGS		
Setting	Label	Position
í CMOS memory normal operation	J2	Open
CMOS memory clear	J2	Closed
í Monitor type select monochrome/EGA/VGA	J3	Open
Monitor type select color	J3	Closed

DRAM		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	None
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE SIZE			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	None	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER		
Size	JS1	JS2
64KB	Pins 1 & 2 closed	Pins 1 & 2 closed
128KB	Pins 1 & 2 closed	Pins 2 & 3 closed
256KB	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU TYPE				
Setting	JC1	JC2	JC3	JC4
80486SX	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Open
80487SX	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
80486DX	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
80486DX2	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed

CPU SPEED		
Setting	JK1	JK2
25MHz	Pins 1 & 2 closed	Pins 1 & 2 closed
33MHz	Pins 2 & 3 closed	Pins 2 & 3 closed
50iMHz	Pins 1 & 2 closed	Pins 1 & 2 closed
66iMHz	Pins 2 & 3 closed	Pins 2 & 3 closed

