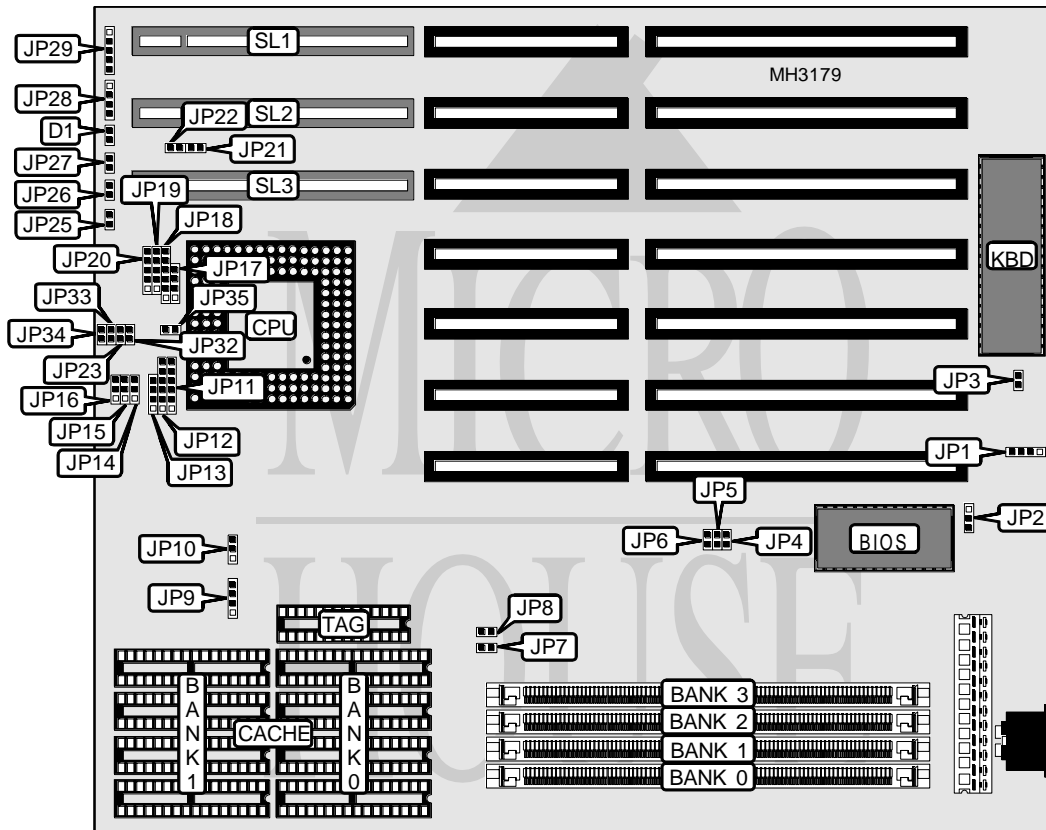


# AQUARIUS SYSTEMS, INC.

## MB-4DUVC MODEL 2

<b>Processor</b>	CX486S/80486SX/SL80486SX/80486SX2/SL80486SX2/CX486DX/AM486DX/ 80486DX/SL80486DX/UMCU5S/CX486DX2/AM486DX2/(SL)AM486DX2/ (SL)AM486DXL2/SL80486DX2/P24D/AM486DX4/(SL)AM486DXL4/80486DX4/ Pentium Overdrive/CXM1
<b>Processor Speed</b>	25/33/40/50(internal)/50/66(internal)/75(internal)100(internal)MHz
<b>Chip Set</b>	UMC
<b>Max. Onboard DRAM</b>	64MB
<b>Cache</b>	64/128/256KB
<b>BIOS</b>	Award
<b>Dimensions</b>	250mm x 220mm
<b>I/O Options</b>	32-bit VESA local bus slots (3), green PC connector
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	D1	Turbo switch	JP27
External battery	JP1	Speaker	JP28
Green PC connector	JP25	Power LED & keylock	JP29
Reset switch	JP26	32-bit VESA local bus slots	SL1 - SL3

Continued on next page. . .

# AQUARIUS SYSTEMS, INC.

## MB-4DUVC MODEL 2

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
Battery type select internal	JP1	pins 2 & 3 closed
Battery type select external	JP1	Closed
CMOS memory clear	JP1	pins 3 & 4 closed
Flash BIOS voltage select 5v	JP2	pins 1 & 2 closed
Flash BIOS voltage select 12v	JP2	pins 2 & 3 closed
Keyboard controller IC U2 not installed	JP3	Closed
Keyboard controller IC U2 installed	JP3	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE
2MB	(1) 512K x 36	NONE	NONE	NONE
3MB	(1) 512K x 36	(1) 256K x 36	NONE	NONE
4MB	(1) 1M x 36	NONE	NONE	NONE
4MB	(1) 512K x 36	(1) 512K x 36	NONE	NONE
4MB	(1) 512K x 36	(1) 256K x 36	NONE	(1) 256K x 36
5MB	(1) 1M x 36	(1) 256K x 36	NONE	NONE
6MB	(1) 512K x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 2M x 36	NONE	NONE	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
9MB	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36	NONE
10MB	(1) 1M x 36	(1) 512K x 36	(1) 1M x 36	NONE
10MB	(1) 2M x 36	(1) 512K x 36	NONE	NONE
10MB	(1) 256K x 36	(1) 2M x 36	(1) 256K x 36	NONE
12MB	(1) 2M x 36	(1) 1M x 36	NONE	NONE
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE
16MB	(1) 2M x 36	(1) 2M x 36	NONE	NONE
16MB	(1) 2M x 36	(1) 1M x 36	NONE	(1) 1M x 36
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
17MB	(1) 256K x 36	NONE	(1) 4M x 36	NONE
18MB	(1) 256K x 36	(1) 4M x 36	(1) 256K x 36	NONE
18MB	(1) 512K x 36	(1) 4M x 36	NONE	NONE
19MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 4M x 36
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
22MB	(1) 512K x 36	(1) 4M x 36	NONE	(1) 1M x 36
24MB	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36	NONE
24MB	(1) 2M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 8M x 36	NONE	NONE	NONE
64MB	(1) 8M x 36	(1) 8M x 36	NONE	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36

Continued on next page...

# AQUARIUS SYSTEMS, INC.

## MB-4DUVC MODEL 2

... continued from previous page

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB (A)	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
256KB (B)	(4) 64K x 8	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP7	JP8	JP9	JP10
64KB	Open	Open	Open	2 & 3
128KB	Open	Closed	1 & 2	1 & 2
256KB (A)	Closed	Closed	2 & 3	2 & 3
256KB (B)	Closed	Closed	1 & 2, 3 & 4	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION			
Type	JP11	JP12	JP13
CX486S	1 & 2, 3 & 4	1 & 2, 3 & 4, 5 & 6	2 & 3
80486SX	Open	2 & 3	2 & 3
SL80486SX	1 & 2	1 & 2	2 & 3
80486SX2	Open	2 & 3	2 & 3
SL80486SX2	1 & 2	1 & 2	2 & 3
CX486DX	1 & 2, 3 & 4	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4
AM486DX	Open	2 & 3	1 & 2, 3 & 4
80486DX	Open	2 & 3	1 & 2, 3 & 4
SL80486DX	1 & 2	1 & 2	1 & 2, 3 & 4
UMC U55	2 & 3	2 & 3	2 & 3
CX486DX2	1 & 2, 3 & 4	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4
AM486DX2	Open	2 & 3	1 & 2, 3 & 4
(SL)AM486DX2	Open	2 & 3	1 & 2, 3 & 4
(SL)AM486DXL2	2 & 3	2 & 3	1 & 2, 3 & 4
SL80486DX2	1 & 2	1 & 2	1 & 2, 3 & 4
P24D	1 & 2, 4 & 5	1 & 2, 4 & 5	1 & 2, 3 & 4
AM486DX4 (2x)	Open	2 & 3	1 & 2, 3 & 4
AM486DX4 (3x)	Open	2 & 3	1 & 2, 3 & 4
(SL)AM486DXL4 (2x)	2 & 3	2 & 3	1 & 2, 3 & 4
(SL)AM486DXL4 (3x)	2 & 3	2 & 3	1 & 2, 3 & 4
80486DX4	1 & 2	1 & 2	1 & 2, 3 & 4
Pentium Overdrive	1 & 2	1 & 2	1 & 2, 3 & 4
CX M1	1 & 2, 4 & 5	1 & 2, 3 & 4	1 & 2, 3 & 4

Note: Pins designated should be in the closed position.

Continued on next page...

# AQUARIUS SYSTEMS, INC.

## MB-4DUVC MODEL 2

... continued from previous page

CPU TYPE CONFIGURATION (CON'T)				
Type	JP17	JP18	JP19	JP35
CX486S	Open	2 & 3, 4 & 5	2 & 3, 4 & 5	Open
80486SX	Open	Open	Open	Open
SL80486SX	Open	5 & 6	1 & 2, 3 & 4	Open
80486SX2	Open	Open	Open	Open
SL80486SX2	Open	5 & 6	1 & 2, 3 & 4	Open
CX486DX	1 & 2	2 & 3, 4 & 5	2 & 3	Open
AM486DX	1 & 2	Open	Open	Open
80486DX	1 & 2	Open	Open	Open
SL80486DX	1 & 2	5 & 6	1 & 2, 3 & 4	Open
UMC U5S	3 & 4	1 & 2	Open	Open
CX486DX2	1 & 2	2 & 3, 4 & 5	2 & 3	Open
AM486DX2	1 & 2	Open	Open	Open
(SL)AM486DX2	1 & 2	Open	Open	Closed
(SL)AM486DXL2	1 & 2, 3 & 4	1 & 2	Open	Open
SL80486DX2	1 & 2	5 & 6	1 & 2, 3 & 4	Open
P24D	1 & 2	3 & 4, 5 & 6	1 & 2, 3 & 4	Open
AM486DX4 (2x)	1 & 2	Open	Open	Closed
AM486DX4 (3x)	1 & 2	Open	Open	Open
(SL)AM486DXL4 (2x)	1 & 2, 3 & 4	1 & 2	Open	Closed
(SL)AM486DXL4 (3x)	1 & 2, 3 & 4	1 & 2	Open	Open
80486DX4	1 & 2	5 & 6	1 & 2, 3 & 4	Open
Pentium Overdrive	2 & 3	5 & 6	1 & 2, 3 & 4	Open
CX M1	1 & 2	3 & 4, 5 & 6	1 & 2, 3 & 4	Open

Note: Pins designated should be in the closed position.

CPU SPEED CONFIGURATION			
Speed	JP4	JP5	JP6
25MHz	Open	Open	Closed
33MHz	Closed	Closed	Closed
40MHz	Open	Closed	Closed
50iMHz	Open	Open	Closed
50MHz	Closed	Open	Open
66iMHz	Closed	Closed	Closed
75iMHz	Open	Open	Closed
100iMHz	Closed	Closed	Closed

CPU SPEED CONFIGURATION (CX M1 & DX4 ONLY)	
Speed	JP20
CX M1	pins 4 & 5 closed
Intel CPU 2x	pins 2 & 3 closed
CX CPU 2x	pins 2 & 3, 4 & 5 closed
Intel CPU 2.5x	pins 1 & 2 closed
Intel CPU 3x	Open

Continued on next page. . .

# AQUARIUS SYSTEMS, INC.

## MB-4DUVC MODEL 2

... continued from previous page

CPU VOLTAGE CONFIGURATION							
Voltage	JP14	JP15	JP16	JP23	JP32	JP33	JP34
3.3v	1 & 2	1 & 2	1 & 2	Open	Closed	Open	Open
3.45v	1 & 2	1 & 2	1 & 2	Closed	Open	Open	Open
3.6v	1 & 2	1 & 2	1 & 2	Open	Open	Closed	Open
4v	1 & 2	1 & 2	1 & 2	Open	Open	Open	Closed
5v	2 & 3	2 & 3	2 & 3	Open	Open	Open	Open

Note: Pins designated should be in the closed position.

VL BUS WAIT STATE CONFIGURATION	
Wait states	JP22
0 wait states	Open
1 wait state	Closed

VL BUS SPEED CONFIGURATION	
CPU speed	JP21
<= 33MHz	Open
> 33MHz	Closed