## UNIDENTIFIED 386 CACHE 33MHZ

80386SX (exact location unidentified) **Processor** 

**Processor Speed** 25/33MHz Chip Set Unidentified

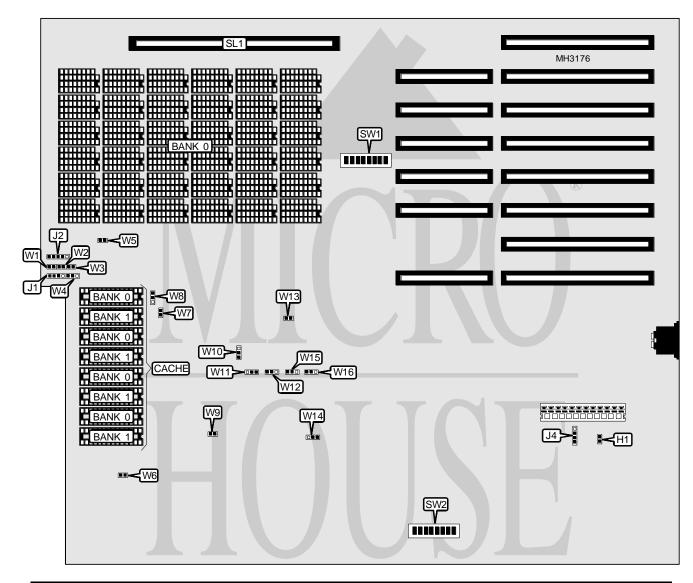
Max. Onboard DRAM 16MB (12MB on external memory card)

Cache 32/64KB **BIOS** Phoenix

**Dimensions** 355mm x 305mm

I/O Options 32-bit external memory card

**NPU Options** 80387SX/3167(exact location unidentified)



CONNECTIONS				
Purpose Location Purpose Location				
Speaker	J1	External memory card	SL1	
Power LED & keylock	J2	Reset switch	W3	
External battery	J4	Cache/turbo LED	W5	

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USER CONFIGURABLE SETTINGS			
Function	Jumper/Switch	Position	
Battery type select internal	H1	Closed	
Battery type select external	H1	Open	
í Factory configured - do not alter	SW2/1	On	
NPU disabled	SW2/2	Off	
NPU enabled	SW2/2	On	
í Factory configured - do not alter	SW2/3	Off	
í EGA BIOS relocated	SW2/4	On	
EGA BIOS not relocated	SW2/4	Off	
í Factory configured - do not alter	SW2/5	Off	
Monitor type select CGA/EGA/VGA	SW2/6	On	
Monitor type select MGA	SW2/6	Off	
Slot speed select 12.5MHz	SW2/8	On	
Slot speed select 8.3MHz	SW2/8	Off	
í Factory configured - do not alter	W1	Open	
í Factory configured - do not alter	W2	Open	
í Factory configured - do not alter	W6	Open	
í Factory configured - do not alter	W9	Open	
í Factory configured - do not alter	W10	pins 2 & 3 closed	
í Factory configured - do not alter	W12	N/A	
í Factory configured - do not alter	W14	N/A	
í Factory configured - do not alter	W15	N/A	
í Factory configured - do not alter	W16	N/A	

	DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3	
1MB	(36) 41256	NONE	NONE	NONE	
2MB	(36) 41256	(36) 41256	NONE	NONE	
4MB	(36) 411000	NONE	NONE	NONE	
8MB	(36) 411000	(36) 411000	NONE	NONE	
16MB	(36) 411000	(36) 411000	(4) 1M x 9	(4) 1M x 9	
Note: Banks 1, 2, & 3 are located on the external memory card.					

DRAM SWITCH CONFIGURATION							
Size	SW1/1	SW1/2	SW1/3	SW1/4	SW1/6	SW1/7	SW1/8
1MB	On	Off	On	On	Off	Off	Off
2MB	Off	On	On	On	Off	Off	Off
4MB	On	On	Off	Off	Off	Off	Off
8MB	Off	On	Off	Off	Off	Off	Off
16MB	Off						

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DRAM SPEED CONFIGURATION				
CPU speed	DRAM speed	SW1/5		
25MHz	80ns	Off		
25MHz	100ns	Off		
25MHz	120ns	On		
33MHz	60ns	Off		
33MHz	80ns	On		

	CACHE CONFIGURATION	
Size	Bank 0	Bank 1
32KB	(4) 8K x 8	NONE
64KB	(4) 8K x 8	(4) 8K x 8

CACHE JUMPER CONFIGURATION				
Size	W8	W13		
32KB	pins 1 & 2 closed	pins 1 & 2 closed		
64KB	pins 2 & 3 closed	pins 2 & 3 closed		

CACHE JUMPER/SWITCH CONFIGURATION				
Setting W4 SW2/7				
Enabled	pins 1 & 2 closed	On		
Disabled	pins 2 & 3 closed	Off		

	NPU CONFIGURATION	
Setting	W7	W11
Asynchronous	Closed	pins 1 & 2 closed
Synchronous	Open	pins 2 & 3 closed