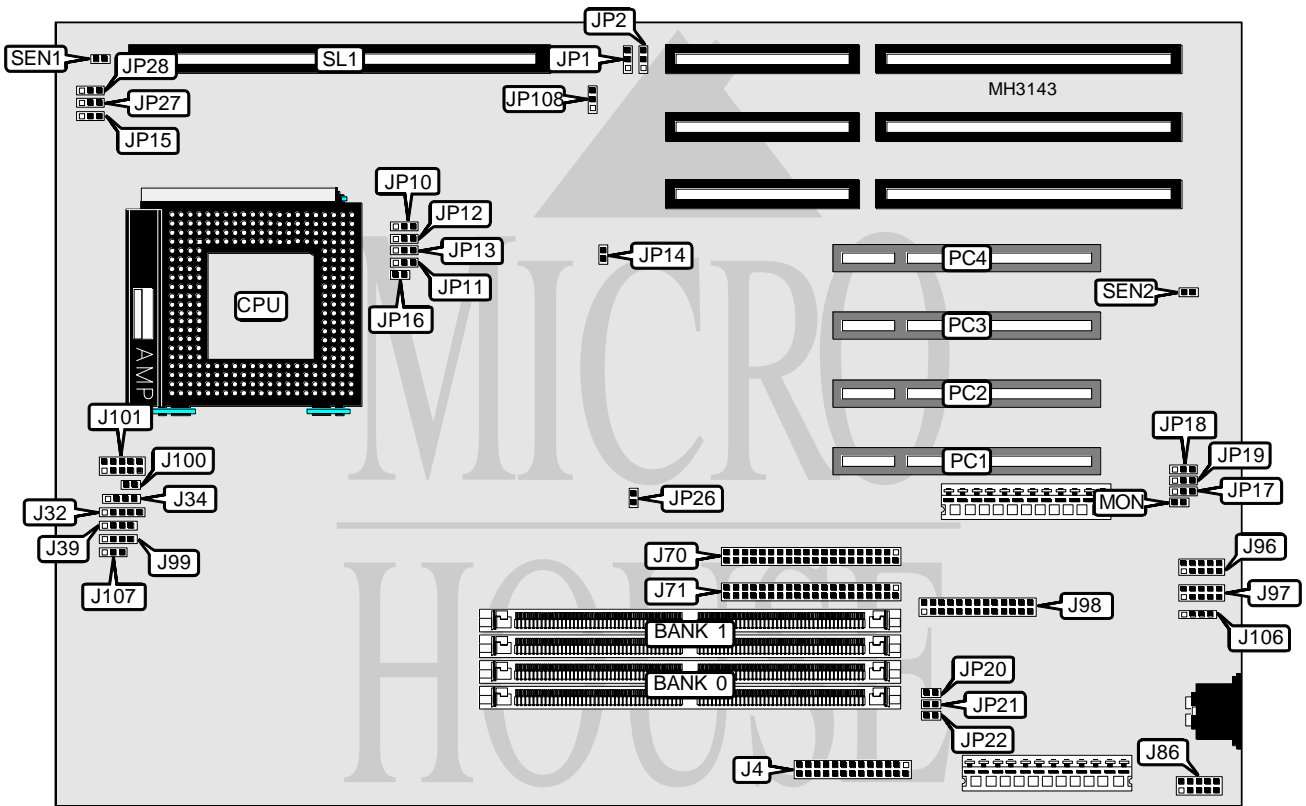


CSS LABORATORIES, INC.
 PREFERRED TR SYSTEMS, MAXPRO MT TR SYSTEMS,
 PRORACK TR SYSTEMS, MB-5864

Processor	Pentium
Processor Speed	75/90/100/120/133/150/166MHz
Chip Set	Unidentified
Max. Onboard DRAM	128MB
Cache	256/512KB
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), cache slot
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Floppy drive interface	J4	Turbo LED	J99 pins 3 & 4
Power LED & keylock	J32	IDE interface LED	J100
PCI IDE interface LED	J34	Front panel connector	J101
Speaker	J39	External battery	J106
IDE interface 1	J70	IR connector	J107
IDE interface 2	J71	Green PC connector (monitor)	MON
PS/2 mouse interface	J86	32-bit PCI slots	PC1 - PC4
Serial port 2	J96	Internal temperature sensor	SEN1
Serial port 1	J97	Internal temperature sensor	SEN2
Parallel port	J98	Cache slot	SL1
Reset switch	J99 pins 1 & 2		

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Pipeline enabled	JP16	Closed
Pipeline disabled	JP16	Open
í CMOS memory normal operation	JP20	Open
CMOS memory clear	JP20	Closed
í Password normal operation	JP21	Open
Password clear	JP21	Closed
í Factory configured - do not alter	JP22	N/A
í Factory configured - do not alter	JP27	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 32	NONE
8MB	(2) 512K x 32	(2) 512K x 32
10MB	(2) 1M x 32	(2) 256K x 32
10MB	(2) 256K x 32	(2) 1M x 32
12MB	(2) 1M x 32	(2) 512K x 32
12MB	(2) 512K x 32	(2) 1M x 32
16MB	(2) 2M x 32	NONE
16MB	(2) 1M x 32	(2) 1M x 32
18MB	(2) 2M x 32	(2) 256K x 32
18MB	(2) 256K x 32	(2) 2M x 32
20MB	(2) 2M x 32	(2) 512K x 32

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
20MB	(2) 512K x 32	(2) 2M x 32
24MB	(2) 2M x 32	(2) 1M x 32
24MB	(2) 1M x 32	(2) 2M x 32
32MB	(2) 4M x 32	NONE
32MB	(2) 2M x 32	(2) 2M x 32
34MB	(2) 4M x 32	(2) 256K x 32
34MB	(2) 256K x 32	(2) 4M x 32
36MB	(2) 4M x 32	(2) 512K x 32
36MB	(2) 512K x 32	(2) 4M x 32
40MB	(2) 4M x 32	(2) 1M x 32
40MB	(2) 1M x 32	(2) 4M x 32
48MB	(2) 4M x 32	(2) 2M x 32
48MB	(2) 2M x 32	(2) 4M x 32
64MB	(2) 8M x 32	NONE
64MB	(2) 4M x 32	(2) 4M x 32
66MB	(2) 8M x 32	(2) 256K x 32
66MB	(2) 256K x 32	(2) 8M x 32
68MB	(2) 8M x 32	(2) 512K x 32
68MB	(2) 512K x 32	(2) 8M x 32
72MB	(2) 8M x 32	(2) 1M x 32
72MB	(2) 1M x 32	(2) 8M x 32
80MB	(2) 8M x 32	(2) 2M x 32
80MB	(2) 2M x 32	(2) 8M x 32
96MB	(2) 8M x 32	(2) 4M x 32
96MB	(2) 4M x 32	(2) 8M x 32
128MB	(2) 8M x 32	(2) 8M x 32

CACHE CONFIGURATION	
Size	SL1
256KB	Installed
512KB	Installed

CACHE JUMPER CONFIGURATION		
Size	JP12	JP13
None	pins 2 & 3 closed	pins 2 & 3 closed
256KB	pins 2 & 3 closed	pins 1 & 2 closed
512KB	pins 1 & 2 closed	pins 2 & 3 closed

CACHE JUMPER CONFIGURATION		
Type	JP10	JP11
Asynchronous	pins 2 & 3 closed	pins 1 & 2 closed
Burst	pins 1 & 2 closed	pins 2 & 3 closed
Pipeline burst	pins 2 & 3 closed	pins 2 & 3 closed

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CPU SPEED CONFIGURATION					
Speed	JP1	JP2	JP15	JP28	JP108
75MHz	2 & 3	2 & 3	Open	1 & 2	1 & 2
90MHz	1 & 2	2 & 3	Open	1 & 2	2 & 3
100MHz	2 & 3	1 & 2	Open	1 & 2	2 & 3
120MHz	1 & 2	2 & 3	2 & 3	1 & 2	2 & 3
133MHz	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3
150MHz	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3
166MHz	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

BIOS CONFIGURATION			
Setting	JP17	JP18	JP19
EPROM	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
Flash normal	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
Flash programming	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed

FLASH BIOS CONFIGURATION		
Setting	JP14	JP26
Green PC enabled	Open	Open
Green PC disabled	Closed	Closed

Note: JP14 may not be present on all boards.