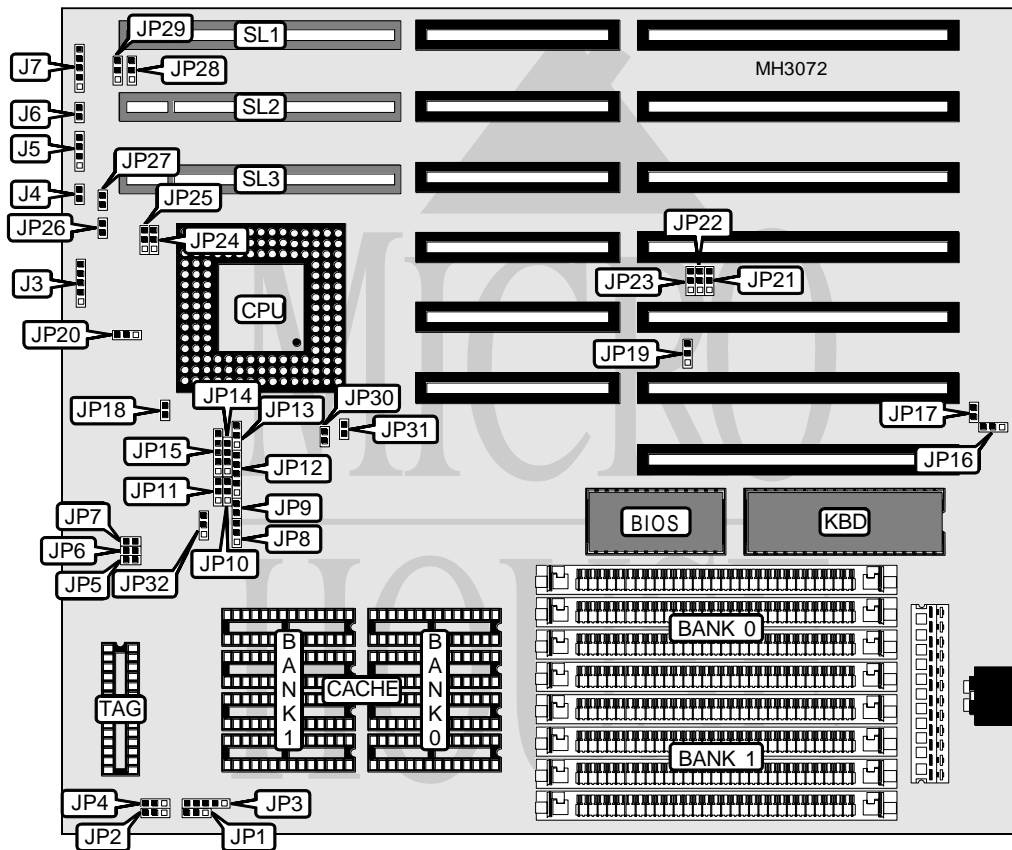


DTK COMPUTER, INC. PKM-0038S (VER. 1.00)

| | |
|--------------------------|--|
| Processor | CX486M6/80486SX/CX486S2/SL80486SX/CX486M7/80486DX/SL80486DX/80486DX2/SL80486DX2/P24D/Pentium Overdrive |
| Processor Speed | 20/25/33/40/50(internal)/50/66(internal)MHz |
| Chip Set | Unidentified |
| Max. Onboard DRAM | 128MB |
| Cache | 128/256/512KB |
| BIOS | Award |
| Dimensions | 250mm x 220mm |
| I/O Options | 32-bit VESA local bus slots (3), green PC connector |
| NPU Options | None |



| CONNECTIONS | | | |
|--------------|---------------|-----------------------------|-----------|
| Purpose | Location | Purpose | Location |
| Turbo switch | J3 pins 3 & 4 | Power LED & keylock | J7 |
| Turbo LED | J4 | Green PC connector | JP26 |
| Speaker | J5 | 32-bit VESA local bus slots | SL1 - SL3 |
| Reset switch | J6 | | |

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| USER CONFIGURABLE SETTINGS | | |
|--|--------|-------------------|
| Function | Jumper | Position |
| í Turbo switch enabled | J3 | pins 3 & 4 closed |
| Turbo switch disabled | J3 | pins 4 & 5 closed |
| í CMOS memory normal operation | JP16 | pins 2 & 3 closed |
| CMOS memory clear | JP16 | pins 1 & 2 closed |
| í Monitor type select monochrome/EGA/VGA | JP17 | Open |
| Monitor type select CGA | JP17 | Closed |
| í CPU type select any CPU installed | JP18 | Open |
| CPU type select CX486S2 only | JP18 | Closed |
| í Factory configured - do not alter | JP19 | pins 1 & 2 closed |
| í CPU clock delay select CLKIN delays CPUCLK | JP20 | pins 2 & 3 closed |
| CPU clock delay select CLKIN = CPUCLK | JP20 | pins 1 & 2 closed |
| í Factory configured - do not alter | JP23 | pins 1 & 2 closed |
| í Factory configured - do not alter | JP27 | Open |
| í Factory configured - do not alter | JP32 | pins 1 & 2 closed |

| DRAM CONFIGURATION | | |
|--------------------|--------------|--------------|
| Size | Bank 0 | Bank 1 |
| 1MB | (4) 256K x 9 | NONE |
| 2MB | (4) 256K x 9 | (4) 256K x 9 |
| 4MB | (4) 1M x 9 | NONE |
| 5MB | (4) 256K x 9 | (4) 1M x 9 |
| 8MB | (4) 1M x 9 | (4) 1M x 9 |
| 16MB | (4) 4M x 9 | NONE |
| 17MB | (4) 256K x 9 | (4) 4M x 9 |
| 20MB | (4) 1M x 9 | (4) 4M x 9 |
| 32MB | (4) 4M x 9 | (4) 4M x 9 |
| 64MB | (4) 16M x 9 | NONE |
| 65MB | (4) 256K x 9 | (4) 16M x 9 |
| 68MB | (4) 1M x 9 | (4) 16M x 9 |
| 80MB | (4) 4M x 9 | (4) 16M x 9 |
| 128MB | (4) 16M x 9 | (4) 16M x 9 |

| CACHE CONFIGURATION | | | |
|---------------------|--------------|-------------|-------------|
| Size | Bank 0 | Bank 1 | TAG |
| 128KB | (4) 32K x 8 | NONE | (1) 8K x 8 |
| 256KB | (4) 32K x 8 | (4) 32K x 8 | (1) 32K x 8 |
| 512KB | (4) 128K x 8 | NONE | (1) 32K x 8 |

| CACHE JUMPER CONFIGURATION | | | | |
|----------------------------|-------|-------|--------------|-------|
| Size | JP1 | JP2 | JP3 | JP4 |
| 128KB | 1 & 2 | 1 & 2 | 1 & 2, 3 & 4 | 1 & 2 |
| 256KB | 2 & 3 | 2 & 3 | 2 & 3, 4 & 5 | 1 & 2 |
| 512KB | 1 & 2 | 2 & 3 | 1 & 2, 3 & 4 | 2 & 3 |

Note: Pins designated should be in the closed position.

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| CPU TYPE CONFIGURATION | | | | | |
|------------------------|-------|--------|-------|-------|--------------|
| Type | JP8 | JP9 | JP10 | JP11 | JP12 |
| CX486M6 | 2 & 3 | Closed | 2 & 3 | Open | 2 & 3, 4 & 5 |
| 80486SX | 2 & 3 | Open | 2 & 3 | Open | Open |
| SL80486SX | 2 & 3 | Open | 2 & 3 | Open | 3 & 4 |
| CX486M7 | 1 & 2 | Closed | 2 & 3 | 2 & 3 | 2 & 3 |
| 80486DX | 1 & 2 | Closed | 2 & 3 | 2 & 3 | Open |
| SL80486DX | 1 & 2 | Closed | 2 & 3 | 2 & 3 | 3 & 4 |
| 80486DX2 | 1 & 2 | Closed | 2 & 3 | 2 & 3 | Open |
| SL80486DX2 | 1 & 2 | Closed | 2 & 3 | 2 & 3 | 3 & 4 |
| P24D | 1 & 2 | Closed | 1 & 2 | 2 & 3 | 1 & 2, 3 & 4 |
| P24T | 1 & 2 | Closed | 1 & 2 | 1 & 2 | 3 & 4 |

Note: Pins designated should be in the closed position.

| CPU TYPE CONFIGURATION (CON'T) | | | | |
|--------------------------------|-------|--------------|-------|-------|
| Type | JP13 | JP14 | JP15 | JP21 |
| CX486M6 | Open | 1 & 2, 3 & 4 | 2 & 3 | 1 & 2 |
| 80486SX | Open | Open | Open | 2 & 3 |
| SL80486SX | 1 & 2 | 2 & 3 | 4 & 5 | 2 & 3 |
| CX486M7 | Open | 1 & 2, 3 & 4 | 2 & 3 | 1 & 2 |
| 80486DX | Open | Open | Open | 2 & 3 |
| SL80486DX | 1 & 2 | 2 & 3 | 4 & 5 | 2 & 3 |
| 80486DX2 | Open | Open | Open | 2 & 3 |
| SL80486DX2 | 1 & 2 | 2 & 3 | 4 & 5 | 2 & 3 |
| P24D | 2 & 3 | 2 & 3 | 4 & 5 | 2 & 3 |
| P24T | Open | 2 & 3 | 1 & 2 | 1 & 2 |

Note: Pins designated should be in the closed position.

| CPU TYPE CONFIGURATION (CON'T) | | | | | |
|--------------------------------|-------|-------|-------|--------|--------|
| Type | JP22 | JP24 | JP25 | JP30 | JP31 |
| CX486M6 | 2 & 3 | 2 & 3 | 2 & 3 | Open | Open |
| 80486SX | 1 & 2 | Open | Open | Open | Open |
| SL80486SX | 1 & 2 | Open | Open | Open | Open |
| CX486M7 | 2 & 3 | 2 & 3 | 2 & 3 | Open | Open |
| 80486DX | 1 & 2 | Open | Open | Open | Open |
| SL80486DX | 1 & 2 | Open | Open | Open | Open |
| 80486DX2 | 1 & 2 | Open | Open | Open | Open |
| SL80486DX2 | 1 & 2 | Open | Open | Open | Open |
| P24D | 1 & 2 | Open | 1 & 2 | Closed | Closed |
| P24T | 1 & 2 | 1 & 2 | 1 & 2 | Open | Open |

Note: Pins designated should be in the closed position.

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| CPU SPEED CONFIGURATION | | | |
|-------------------------|--------|--------|--------|
| Speed | JP5 | JP6 | JP7 |
| 20MHz | Open | Open | Open |
| 25MHz | Open | Closed | Open |
| 33MHz | Closed | Closed | Closed |
| 40MHz | Open | Closed | Closed |
| 50iMHz | Open | Closed | Open |
| 50MHz | Closed | Open | Open |
| 66iMHz | Closed | Closed | Closed |

| VL BUS WAIT STATE CONFIGURATION | |
|---------------------------------|-------------------|
| Wait states | JP28 |
| 0 wait states | pins 1 & 2 closed |
| 1 wait state | pins 2 & 3 closed |

| VL BUS SPEED CONFIGURATION | |
|----------------------------|-------------------|
| CPU speed | JP29 |
| <= 33MHz | pins 1 & 2 closed |
| > 33MHz | pins 2 & 3 closed |