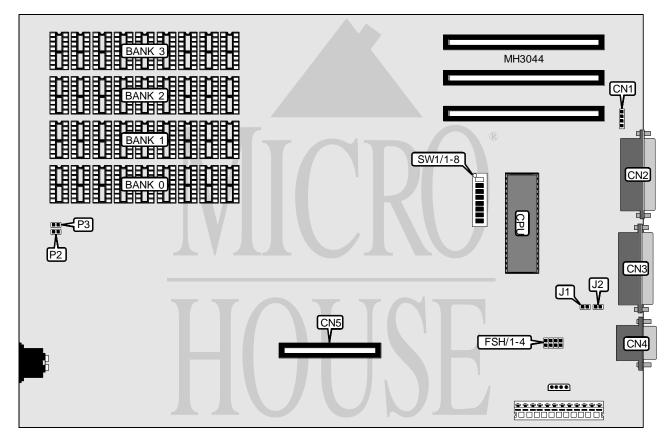
OLIVETTI M 1 9

Processor Processor Speed Chip Set Max. Onboard DRAM Cache BIOS Dimensions I/O Options NPU Options 8088 4.77/8MHz Unidentified 640KB None Unidentified 330mm x 218mm VGA port, IDE interface, Parallel port, Serial port None



CONNECTIONS				
Purpose Location Purpose Locatio				
Speaker	CN1	VGA port	CN4	
Parallel port	CN2	IDE interface connector	CN5	
Serial port	CN3			

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DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
256KB	(9) 41256	NONE	NONE	NONE
640KB	(9) 41256	(9) 41256	(9) 4164	(9) 4164

DRAM JUMPER CONFIGURATION				
Size	P2	P3		
256KB	Closed	Open		
640KB	Closed	Closed		

CPU SPEED CONFIGURATION						
Speed	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
4.77Mhz	Closed	Closed	Closed	Closed	Open	Open
8Mhz	Open	Open	Open	Open	Closed	Closed

CPU SPEED CONFIGURATION (CON'T)						
Speed	SW1/7	SW1/8	FSH1	FSH2	FSH3	FSH4
4.77Mhz	Open	Open	Open	Closed	Open	Closed
8Mhz	Closed	Closed	Closed	Open	Closed	Open

DISPLAY CONFIGURATION				
Туре	J1	J2		
Monochrome	Closed	Open		
Color	Open	Closed		
External switch enabled	Open	Open		