# CHAINTECH COMPUTER COMPANY, LTD
# 4 8 6 S P M

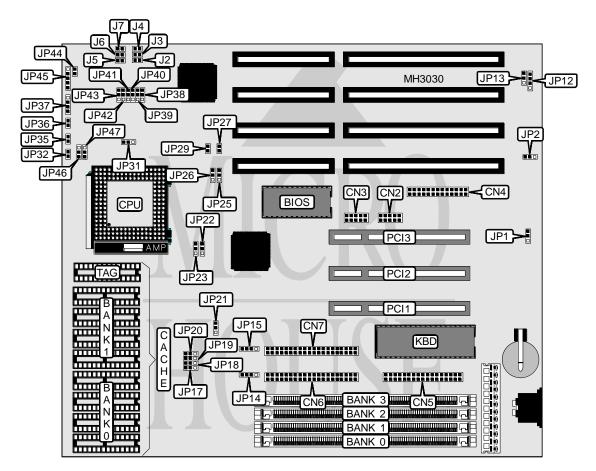| | |
|---|---|
| **Processor** | 80486SX/CX486DX/AM486DX/U5S/80486DX/ODP486SX/CX486DX2/ CX486DX2V/AM486DX2/AM486DX2/80486DX2/CX486DX4/AM486DX4-NV8T/ AM486DX4-SV8B/80486DX4/80486DX4(WB)/Pentium Overdrive/ SL Pentium Overdrive/CXM1SC |
| **Processor Speed** | 25/33/40/50(internal)/50/66(internal)/75(internal)/80(internal)/100(internal)/ 120(internal)MHz |
| **Chip Set** | Unidentified |
| **Max. Onboard DRAM** | 128MB |
| **Cache** | 128/256/512/1024KB |
| **BIOS** | Award |
| **Dimensions** | 220mm x 250mm |
| **I/O Options** | Green PC connector, floppy drive interface, IDE interfaces (2), parallel port, serial ports (2), 32-bit PCI slots (3) |
| **NPU Options** | None |

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| CONNECTIONS | | | |
|---|---|---|---|
| Purpose | Location | Purpose | Location |
| Serial port 1 | CN2 | Green PC LED | JP27 |
| Serial port 2 | CN3 | Green PC connector | JP29 |
| Parallel port | CN4 | Turbo switch | JP35 |
| Floppy drive interface | CN5 | Turbo LED | JP36 |
| IDE interface 1 | CN6 | Speaker | JP37 |
| IDE interface 2 | CN7 | Reset switch | JP44 |
| External battery | JP12 | Power LED & keylock | JP45 |
| IDE interface LED 1 | JP14 | 32-bit PCI slots | PCI1 - PCI3 |
| IDE interface LED 2 | JP15 | | |

| USER CONFIGURABLE SETTINGS | | |
|---|---|---|
| Function | Jumper | Position |
| í  CMOS memory normal operation | JP13 | pins 1 & 2 closed |
| CMOS memory clear | JP13 | pins 2 & 3 closed |
| í  PCI-Bus clock select normal speed | JP21 | pins 1 & 2 closed |
| PCI-Bus clock select ½ speed | JP21 | pins 2 & 3 closed |
| í  Factory configured - do not alter | JP31 | pins 1 & 2 closed |
| í  Turbo switch select high speed | JP35 | Closed |
| Turbo switch select low speed | JP35 | Open |
| í  Factory configured - do not alter | JP39 | pins 1 & 2 closed |
| í  Factory configured - do not alter | JP40 | pins 1 & 2 closed |
| í  Factory configured - do not alter | JP46 | N/A |
| í  Factory configured - do not alter | JP47 | N/A |

| DRAM CONFIGURATION | | | | |
|---|---|---|---|---|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 1MB | (1) 256K x 36 | NONE | NONE | NONE |
| 2MB | (1) 256K x 36 | (1) 256K x 36 | NONE | NONE |
| 2MB | (1) 512K x 36 | NONE | NONE | NONE |
| 4MB | (1) 256K x 36 | (1) 256K x 36 | (1) 512K x 36 | NONE |
| 4MB | (1) 512K x 36 | (1) 512K x 36 | NONE | NONE |
| 4MB | (1) 1M x 36 | NONE | NONE | NONE |
| 5MB | (1) 256K x 36 | (1) 1M x 36 | NONE | NONE |
| 6MB | (1) 256K x 36 | (1) 256K x 36 | (1) 1M x 36 | NONE |
| 6MB | (1) 512K x 36 | (1) 1M x 36 | NONE | NONE |
| 8MB | (1) 256K x 36 | (1) 256K x 36 | (1) 512K x 36 | (1) 1M x 36 |
| 8MB | (1) 512K x 36 | (1) 512K x 36 | (1) 1M x 36 | NONE |
| 8MB | (1) 1M x 36 | (1) 1M x 36 | NONE | NONE |
| 8MB | (1) 2M x 36 | NONE | NONE | NONE |
| 10MB | (1) 256K x 36 | (1) 256K x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 12MB | (1) 512K x 36 | (1) 512K x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 12MB | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 | NONE |
| 16MB | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 16MB | (1) 2M x 36 | (1) 2M x 36 | NONE | NONE |

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| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
|---|---|---|---|---|
| DRAM CONFIGURATION (CON'T) | | | | |
| 16MB | (1) 4M x 36 | NONE | NONE | NONE |
| 17MB | (1) 256K x 36 | (1) 4M x 36 | NONE | NONE |
| 18MB | (1) 256K x 36 | (1) 256K x 36 | (1) 4M x 36 | NONE |
| 18MB | (1) 512K x 36 | (1) 4M x 36 | NONE | NONE |
| 20MB | (1) 512K x 36 | (1) 512K x 36 | (1) 4M x 36 | NONE |
| 20MB | (1) 1M x 36 | (1) 4M x 36 | NONE | NONE |
| 24MB | (1) 512K x 36 | (1) 512K x 36 | (1) 1M x 36 | (1) 4M x 36 |
| 24MB | (1) 1M x 36 | (1) 1M x 36 | (1) 4M x 36 | NONE |
| 24MB | (1) 2M x 36 | (1) 2M x 36 | (1) 2M x 36 | NONE |
| 32MB | (1) 2M x 36 | (1) 2M x 36 | (1) 2M x 36 | (1) 2M x 36 |
| 32MB | (1) 4M x 36 | (1) 4M x 36 | NONE | NONE |
| 32MB | (1) 8M x 36 | NONE | NONE | NONE |
| 36MB | (1) 512K x 36 | (1) 512K x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 36MB | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 | NONE |
| 36MB | (1) 1M x 36 | (1) 8M x 36 | NONE | NONE |
| 40MB | (1) 1M x 36 | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 40MB | (1) 1M x 36 | (1) 1M x 36 | (1) 8M x 36 | NONE |
| 48MB | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 | NONE |
| 48MB | (1) 4M x 36 | (1) 8M x 36 | NONE | NONE |
| 64MB | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 64MB | (1) 16M x 36 | NONE | NONE | NONE |
| 64MB | (1) 4M x 36 | (1) 4M x 36 | (1) 8M x 36 | NONE |
| 64MB | (1) 8M x 36 | (1) 8M x 36 | NONE | NONE |
| 65MB | (1) 256K x 36 | (1) 16M x 36 | NONE | NONE |
| 68MB | (1) 1M x 36 | (1) 16M x 36 | NONE | NONE |
| 68MB | (1) 1M x 36 | (1) 8M x 36 | (1) 8M x 36 | NONE |
| 72MB | (1) 1M x 36 | (1) 1M x 36 | (1) 16M x 36 | NONE |
| 72MB | (1) 1M x 36 | (1) 1M x 36 | (1) 8M x 36 | (1) 8M x 36 |
| 80MB | (1) 4M x 36 | (1) 16M x 36 | NONE | NONE |
| 80MB | (1) 4M x 36 | (1) 8M x 36 | (1) 8M x 36 | NONE |
| 96MB | (1) 4M x 36 | (1) 4M x 36 | (1) 16M x 36 | NONE |
| 96MB | (1) 4M x 36 | (1) 4M x 36 | (1) 8M x 36 | (1) 8M x 36 |
| 96MB | (1) 8M x 36 | (1) 8M x 36 | (1) 8M x 36 | NONE |
| 128MB | (1) 16M x 36 | (1) 16M x 36 | NONE | NONE |
| 128MB | (1) 8M x 36 | (1) 8M x 36 | (1) 8M x 36 | (1) 8M x 36 |

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| CACHE CONFIGURATION | | | |
|---|---|---|---|
| Size | Bank 0 | Bank 1 | TAG |
| 128KB | (4) 32K x 8 | NONE | (1) 8K x 8 |
| 256KB | (4) 32K x 8 | (4) 32K x 8 | (1) 32K x 8 |
| 256KB | (4) 64K x 8 | NONE | (1) 32K x 8 |
| 512KB | (4) 64K x 8 | (4) 64K x 8 | (1) 32K x 8 |
| 512KB | (4) 128K x 8 | NONE | (1) 32K x 8 |
| 1MB | (4) 128K x 8 | (4) 128K x 8 | (1) 64K x 8 |

| CACHE JUMPER CONFIGURATION | | | | | | |
|---|---|---|---|---|---|---|
| Size | JP17 | JP18 | JP19 | JP20 | JP25 | JP26 |
| 128KB | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 |
| 256KB | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 | 1 & 2 |
| 256KB | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 | 2 & 3 | 1 & 2 |
| 512KB | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| 512KB | 1 & 2 | 1 & 2 | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 |
| 1MB | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| Note:  Pins designated should be in the closed position. | | | | | | |

| CPU TYPE CONFIGURATION | | | | | |
|---|---|---|---|---|---|
| Type | J2 | J3 | J4 | J5 | J6 |
| 80486SX | Closed | Open | Open | Open | Closed |
| CX486DX | Open | Closed | Open | Closed | Open |
| AM486DX | Open | Closed | Open | Open | Closed |
| U5S | Closed | Open | Open | Open | Closed |
| 80486DX | Open | Closed | Open | Open | Closed |
| ODP486SX | Open | Closed | Open | Open | Closed |
| CX486DX2V | Open | Closed | Open | Closed | Open |
| CX486DX2 | Open | Closed | Open | Closed | Open |
| AM486DX2 | Open | Closed | Open | Open | Closed |
| AM486DX2-80 | Open | Closed | Open | Open | Closed |
| 80486DX2 | Open | Closed | Open | Open | Closed |
| CX486DX4 | Open | Closed | Open | Closed | Open |
| AM486DX4-NV8T | Open | Closed | Open | Open | Closed |
| AM486DX4-SV8B | Open | Open | Closed | Open | Open |
| 80486DX4 | Open | Closed | Open | Open | Closed |
| 80486DX4 (WB) | Open | Open | Closed | Open | Open |
| Pentium Overdrive | Open | Open | Closed | Open | Open |
| SL Pentium Overdrive | Open | Open | Closed | Open | Open |
| CXM1SC | Open | Open | Closed | Open | Open |

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| CPU TYPE CONFIGURATION (CON'T) | | | | |
|---|---|---|---|---|
| Type | J7 | JP41 | JP42 | JP43 |
| 80486SX | Open | Open | 1 & 2 | 1 & 2 |
| CX486DX | Open | 1 & 2 | 1 & 2 | 1 & 2 |
| AM486DX | Open | Open | 1 & 2 | 1 & 2 |
| U5S | Open | Open | 1 & 2 | 1 & 2 |
| 80486DX | Open | Open | 1 & 2 | 1 & 2 |
| ODP486SX | Open | Open | 1 & 2 | 2 & 3 |
| CX486DX2V | Open | 1 &2 | 1 & 2 | 1 & 2 |
| CX486DX2 | Open | Open | 1 & 2 | 1 & 2 |
| AM486DX2 | Open | Open | 1 & 2 | 1 & 2 |
| AM486DX2-80 | Open | Open | 2 & 3 | 1 & 2 |
| 80486DX2 | Open | Open | 1 & 2 | 1 & 2 |
| CX486DX4 | Open | 1 & 2 | 1 & 2 | 1 & 2 |
| AM486DX4-NV8T | Open | Open | 1 & 2 | 1 & 2 |
| AM486DX4-SV8B | Closed | Open | 1 & 2 | 1 & 2 |
| 80486DX4 | Open | Open | 1 & 2 | 1 & 2 |
| 80486DX4 (WB) | Closed | Open | 1 & 2 | 1 & 2 |
| Pentium Overdrive | Closed | Open | 1 & 2 | 1 & 2 |
| SL Pentium Overdrive | Closed | Open | 1 & 2 | 2 & 3 |
| CXM1SC | Closed | Open | 1 & 2 | 1 & 2 |
| Note:  Pins designated should be in the closed position. | | | | |

| CPU SPEED CONFIGURATION | | |
|---|---|---|
| Speed | JP22 | JP23 |
| 25MHz | pins 2 & 3 closed | pins 2 & 3 closed |
| 33MHz | pins 1 & 2 closed | pins 1 & 2 closed |
| 40MHz | pins 1 & 2 closed | pins 2 & 3 closed |
| 50iMHz | pins 2 & 3 closed | pins 2 & 3 closed |
| 50MHz | pins 2 & 3 closed | pins 1 & 2 closed |
| 66iMHz | pins 1 & 2 closed | pins 1 & 2 closed |
| 75iMHz | pins 2 & 3 closed | pins 2 & 3 closed |
| 80iMHz | pins 1 & 2 closed | pins 2 & 3 closed |
| 100iMHz | pins 1 & 2 closed | pins 1 & 2 closed |
| 120iMHz | pins 1 & 2 closed | pins 2 & 3 closed |

| CPU VOLTAGE CONFIGURATION | | |
|---|---|---|
| Voltage | JP32 | JP38 |
| 3.3v | Open | pins 1 & 2 closed |
| 4v | Open | pins 2 & 3 closed |
| 5v | Closed | Open |

| DMA CONFIGURATION | | |
|---|---|---|
| DMA | JP1 | JP2 |
| DMA 1 | pins 1 & 2 closed | pins 1 & 2 closed |
| DMA 3 | pins 2 & 3 closed | pins 2 & 3 closed |