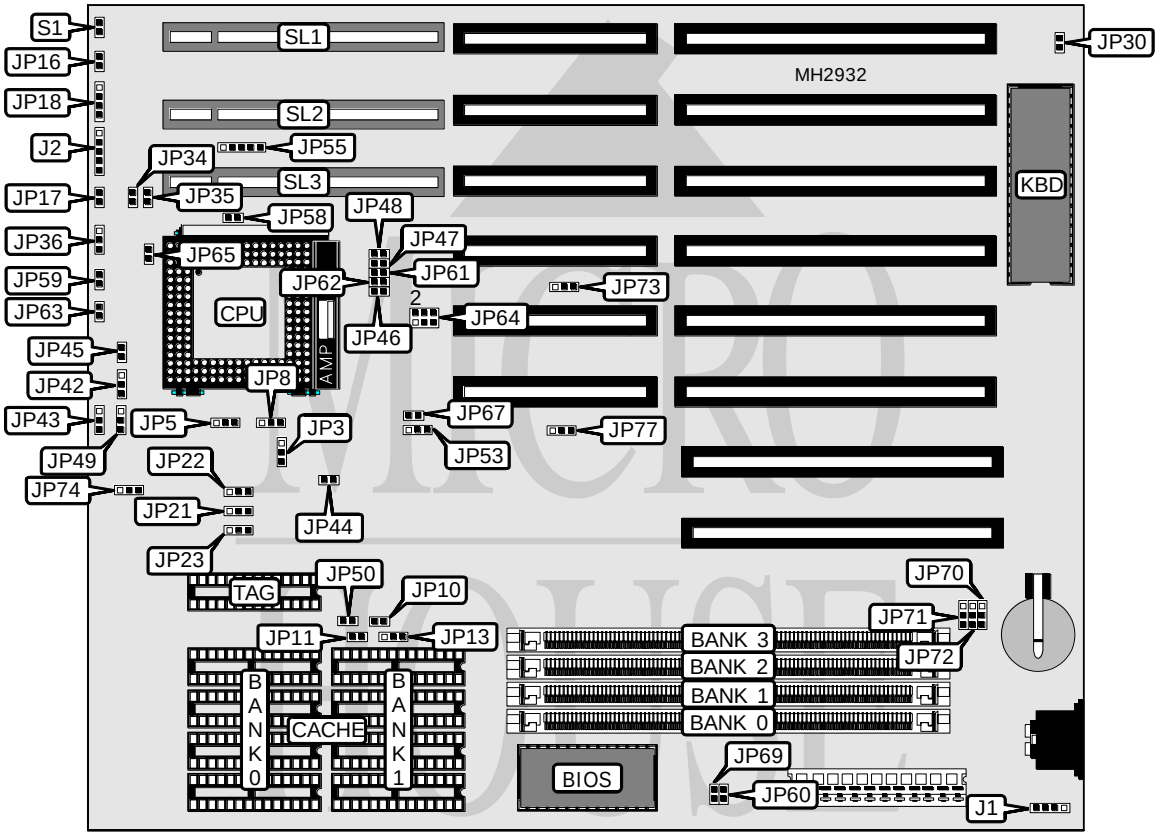
SHUTTLE COMPUTER INTERNATIONAL, INC.
H O T - 4 1 9 (V E R . R 1 . 1)

Processor	80486SX/SL80486SX/80487SX/CX486DX/AM486DX/80486DX/SL80486DX/ CX486DX2/AM486DX2/80486DX2/SL80486DX2/80486DX4/Pentium Overdrive
Processor Speed	20/25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	128MB
Cache	64/128/256/512KB
BIOS	AMI
Dimensions	260mm x 220mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Green PC connector	JP63
Power LED & keylock	J2	Green PC connector	JP68
Turbo LED	JP16	Reset switch	S1
Turbo switch	JP17	32-bit VESA local bus slots	SL1 - SL3
Speaker	JP18		
Note: The location of JP68 is unidentified.			

Continued on next page. . .

SHUTTLE COMPUTER INTERNATIONAL, INC.
H O T - 4 1 9 (V E R . R 1 . 1)

. . . continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP30	Closed
Monitor type select monochrome	JP30	Open
í CPU ADS# signal select normal operation	JP36	pins 2 & 3 closed
CPU ADS# signal select delayed	JP36	pins 1 & 2 closed
í Factory configured - do not alter	JP53	pins 1 & 2 closed
í Factory configured - do not alter	JP67	Closed
í Factory configured - do not alter	JP74	pins 1 & 2 closed
í Factory configured - do not alter	JP77	pins 2 & 3 closed

DRAM CONFIGURATION 1				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE
4MB	(1) 1M x 36	NONE	NONE	NONE
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
5MB	(1) 256K x 36	(1) 1M x 36	NONE	NONE
6MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 1M x 36	NONE	(1) 1M x 36	NONE
10MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	NONE	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	NONE	NONE	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
17MB	(1) 256K x 36	(1) 4M x 36	NONE	NONE
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
20MB	(1) 1M x 36	NONE	(1) 4M x 36	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 4M x 36	NONE	(1) 4M x 36	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 16M x 36	NONE	NONE	NONE
128MB	(1) 16M x 36	(1) 16M x 36	NONE	NONE
128MB	(1) 16M x 36	NONE	(1) 16M x 36	NONE

DRAM CONFIGURATION 2			
Size	Bank 0	Bank 1	Bank 2
2MB	(1) 512K x 36	NONE	NONE
4MB	(1) 1M x 36	NONE	NONE
4MB	(1) 512K x 36	(1) 512K x 36	NONE
6MB	(1) 512K x 36	(1) 1M x 36	NONE
6MB	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36
8MB	(1) 2M x 36	NONE	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE
12MB	(1) 1M x 36	(1) 2M x 36	NONE
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 512K x 36	(1) 512K x 36	(1) 2M x 36

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SHUTTLE COMPUTER INTERNATIONAL, INC.
H O T - 4 1 9 (V E R . R 1 . 1)

. . . continued from previous page

DRAM CONFIGURATION 2 (CON'T)			
Size	Bank 0	Bank 1	Bank 2
16MB	(1) 2M x 36	(1) 2M x 36	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 2M x 36
20MB	(1) 1M x 36	(1) 4M x 36	NONE
20MB	(1) 1M x 36	(1) 2M x 36	(1) 2M x 36
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36
24MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
32MB	(1) 8M x 36	NONE	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE
32MB	(1) 2M x 36	(1) 2M x 36	(1) 4M x 36
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
40MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 2M x 36	(1) 2M x 36	(1) 8M x 36
64MB	(1) 16M x 36	NONE	NONE
64MB	(1) 8M x 36	(1) 8M x 36	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36
96MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
128MB	(1) 16M x 36	(1) 16M x 36	NONE
Note: Bank 3 is not used in this configuration.			

DRAM JUMPER CONFIGURATION						
Setting	JP60	JP61	JP62	JP70	JP71	JP72
Mode 1	Closed	Closed	Closed	1 & 2	1 & 2	1 & 2
Mode 2	Open	Open	Open	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.						

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP10	JP11	JP13	JP50
64KB	Open	Open	pins 1 & 2 closed	Open
128KB	Open	Closed	pins 2 & 3 closed	Open
256KB	Closed	Closed	pins 1 & 2 closed	Open
512KB	Closed	Closed	pins 2 & 3 closed	Closed

CACHE WRITE CONFIGURATION		
Setting	JP58	JP59
Write through	Open	Open
Write back	Closed	Closed

Continued on next page. . .

SHUTTLE COMPUTER INTERNATIONAL, INC.
H O T - 4 1 9 (V E R . R 1 . 1)

. . . continued from previous page

CPU TYPE CONFIGURATION					
Type	JP3	JP5	JP8	JP42	JP43
SL80486SX	Open	Open	1 & 2	1 & 2	1 & 2
80486SX	Open	Open	1 & 2	Open	1 & 2
80487SX	2 & 3	1 & 2	2 & 3	Open	1 & 2
CX486DX	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2
SL80486DX	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
80486DX	1 & 2	1 & 2	2 & 3	Open	1 & 2
CX486DX2	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
80486DX4	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
P24D	2 & 3	1 & 2	2 & 3	Open	1 & 2
P24T	2 & 3	1 & 2	2 & 3	Open	1 & 2
Note: Pins designated should be in the closed position.					

CPU TYPE CONFIGURATION (CON'T)						
Type	JP44	JP45	JP46	JP47	JP48	JP49
SL80486SX	Closed	Closed	Closed	Open	Open	1 & 2
80486SX	Open	Open	Open	Open	Open	1 & 2
80487SX	Open	Open	Open	Open	Open	1 & 2
CX486DX	Open	Open	Open	Closed	Closed	1 & 2
SL80486DX	Closed	Closed	Closed	Open	Open	1 & 2
80486DX	Open	Open	Open	Open	Open	1 & 2
CX486DX2	Open	Open	Open	Closed	Closed	2 & 3
80486DX4	Closed	Closed	Closed	Open	Open	1 & 2
P24D	Open	Open	Open	Open	Open	1 & 2
P24T	Open	Open	Open	Open	Open	1 & 2
Note: Pins designated should be in the closed position.						

CPU SPEED CONFIGURATION			
Speed	JP21	JP22	JP23
20MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
25MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed
33MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
40MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
50iMHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed
50MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
66iMHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
75iMHz	pins 1 & 2 closed	Open	pins 1 & 2 closed
100iMHz (33MHz clock)	Open	pins 1 & 2 closed	pins 1 & 2 closed
100iMHz (50MHz clock)	Open	pins 1 & 2 closed	Open

CPU BUS RATIO CONFIGURATION	
Ratio	JP65
2x	Closed
3x	Open

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SHUTTLE COMPUTER INTERNATIONAL, INC.
H O T - 4 1 9 (V E R . R 1 . 1)

. . . continued from previous page

CPU RDY# DELAY CONFIGURATION		
Setting	JP55	JP69
3v	pins 1 & 3, 2 & 4 closed	pins 2 & 3 closed
3.3v	pins 1 & 3, 2 & 4 closed	Open
Fast VL-bus devices	pins 2 & 3, 3 & 5 closed	Open
Slow VL-bus devices	pins 3 & 4 closed	Closed

CPU VOLTAGE CONFIGURATION		
Voltage	JP64	JP73
3v	pins 1 & 3, 2 & 4 closed	pins 2 & 3 closed
3.3v	pins 1 & 3, 2 & 4 closed	Open
3.45v	pins 1 & 3, 2 & 4 closed	pins 1 & 2 closed
5v	pins 3 & 5, 4 & 6 closed	Open

VESA WAIT STATE CONFIGURATION	
Wait states	JP34
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	JP35
<= 33MHz	Open
> 33MHz	Closed