ADVANCED INTEGRATION RESEARCH, INC. 486PH REV. 1.0

80486SX/CX486DX/AM486DX/80486DX/CX486DX2/AM486DX2/80486DX2/ **Processor**

AM486DX4/80486DX4

25/33/50(internal)/66(internal)/75(internal)/100(internal)MHz **Processor Speed**

Chip Set Intel Max. Onboard DRAM 64MB

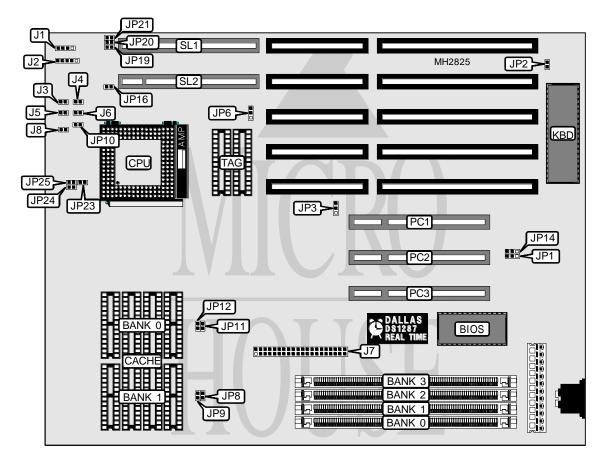
Cache 128/256/512KB

BIOS AMI

Dimensions 330mm x 218mm

I/O Options 32-bit VESA local bus slots (2), 32-bit PCI slots (3), green PC connector, IDE interface

NPU Options



CONNECTIONS					
Purpose	Location	Purpose	Location		
Speaker	J1	Turbo LED	J6		
Power LED & keylock	J2	IDE interface	J7		
IDE interface LED	J3	Green PC connector	J8		
Turbo switch	J4	32-bit PCI slots PC1 - PC3			
Reset switch	J5	32-bit VESA local bus slots	SL1 & SL2		

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í CMOS memory normal operation	JP1	pins 1 & 2 closed		
CMOS memory clear	JP1	pins 2 & 3 closed		
í Monitor type select monochrome/EGA/VGA	JP2	Open		
Monitor type select CGA	JP2	Closed		
í Factory configured - do not alter	JP3	pins 1 & 2 closed		
í Factory configured - do not alter	JP14	pins 1 & 2 closed		

		DRAM CONFIGURATION	N	
Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(1) 1M x 36	NONE	NONE	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 2M x 36	NONE	NONE	NONE
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE
12MB	(1) 2M x 36	(1) 1M x 36	NONE	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	(1) 1M x 36	NONE	(1) 1M x 36
16MB	(1) 2M x 36	(1) 2M x 36	NONE	NONE
16MB	(1) 4M x 36	NONE	NONE	NONE
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	NONE
24MB	(1) 2M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 8M x 36	NONE	NONE	NONE
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	NONE
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
40MB	(1) 2M x 36	(1) 4M x 36	NONE	(1) 4M x 36
40MB	(1) 2M x 36	(1) 8M x 36	NONE	NONE
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	(1) 8M x 36	NONE	NONE

CACHE CONFIGURATION					
Size	Bank 0	Bank 1	TAG		
128KB	(4) 32K x 8	NONE	(2) 32K x 8		
256KB	(4) 32K x 8	(4) 32K x 8	(2) 32K x 8		
256KB	(4) 64K x 8	NONE	(2) 32K x 8		
512KB	(4) 128K x 8	NONE	(2) 32K x 8		

CACHE JUMPER CONFIGURATION					
Size JP8 JP9 JP11 JP12					
128KB	Closed	Open	Open	Open	
256KB	Open	Closed	Closed	Open	
256KB	Closed	Open	Closed	Open	
512KB	Closed	Open	Closed	Closed	

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CPU TYPE CONFIGURATION					
Туре	JP10	JP16	JP23	JP24	JP25
80486SX	Closed	Open	Open	2 & 3	1 & 2
CX486DX (3.3v)	Closed	Closed	Closed	1 & 2	2 & 3
CX486DX (5v)	Closed	Open	Closed	1 & 2	2 & 3
AM486DX	Closed	Open	Closed	1 & 2	2 & 3
80486DX	Closed	Open	Open	1 & 2	1 & 2
CX486DX2 (3.3v)	Closed	Closed	Closed	1 & 2	2 & 3
CX486DX2 (5v)	Closed	Open	Closed	1 & 2	2 & 3
AM486DX2	Closed	Open	Open	1 & 2	2 & 3
80486DX2	Closed	Open	Open	1 & 2	1 & 2
AM486DX4	Closed	Open	Closed	1 & 2	2 & 3
80486DX4	Closed	Open	Open	1 & 2	1 & 2
Note: Pins designated should be in the closed position.					

CPU SPEED CONFIGURATION					
Speed	JP6	JP19	JP20	JP21	
25MHz	pins 2 & 3 closed	Closed	Open	Open	
33MHz	pins 1 & 2 closed	Open	Closed	Closed	
50iMHz	pins 2 & 3 closed	Closed	Open	Open	
66iMHz	pins 1 & 2 closed	Open	Closed	Closed	
75iMHz	pins 2 & 3 closed	Closed	Open	Open	
100iMHz	pins 1 & 2 closed	Open	Closed	Closed	