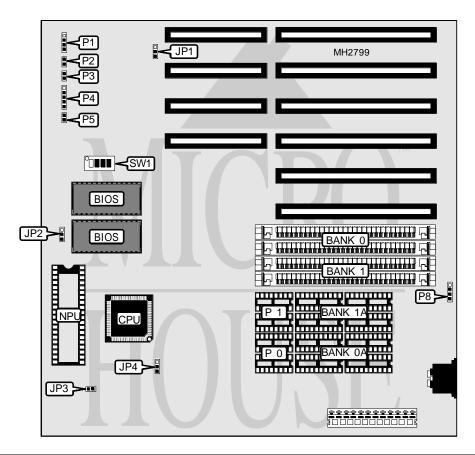
UNIDENTIFIED FOX-286

80286 **Processor Processor Speed** 12/16MHz Unidentified **Chip Set**

Max. Onboard DRAM 4MB Cache None **BIOS** AMI

Dimensions 220mm x 220mm

I/O Options None **NPU Options** 80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	P1	Power LED & keylock	P4
Reset switch	P2	Turbo switch	P5
Turbo LED	Р3	External battery	P8

Continued on next page. . .

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í Parity disabled	JP1	pins 1 & 2 closed		
Parity enabled	JP1	pins 2 & 3 closed		
í BIOS type select 27256	JP2	pins 1 & 2 closed		
BIOS type select 27128	JP2	pins 2 & 3 closed		
í Monitor type select CGA\EGA\VGA	JP3	Closed		
Monitor type select monochrome	JP3	Open		
í DRAM speed select normal	JP4	pins 2 & 3 closed		
DRAM speed select non standard memory installed	JP4	pins 1 & 2 closed		

DRAM CONFIGURATION						
Size	Bank 0A	Parity 0	Bank 1A	Parity 1	Bank O	Bank 1
256KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(2) 4164	NONE	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9

DRAM SWITCH CONFIGURATION			
Size	SW1/2	SW1/3	SW1/4
256KB	On	On	On
512KB	On	Off	On
640KB	On	On	Off
1MB	On	Off	Off
2MB	Off	On	Off
4MB	Off	Off	Off

WAIT STATE CONFIGURATION		
Wait states	SW1/1	
0 wait states	On	
1 wait state	Off	