ILON USA, INC.
M - 4 2 3

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	20/25/33/50(internal)MHz
Chip Set	PC Chips
Max. Onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	4167
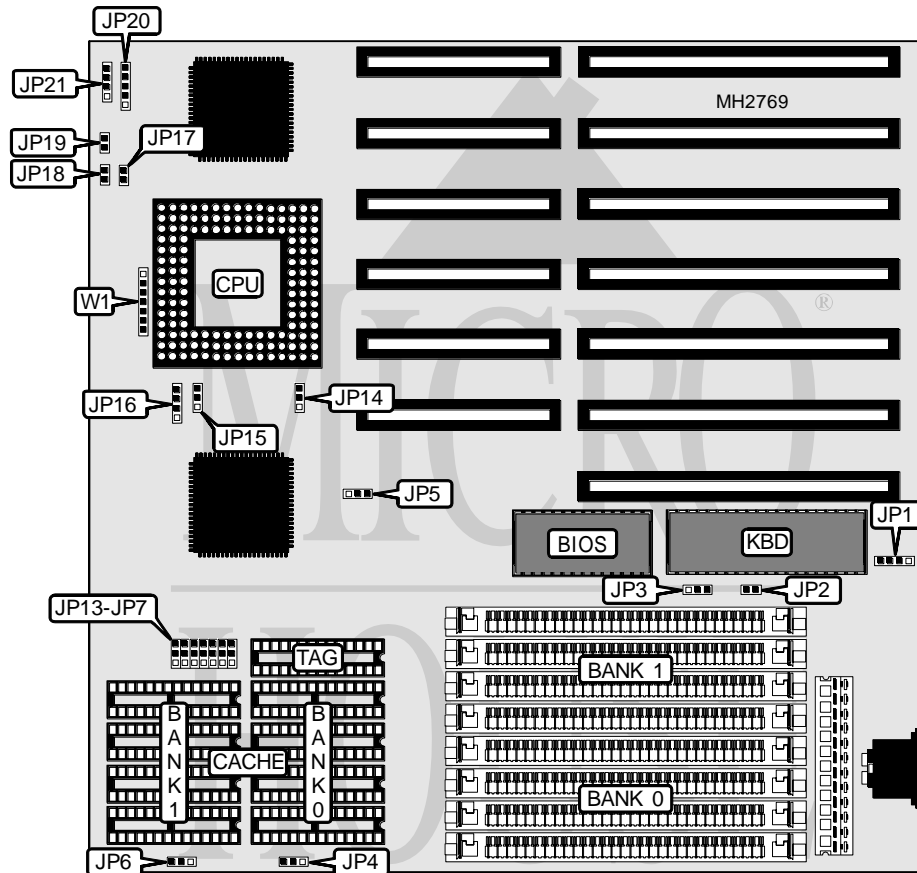
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Power LED & keylock	JP20
Reset switch	JP17	Speaker	JP21
Turbo LED	JP18	Weitek 4167 connector	W1
Turbo switch	JP19		

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open
í Gate type select fast A20	JP3	pins 1 & 2 closed
Gate type select keyboard A20	JP3	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER WRITE-THROUGH CONFIGURATION					
Size	JP4	JP6	JP11	JP12	JP13
32KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
64KB	1 & 2	2 & 3	1 & 2	1 & 2	2 & 3
128KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
256KB	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in closed position.					

CACHE JUMPER WRITE-BACK CONFIGURATION				
Size	JP7	JP8	JP9	JP10
32KB	1 & 2	2 & 3	2 & 3	2 & 3
64KB	2 & 3	2 & 3	2 & 3	1 & 2
128KB	2 & 3	2 & 3	1 & 2	2 & 3
256KB	2 & 3	1 & 2	2 & 3	2 & 3
Note: Pins designated should be in closed position.				

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CPU TYPE CONFIGURATION			
Type	JP14	JP15	JP16
80486SX	pins 2 & 3 closed	Open	pins 2 & 3 closed
80487SX	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2, 3 & 4 closed
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2, 3 & 4 closed
80486DX2	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2, 3 & 4 closed

CPU SPEED CONFIGURATION	
Speed	JP5
20MHz	pins 1 & 2 closed
25MHz	pins 2 & 3 closed
33MHz	pins 1 & 2 closed
50iMHz	pins 2 & 3 closed