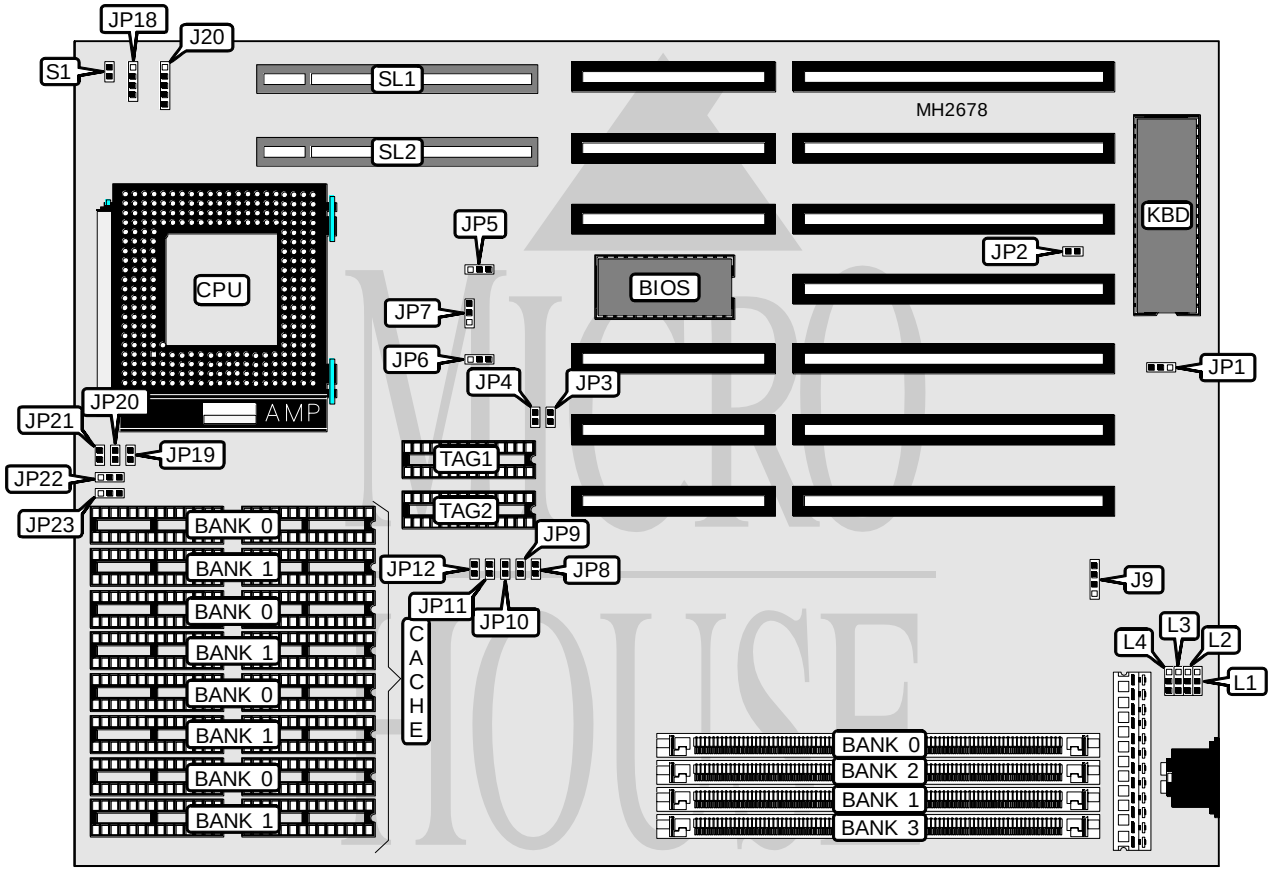
VISION TECHNOLOGIES
V S P T

Processor	Pentium
Processor Speed	60/66MHz
Chip Set	OPTI
Max. Onboard DRAM	128MB
Cache	64/128/256/512KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J9	Reset switch	S1
Power LED & keylock	J20	32-bit VESA local bus slots	SL1 - SL2
Speaker	JP18		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation (Internal Battery)	JP1	pins 1 & 2 closed
CMOS memory clear	JP1	pins 2 & 3 closed
Battery type select internal	JP1	pins 2 & 3 closed
í Monitor type select color	JP2	Open
Monitor type select monochrome	JP2	Closed
í MDHDOE# inactive end of last T2	JP3	Open
MDHDOE# inactive beginning of last T2	JP3	Closed
í LCLK with 74F08 buffer	JP7	pins 1 & 2 closed
Internal LCLK directly drive VESA	JP7	pins 2 & 3 closed
í Back to back IO delay enabled	JP10	Closed
Back to back IO delay disabled	JP10	Open
í LEDV* sample selection end of second T2	JP11	Closed
LEDV* sample selection end of first T2	JP11	Open
í VESA local bus enabled	JP12	Open
VESA local bus disabled	JP12	Closed
í Factory configured - do not alter	L1	N/A
í Factory configured - do not alter	L2	N/A
í Factory configured - do not alter	L3	N/A
í Factory configured - do not alter	L4	N/A

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE
4MB	(1) 512K x 36	(1) 512K x 36	NONE	NONE
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
6MB	(1) 256K x 36	(1) 256K x 36	(1) 512K x 36	(1) 512K x 36
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36
10MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 512K x 36	(1) 512K x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	(1) 2M x 36	NONE	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
18MB	(1) 256K x 36	(1) 256K x 36	(1) 2M x 36	(1) 2M x 36
20MB	(1) 512K x 36	(1) 512K x 36	(1) 2M x 36	(1) 2M x 36
24MB	(1) 1M x 36	(1) 1M x 36	(1) 2M x 36	(1) 2M x 36
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
34MB	(1) 256K x 36	(1) 256K x 36	(1) 4M x 36	(1) 4M x 36
36MB	(1) 512K x 36	(1) 512K x 36	(1) 4M x 36	(1) 4M x 36
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 2M x 36	(1) 2M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	(1) 8M x 36	NONE	NONE

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DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
66MB	(1) 256K x 36	(1) 256K x 36	(1) 8M x 36	(1) 8M x 36
68MB	(1) 512K x 36	(1) 512K x 36	(1) 8M x 36	(1) 8M x 36
72MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36
80MB	(1) 2M x 36	(1) 2M x 36	(1) 8M x 36	(1) 8M x 36
96MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	(1) 8M x 36
128MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG1	TAG2
64KB	(8) 8K x 8	NONE	NONE	(1) 8K x 8
128KB	(8) 8K x 8	(8) 8K x 8	NONE	(1) 8K x 8
256KB	(8) 32K x 8	NONE	NONE	(1) 8K x 8
512KB	(8) 32K x 8	(8) 32K x 8	(1) 8K x 8	(1) 8K x 8

CACHE JUMPER CONFIGURATION						
Size	JP6	JP19	JP20	JP21	JP22	JP23
64KB	1 & 2	Open	Open	Open	1 & 2	1 & 2
128KB	2 & 3	Closed	Open	Open	2 & 3	2 & 3
256KB	1 & 2	Closed	Closed	Open	1 & 2	1 & 2
512KB	2 & 3	Closed	Closed	Closed	2 & 3	2 & 3
Note: Pins designated should be in closed position.						

CPU/LCLK CONFIGURATION		
Setting	JP4	JP5
External	Closed	pins 1 & 2 closed
Internal	Open	pins 2 & 3 closed

CPU/LCLK CONFIGURATION		
Setting	JP8	JP9
LCLK/2	Open	Open
LCLK/3	Closed	Open
LCLK/4	Open	Closed
LCLK/5	Closed	Closed