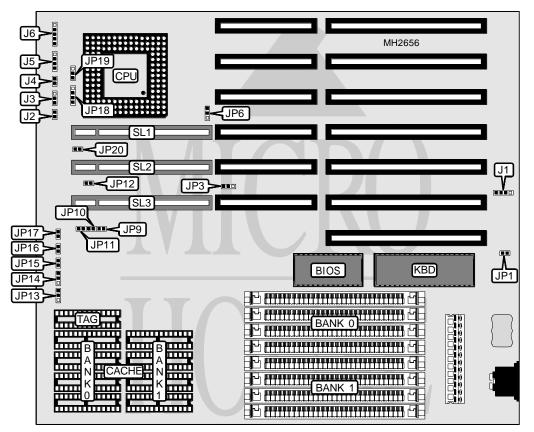
BEK-TRONIK TECHNOLOGY, INC. B E K - V 4 1 9

80486SX/80487SX/80486DX/80486DX2/Pentium Overdrive Processor **Processor Speed** 25/33/40/50(internal)/50/66(internal)MHz UMC Chip Set Max. Onboard DRAM 64MB Cache 32/64/128/256KB BIOS AMI/Award Dimensions 330mm x 218mm I/O Options 32-bit VESA local bus slots (3) **NPU Options** None



CONNECTIONS				
Purpose Location Purpose Location				
External battery	J1	Speaker	J5	
Reset switch	J2	Power LED & keylock	JG	
Turbo switch	J3	32-bit VESA local bus slots	SL1 - SL3	
Turbo LED	J4			

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Battery type select internal	JP1	Closed	
Battery type select external	JP1	Open	
í VESA card type select any card installed	JP3	pins 1 & 2 closed	
VESA card type select Weitek 9000 only	JP3	pins 2 & 3 closed	
í ADS delay select no delays	JP6	pins 1 & 2 closed	
ADS delay select 1 delay for 50MHz	JP6	pins 2 & 3 closed	

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 1M x 9	(4) 1M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

		CACHE JUMPER	CONFIGURATION		
Size	JP13	JP14	JP15	JP16	JP17
32KB	Open	2&3	Open	Open	Open
64KB	Open	1&2	Open	Closed	Open
128KB	2 & 3	2&3	Open	Closed	Closed
256KB 1 & 2 1 & 2 Closed Closed Closed					
Note: Pins designated should be in the closed position.					

	CPU TYPE CONFIGURATION	
Туре	JP18	JP19
80486SX	pins 2 & 3 closed	Open
80487SX	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed
80486DX	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed
80486DX2	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed
Pentium Overdrive	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed

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CPU SPEED CONFIGURATION			
Speed	JP9	JP10	JP11
25MHz	Open	Open	Closed
33MHz	Closed	Open	Closed
40MHz	Open	Closed	Closed
50iMHz	Open	Open	Closed
50MHz	Closed	Open	Open
66iMHz	Closed	Open	Closed

VESA WAIT STATE CONFIGURATION		
Wait states JP20		
0 wait states Open		
1 wait state Closed		

BUS SPEED CONFIGURATION		
CPU speed JP12		
<= 33MHz	Open	
> 33MHz Closed		