HOT-419 (VER.1.0)

CX486S/SL80486SX/80486SX/80487SX/CX486S2/CX486DX/AM80486DX/ **Processor** 

> SL80486DX/80486DX/CX486DX2/80486DX2/80486DX4/P24D/ Pentium Overdrive

20/25/33/40/50(internal)/50/66(internal)/75(inlternal)/100(internal)MHz **Processor Speed** 

**Chip Set** OPTI Max. Onboard DRAM 128MB

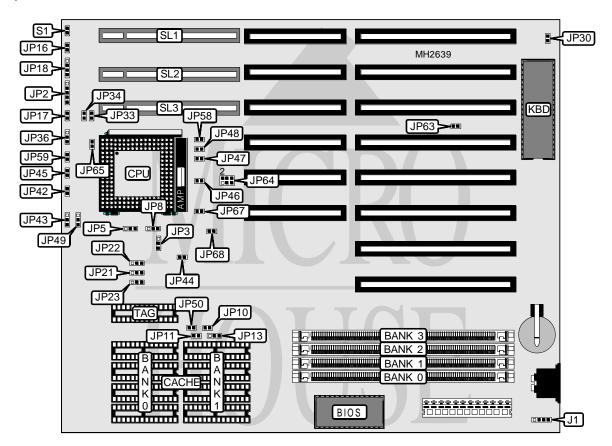
Cache 64/128/256/512KB

**BIOS** AMI

**Dimensions** 260mm x 218mm

I/O Options 32-bit VESA local bus slots (3), green PC connector

**NPU Options** 



CONNECTIONS				
Purpose	Location	Purpose	Location	
External battery	J1	Green PC connector	JP63	
Power LED & keylock	JP2	Green PC connector	JP68	
Turbo LED	JP16	Reset switch	S1	
Turbo switch	JP17	32-bit VESA local bus slots	SL1 - SL3	
Speaker	JP18			

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í Monitor type select color	JP30	Closed		
Monitor type select monochrome	JP30	Open		
í ADS signal delay disabled	JP36	pins 2 & 3 closed		
ADS signal delay enabled	JP36	pins 1 & 2 closed		
í CPU cache type select write back (P24T)	JP58	Closed		
CPU cache type select write through (P24T)	JP58	Open		
í CPU cache type select write back (P24D)	JP59	Closed		
CPU cache type select write through (P24D)	JP59	Open		
í Clock ratio select 2x	JP65	Closed		
Clock ratio select 3x	JP65	Open		

		DRAM CONFIGURATION	V	
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE
4MB	(1) 1M x 36	NONE	NONE	NONE
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
5MB	(1) 256K x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
10MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	NONE	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	NONE	NONE	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
20MB	(1) 1M x 36	NONE	(1) 4M x 36	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
64MB	(1) 16M x 36	NONE	NONE	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
128MB	(1) 16M x 36	(1) 16M x 36	NONE	NONE
128MB	(1) 16M x 36	NONE	(1) 16M x 36	NONE

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	
128KB	(4) 32K x 8	NONE	(1) 32K x 8	
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	
512KB	(4) 128K x 8	(4) 32K x 8	(1) 32K x 8	

CACHE JUMPER CONFIGURATION					
Size	JP10	JP11	JP13	JP50	JP67
64KB	Open	Open	2 & 3	Open	Closed
128KB	Open	Closed	1 & 2	Open	Open
256KB	Closed	Closed	2 & 3	Open	Closed
512KB	Closed	Closed	1 & 2	Closed	Open
Note: Pins designated should be in closed position.					

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CPU TYPE CONFIGURATION						
Туре	JP3	JP5	JP8	JP42	JP43	JP44
CX486S	Open	Open	2 & 3	2 & 3	1 & 2	Open
SL80486SX	Open	Open	2 & 3	1 & 2	1 & 2	Closed
80486SX	1 & 2	2 & 3	1 & 2	Open	1 & 2	Open
80487SX	1 & 2	2 & 3	1 & 2	Open	1 & 2	Open
CX486S2	Open	Open	2 & 3	2 & 3	2 & 3	Open
CX486DX	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	Open
AM80486DX	1 & 2	2 & 3	1 & 2	Open	1 & 2	Open
SL80486DX	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2	Closed
80486DX	1 & 2	2 & 3	1 & 2	Open	1 & 2	Open
CX486DX2	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3	Open
80486DX2	1 & 2	2 & 3	1 & 2	Open	1 & 2	Open
80486DX4	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2	Closed
P24D	2 & 3	2 & 3	1 & 2	Open	1 & 2	Open
P24T	2 & 3	2 & 3	1 & 2	Open	1 & 2	Open
Note: Pins desi	gnated should be	in closed positio	n.			

CPU TYPE CONFIGURATION (CON'T)					
Type	JP45	JP46	JP47	JP48	JP49
CX486S	Open	Open	Closed	Closed	1 & 2
SL80486SX	Closed	Closed	Open	Open	1 & 2
80486SX	Open	Open	Open	Open	1 & 2
80487SX	Open	Open	Open	Open	1 & 2
CX486S2	Open	Open	Closed	Closed	2 & 3
CX486DX	Open	Open	Closed	Closed	1 & 2
AM80486DX	Open	Open	Open	Open	1 & 2
SL80486DX	Closed	Closed	Open	Open	1 & 2
80486DX	Open	Open	Open	Open	1 & 2
CX486DX2	Open	Open	Closed	Closed	1 & 2
80486DX2	Open	Open	Open	Open	1 & 2
80486DX4	Closed	Closed	Open	Open	1 & 2
P24D	Open	Open	Open	Open	1 & 2
P24T	Open	Open	Open	Open	1 & 2
Note: Pins designa	ated should be in clo	sed position.	_	·	

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	CPU SPEED CONFIGURATION				
Speed	JP21	JP22	JP23		
20MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed		
25MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed		
33MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed		
40MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed		
50iMHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed		
50MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed		
66iMHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed		
75iMHz	Open	pins 2 & 3 closed	pins 2 & 3 closed		
100iMHz	pins 2 & 3 closed	Open	pins 2 & 3 closed		
100iMHz	pins 2 & 3 closed	Open	Open		

CPU VOLTAGE CONFIGURATION		
Voltage	JP64	
3.3v	pins 3 & 5, 4 & 6 closed	
5v	pins 1 & 3, 2 & 4 closed	

VESA WAIT STATE CONFIGURATION		
Wait states	JP33	
0 wait states	Open	
1 wait state	Closed	

BUS SPEED CONFIGURATION		
CPU speed	JP34	
<= 33MHz	Open	
> 33MHz	Closed	