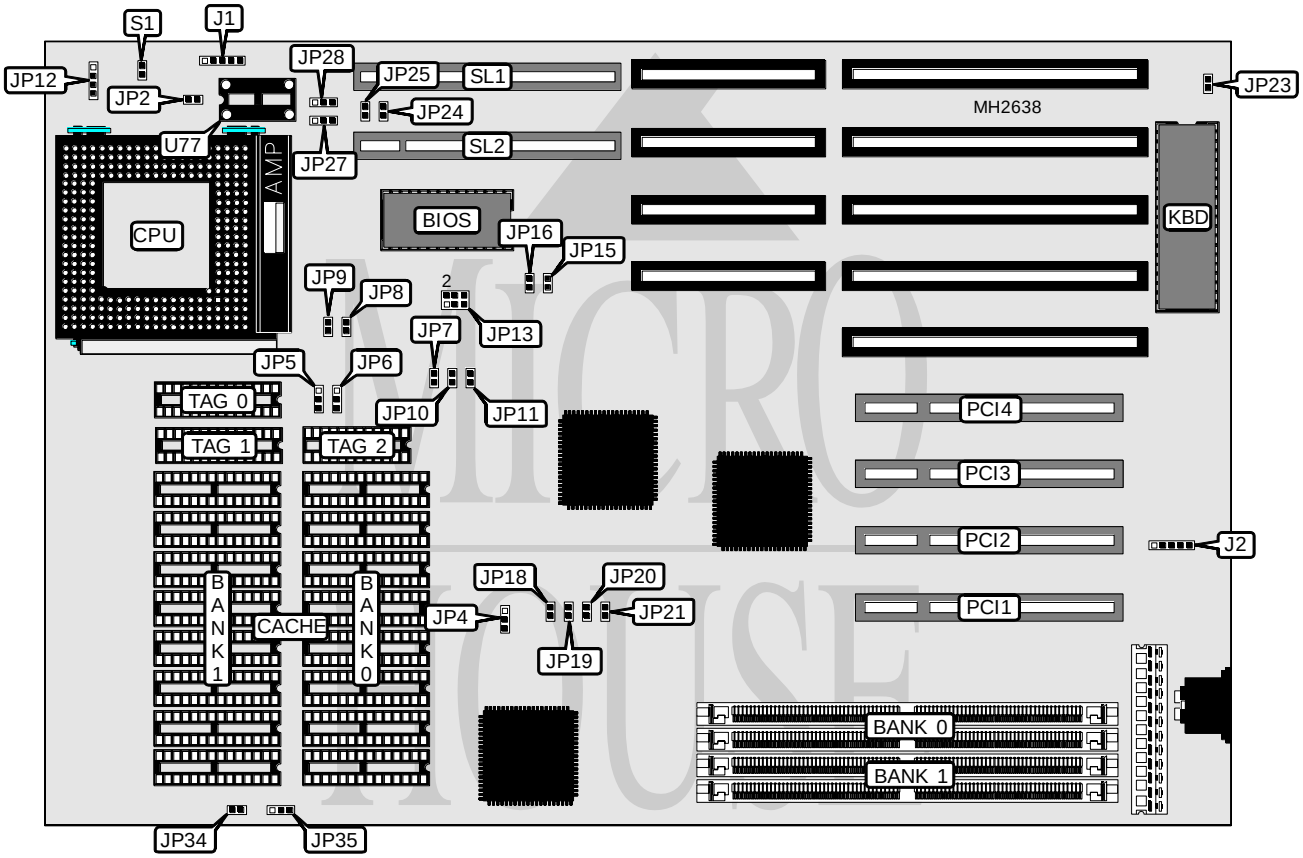
SHUTTLE COMPUTER INTERNATIONAL, INC.
H O T - 5 4 3

Processor	Pentium
Processor Speed	75/90/100MHz
Chip Set	OPTI
Max. Onboard DRAM	128MB
Cache	64/128/256/512/1024/2048KB
BIOS	Award
Dimensions	330mm x 220mm
I/O Options	32-bit VESA local bus slots (2), 32-bit PCI slots (4)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	32-bit PCI slots	PCI1 - PCI4
External battery	J2	Reset switch	S1
Speaker	JP12	32-bit VESA local bus	SL1 - SL2

Continued on next page. . .

SHUTTLE COMPUTER INTERNATIONAL, INC.
H O T - 5 4 3

. . . continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP15	Closed
í Back-to-Back I/O cycle select fast	JP20	Open
Back-to-Back I/O cycle select slow	JP20	Closed
í LDEV# type select end of first T2	JP21	Open
LDEV# type select end of second T2	JP21	Closed
í Monitor type select color	JP23	Closed
Monitor type select monochrome	JP23	Open
í Factory configured - do not alter	JP34	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	NONE
4MB	(2) 512K x 36	NONE
4MB	(2) 256K x 36	(2) 256K x 36
6MB	(2) 256K x 36	(2) 512K x 36
8MB	(2) 1M x 36	NONE
8MB	(2) 512K x 36	(2) 512K x 36
12MB	(2) 512K x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE
64MB	(2) 4M x 36	(2) 4M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION					
Size	Bank 0	Bank 1	TAG0	TAG1	TAG2
64KB	(8) 8K x 8	NONE	(1) 8K x 8	(1) 8K x 8	(1) 16K x 1
128KB	(8) 8K x 8	(8) 8K x 8	(1) 8K x 8	(1) 8K x 8	(1) 16K x 1
256KB	(8) 32K x 8	NONE	(1) 8K x 8	(1) 8K x 8	(1) 16K x 1
512KB	(8) 32K x 8	(8) 32K x 8	(1) 8K x 8	(1) 8K x 8	(1) 16K x 1
512KB	(8) 64K x 8	NONE	(1) 32K x 8	(1) 32K x 8	NONE
1MB	(8) 128K x 8	NONE	(1) 32K x 8	(1) 32K x 8	NONE
2MB	(8) 128K x 8	(8) 128K x 8	(1) 128K x 8	(1) 128K x 8	NONE

Continued on next page. . .

SHUTTLE COMPUTER INTERNATIONAL, INC.
H O T - 5 4 3

. . . continued from previous page

CACHE JUMPER CONFIGURATION									
Size	JP4	JP5	JP6	JP7	JP8	JP9	JP10	JP11	JP35
64KB	1 & 2	1 & 2	1 & 2	Open	Open	Open	Open	Open	Open
128KB	2 & 3	2 & 3	2 & 3	Open	Open	Closed	Open	Open	Open
256KB	1 & 2	1 & 2	1 & 2	Open	Closed	Closed	Open	Open	Open
512KB	2 & 3	2 & 3	2 & 3	Closed	Closed	Closed	Open	Open	Open
512KB	1 & 2	1 & 2	1 & 2	Closed	Closed	Closed	Open	Open	2 & 3
1MB	1 & 2	1 & 2	1 & 2	Closed	Closed	Closed	Closed	Open	2 & 3
2MB	2 & 3	2 & 3	2 & 3	Closed	Closed	Closed	Closed	Closed	1 & 2
Note: Pins designated should be in closed position.									

CPU SPEED CONFIGURATION	
Speed	JP13
75MHz	pins 5 & 6 closed
90MHz	pins 3 & 4 closed
100MHz	pins 1 & 2, 5 & 6 closed

CPU BUS/CORE CONFIGURATION	
Ratio	JP2
1/2 Bus/Core Ratio	Closed
2/3 Bus/Core Ratio	Open

AT BUS CLOCK CONFIGURATION		
Type	JP18	JP19
LCLK/2	Open	Open
LCLK/3	Closed	Open
LCLK/4	Open	Closed
LCLK/5	Closed	Closed

LCLK CONFIGURATION				
Setting	JP16	JP27	JP28	U77
External	Closed	pins 2 & 3 closed	pins 2 & 3 closed	Installed
Internal	Open	pins 1 & 2 closed	pins 1 & 2 closed	Not Installed

VESA WAIT STATE CONFIGURATION	
Wait states	JP24
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	JP25
<= 33MHz	Closed
> 33MHz	Open