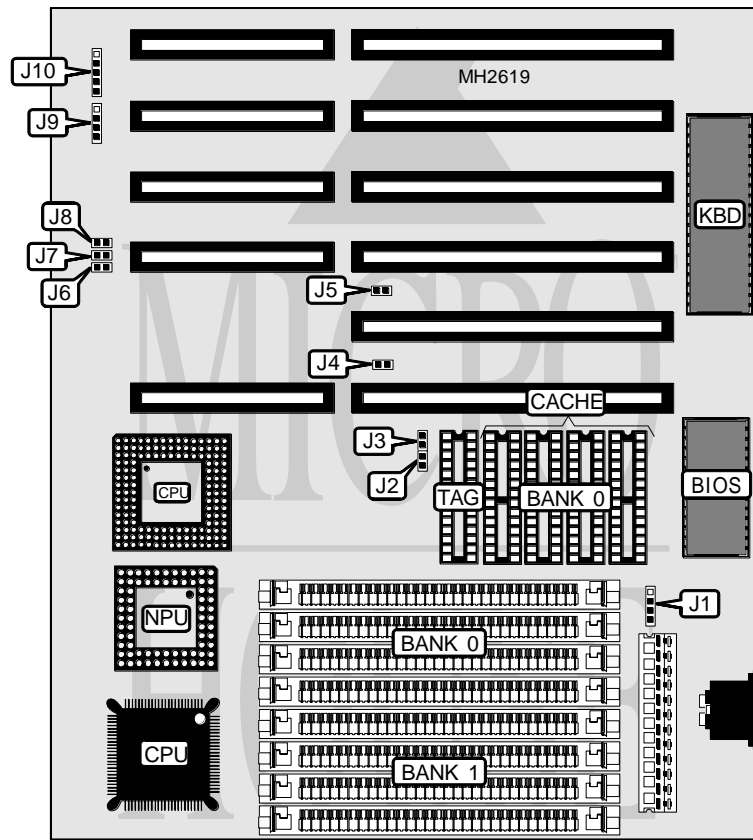


UNIDENTIFIED M-326 VER. 2

Processor	80386DX/CX486DLC
Processor Speed	33/40MHz
Chip Set	Unidentified
Max. Onboard DRAM	32MB
Cache	Unidentified
BIOS	Unidentified
Dimensions	220mm x 170mm
I/O Options	None
NPU Options	80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo LED	J8
Reset switch	J6	Speaker	J9
Turbo switch	J7	Power LED & keylock	J10

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J2	N/A
í Factory configured - do not alter	J3	N/A
í NPU disabled	J5	pins 1 & 2 closed
NPU enabled	J5	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION
Note: The cache configuration is unidentified.

CPU SPEED CONFIGURATION	
Speed	J4
33MHz	pins 2 & 3 closed
40MHz	pins 1 & 2 closed