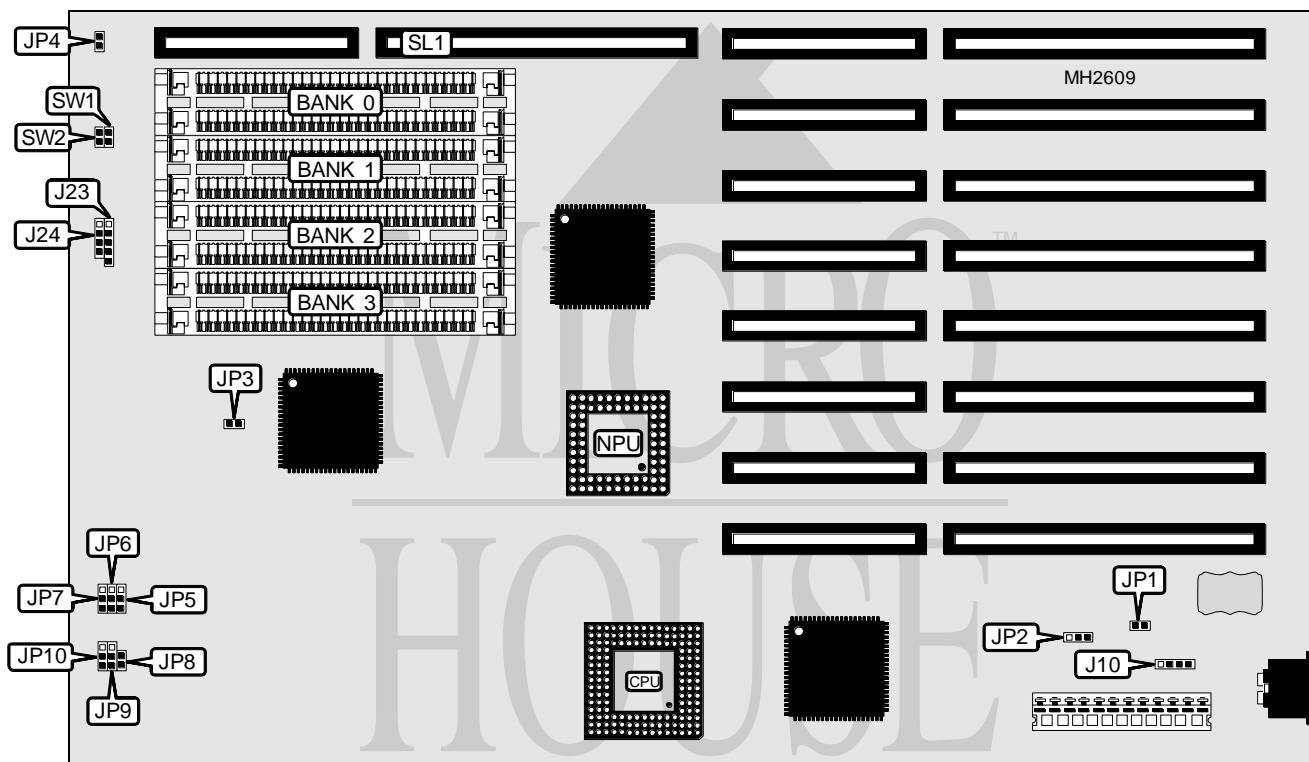


UNIDENTIFIED**386-33/40WB**

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	OPTI
Max. Onboard DRAM	64MB (32MB on external memory card)
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	External memory card
NPU Options	80387DX/3167

**CONNECTIONS**

Purpose	Location	Purpose	Location
External battery	J10	External memory card	SL1
Power LED & keylock	J23	Reset switch	SW1
Speaker	J24	Turbo switch	SW2
Turbo LED	JP4		

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UNIDENTIFIED**386-33/40WB***... continued from previous page***USER CONFIGURABLE SETTINGS**

Function	Jumper	Position
í Monitor type select color	JP1	pins 2 & 3 closed
Monitor type select monochrome	JP1	pins 1 & 2 closed
í CMOS memory normal operation	JP2	Closed
CMOS memory clear	JP2	Open
í I/O bus speed select ATCLK/8	JP3	Closed
I/O bus speed select ATCLK/6	JP3	Open

DRAM CONFIGURATION (ON BOARD)

Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 256K x 9			
2MB	(2) 1M x 9	NONE	NONE	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9			
8MB	(2) 4M x 9	NONE	NONE	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE
24MB	(2) 4M x 9	(2) 4M x 9	(2) 4M x 9	NONE
32MB	(2) 4M x 9			

DRAM CONFIGURATION (MEMORY CARD)

Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 256K x 9			
2MB	(2) 1M x 9	NONE	NONE	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9			
8MB	(2) 4M x 9	NONE	NONE	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE
24MB	(2) 4M x 9	(2) 4M x 9	(2) 4M x 9	NONE
32MB	(2) 4M x 9			

CACHE CONFIGURATION

Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(1) 2K x 9
64KB	(4) 8K x 8	(4) 8K x 8	(1) 4K x 9
128KB	(4) 32K x 8	NONE	(1) 8K x 9
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 9

Note: The location of the cache and TAG is unidentified.

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UNIDENTIFIED**386-33/40WB***... continued from previous page*

CACHE JUMPER CONFIGURATION						
Size	JP5	JP6	JP7	JP8	JP9	JP10
32KB	1 & 2	1 & 2	1 & 2	Open	2 & 3	2 & 3
64KB	2 & 3	1 & 2	1 & 2	Open	2 & 3	1 & 2
128KB	2 & 3	1 & 2	2 & 3	Closed	2 & 3	1 & 2
256KB	2 & 3	2 & 3	2 & 3	Closed	1 & 2	1 & 2

Note: Pins designated should be in the closed position.