FIRST INTERNATIONAL COMPUTER, INC. 4386-VIO

Processor 80386DX/CX486DLC/80486SX/80487SX/80486DX/80486DX2

Processor Speed 20/25/33/40/50(internal)/50/66(internal)MHz

Chip Set VIA

Max. Onboard DRAM 32/128MB (depends on CPU used)

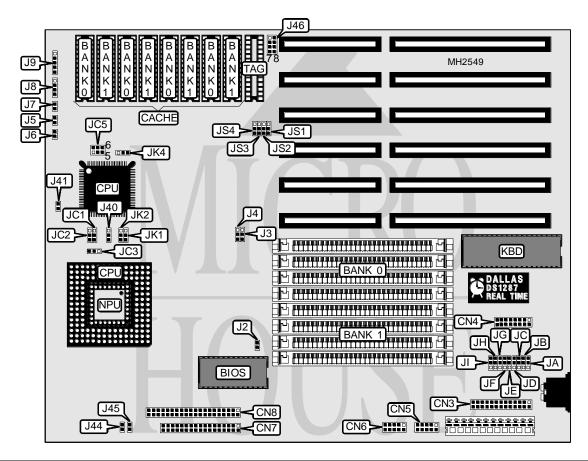
Cache 64/128/256KB

BIOS Award

Dimensions 254mm x 220mm

I/O Options Floppy drive interface, game port, IDE interface, parallel port, serial ports (2)

NPU Options 80387DX



CONNECTIONS				
Purpose	Location	Purpose	Location	
Parallel port	CN3	Turbo LED	J5	
Game port	CN4	Turbo switch	J6	
Serial port 1	CN5	Reset switch	J7	
Serial port 2	CN6	Speaker	J8	
Floppy drive interface	CN7	Power LED & keylock	J9	
IDE interface	CN8	IDE interface LED	J44	

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Parallel port unidirectional (printer)	JG	pins 1 & 2 closed	
Parallel port bidirectional	JG	pins 2 & 3 closed	
í IDE interface enabled	JH	pins 2 & 3 closed	
IDE interface disabled	JH	pins 1 & 2 closed	
í Floppy drive interface enabled	JI	pins 2 & 3 closed	
Floppy drive interface disabled	JI	pins 1 & 2 closed	
í Monitor type select CGA	J2	Closed	
Monitor type select monochrome/EGA/VGA	J2	Open	
í Factory configured - do not alter	J3	N/A	
í Factory configured - do not alter	J4	N/A	
í NPU synchronous with CPU	J40	pins 1 & 2 closed	
NPU asynchronous with CPU	J40	pins 2 & 3 closed	
í Factory configured - do not alter	J41	N/A	
í BALE to IDE select connected to CN8 pin28	J45	Closed	
BALE to IDE select not connected	J45	Open	

DRAM CONFIGURATION (80386)				
Size	Bank 0	Bank 1		
1MB	(4) 256K x 9	NONE		
2MB	(4) 256K x 9	(4) 256K x 9		
4MB	(4) 1M x 9	NONE		
5MB	(4) 256K x 9	(4) 1M x 9		
5MB	(4) 1M x 9	(4) 256K x 9		
8MB	(4) 1M x 9	(4) 1M x 9		
16MB	(4) 4M x 9	NONE		
17MB	(4) 256K x 9	(4) 4M x 9		
17MB	(4) 4M x 9	(4) 256K x 9		
20MB	(4) 1M x 9	(4) 4M x 9		
20MB	(4) 4M x 9	(4) 1M x 9		
32MB	(4) 4M x 9	(4) 4M x 9		

DRAM CONFIGURATION (80486)				
Size	Bank 0	Bank 1		
1MB	(4) 256K x 9	NONE		
2MB	(4) 256K x 9	(4) 256K x 9		
4MB	(4) 1M x 9	NONE		
5MB	(4) 256K x 9	(4) 1M x 9		
5MB	(4) 1M x 9	(4) 256K x 9		
8MB	(4) 1M x 9	(4) 1M x 9		
16MB	(4) 4M x 9	NONE		
17MB	(4) 256K x 9	(4) 4M x 9		
17MB	(4) 4M x 9	(4) 256K x 9		
20MB	(4) 1M x 9	(4) 4M x 9		
20MB	(4) 4M x 9	(4) 1M x 9		

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DRAM CONFIGURATION (80486 CON'T)				
Size	Bank 0	Bank 1		
32MB	(4) 4M x 9	(4) 4M x 9		
64MB	(4) 16M x 9	NONE		
65MB	(4) 256K x 9	(4) 16M x 9		
65MB	(4) 16M x 9	(4) 256K x 9		
68MB	(4) 1M x 9	(4) 16M x 9		
68MB	(4) 16M x 9	(4) 1M x 9		
80MB	(4) 4M x 9	(4) 16M x 9		
80MB	(4) 16M x 9	(4) 4M x 9		
128MB	(4) 16M x 9	(4) 16M x 9		

CACHE CONFIGURATION				
Size Bank 0 Bank 1 TAG				
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	
128KB	(4) 32K x 8	NONE	(1) 8K x 8	
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	

	CACHE JUMPER CONFIGURATION				
Size JS1 JS2 JS3 JS4					
64KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	
128KB	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	
256KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	

	CPU TYPE CONFIGURATION			
Туре	JC1	JC2	JC3	JC5
80386DX-33/40	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	Open
CX486DLC-33/40	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	Closed
80486SX-20/25	pins 2 & 3 closed	pins 2 & 3 closed	Open	Open
80486SX-33	pins 2 & 3 closed	pins 2 & 3 closed	Open	Open
80487SX-20/25	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	Open
80486DX-20/25	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	Open
80486DX-33/50	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	Open
80486DX2-50	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	Open
80486DX2-66	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	Open
Note: If JC5 is closed	Note: If JC5 is closed, pins 1 & 2, 3 & 4, 5 & 6 are closed			

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	CPU TYPE CONFIGURATION (CON'T)				
Туре	JK1	JK2	JK4		
80386DX-33/40	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed		
CX486DLC-33/40	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed		
80486SX-20/25	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed		
80486SX-33	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed		
80487SX-20/25	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed		
80486DX-20/25	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed		
80486DX-33/50	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed		
80486DX2-50	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed		
80486DX2-66	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed		

CPU CLOCK GENERATOR CONFIGURATION				
Speed	J46 pins 1 & 2	J46 pins 3 & 4	J46 pins 5 & 6	J46 pins 7 & 8
33MHz	Closed	Open	Open	Open
40MHz	Closed	Open	Closed	Closed
40MHz	Closed	Closed	Open	Open
50MHz	Open	Open	Closed	Closed
50MHz	Open	Closed	Open	Open
66MHz	Open	Closed	Open	Closed
66MHz	Closed	Open	Open	Closed
80MHz	Closed	Closed	Open	Closed
Note: If an oscillator is installed, these jumper settings have no effect.				

PARALLEL PORT CONFIGURATION			
Setting	JE	JF	
Disabled	pins 1 & 2 closed	pins 1 & 2 closed	
378	pins 1 & 2 closed	pins 2 & 3 closed	
278	pins 2 & 3 closed	pins 1 & 2 closed	
3BC	pins 2 & 3 closed	pins 2 & 3 closed	

SERIAL PORT 1 CONFIGURATION				
Setting	JA	JB		
Disabled	pins 1 & 2 closed	pins 1 & 2 closed		
3E8	pins 1 & 2 closed	pins 2 & 3 closed		
3F8	pins 2 & 3 closed	pins 1 & 2 closed		
2E8	pins 2 & 3 closed	pins 2 & 3 closed		

SERIAL PORT 2 CONFIGURATION		
Setting	JC	JD
Disabled	pins 1 & 2 closed	pins 1 & 2 closed
2E8	pins 1 & 2 closed	pins 2 & 3 closed
2F8	pins 2 & 3 closed	pins 1 & 2 closed
3E8	pins 2 & 3 closed	pins 2 & 3 closed