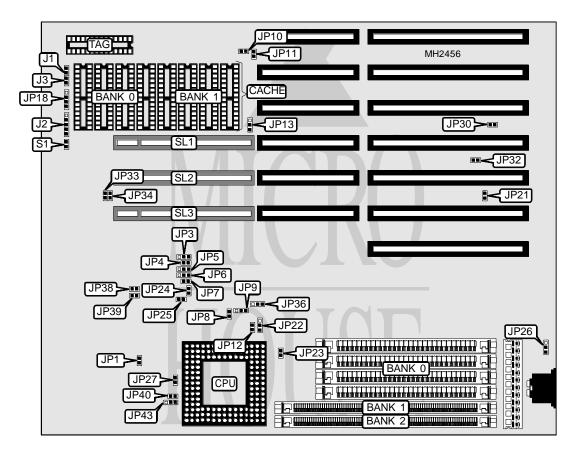
Processor	80486SX/SL80486SX/CX486M7/80486DX/SL80486DX/80486DX2/80486DX4/P24D/Pentiu m Overdrive
Processor Speed	25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	64MB
Cache	64/128/256КВ
BIOS	AMI
Dimensions	254mm x 220mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None



CONNECTIONS					
Purpose	Location	Purpose	Location		
Turbo switch	J1	Green PC connector	JP21		
Power LED & keylock	J2	Green PC connector	JP32		
Turbo LED	J3	Reset switch	S1		
Speaker	JP18	32-bit VESA local bus slots	SL1 - SL3		

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í Battery type select internal	JP26	pins 2 & 3 closed		
CMOS memory clear	JP26	pins 1 & 2 closed		
í Monitor type select color	JP30	Closed		
Monitor type select monochrome	JP30	Open		
í Factory configured - do not alter JP44 pins 2 & 3 closed				
í Factory configured - do not alter JP46 pins 2 & 3 closed				
Note: The locations of JP44 & JP46 are unknown.				

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2		
1MB	(4) 256K x 9	NONE	NONE		
2MB	NONE	(1) 512K x 36	NONE		
4MB	(4) 1M x 9	NONE	NONE		
4MB	NONE	(1) 1M x 36	NONE		
4MB	NONE	(1) 512K x 36	(1) 512K x 36		
6MB	NONE	(1) 512K x 36	(1) 1M x 36		
8MB	(4) 1M x 9	NONE	(1) 1M x 36		
8MB	NONE	(1) 2M x 36	NONE		
8MB	NONE	(1) 1M x 36	(1) 1M x 36		
10MB	NONE	(1) 512K x 36	(1) 2M x 36		
12MB	(4) 1M x 9	NONE	(1) 2M x 36		
12MB	NONE	(1) 1M x 36	(1) 2M x 36		
16MB	(4) 4M x 9	NONE	NONE		
16MB	NONE	(1) 4M x 36	NONE		
16MB	NONE	(1) 2M x 36	(1) 2M x 36		
20MB	(4) 1M x 9	NONE	(1) 4M x 36		
20MB	NONE	(1) 1M x 36	(1) 4M x 36		
32MB	(4) 4M x 9	NONE	(1) 4M x 36		
32MB	NONE	(1) 4M x 36	(1) 4M x 36		
32MB	NONE	(1) 8M x 36	NONE		
64MB	NONE	(1) 8M x 36	(1) 8M x 36		

CACHE CONFIGURATION					
Size	Bank 0	Bank 1	TAG		
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8		
128KB	(4) 32K x 8	NONE	(1) 8K x 8		
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8		

CACHE JUMPER CONFIGURATION						
Size	JP10	JP11	JP13	JP24	JP25	JP36
64KB	Open	Open	2&3	Closed	Closed	2&3
128KB Open Closed 1 & 2 Open Open Open						
256KB Closed Closed 2 & 3 Closed Closed 2 & 3						
Note: Pins designated should be in the closed position.						

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CPU TYPE CONFIGURATION						
Туре	JP3	JP4	JP5	JP6	JP7	JP8
80486SX	Open	Open	Open	1 & 2	Open	2&3
SL80486SX	Open	Open	Open	1 & 2	Open	2&3
CX486M7 (1x)	1&2	Open	2&3	2&3	Open	1&2
CX486M7 (2x)	1&2	Open	2&3	2&3	Open	1&2
80486DX	1&2	Open	2&3	1 & 2	Open	1&2
SL80486DX	1&2	Open	2&3	1&2	Open	1&2
80486DX2	1&2	Open	2&3	1 & 2	Open	1&2
80486DX4	1&2	Open	2&3	1 & 2	Open	1&2
P24D (WB)	1&2	Open	2&3	1 & 2	1 & 2	1&2
P24D (WT)	1&2	Open	2&3	1&2	2&3	1&2
P24T	1&2	Closed	2 & 3	1 & 2	Open	1&2
Note: Pins design	ated should be i	n the closed pos	ition.			

CPU TYPE CONFIGURATION (CON'T)							
Туре	JP9	JP12	JP22	JP23	JP27	JP40	JP43
80486SX	Open	Open	1&2	Open	Open	Open	Open
SL80486SX	Open	Open	1&2	Open	Open	Open	Open
CX486M7 (1x)	1&2	Open	1&2	Open	1&2	Open	Open
CX486M7 (2x)	1&2	Open	1&2	Open	2&3	Open	Open
80486DX	Open	Open	1&2	Open	Open	Open	Open
SL80486DX	Open	Open	1&2	Open	Open	Open	Open
80486DX2	Open	Open	1&2	Open	Open	Open	Open
80486DX4	Open	Open	1&2	Open	Open	Closed	see below
P24D (WB)	2&3	Closed	1&2	Closed	Open	Open	Open
P24D (WT)	2&3	Closed	1&2	Closed	Open	Open	Open
P24T	2&3	Open	1&2	Open	Open	Open	Open
Note: Pins designated should be in the closed position.							

	CPU SPEED CONFIGURATION	
Speed	JP38	JP39
25MHz	Open	Closed
33MHz	Closed	Open
40MHz	Open	Open
50iMHz	Open	Closed
50MHz	Closed	Closed
66iMHz	Closed	Open
75iMHz	Open	Closed
100iMHz	Closed	Open

CPU CLOCK SPEED CONFIGURATION (80486DX4)			
Speed JP43			
2x	Open		
2.5x	pins 2 & 3 closed		
Зх	pins 1 & 2 closed		

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CPU VOLTAGE CONFIGURATION			
Voltage JP1			
3.3v	Open		
5v	Closed		

VESA WAIT STATE CONFIGURATION			
Wait states JP33			
0 wait states	Open		
1 wait state	Closed		

BUS SPEED CONFIGURATION	
CPU speed	JP34
<= 33MHz	Open
> 33MHz	Closed