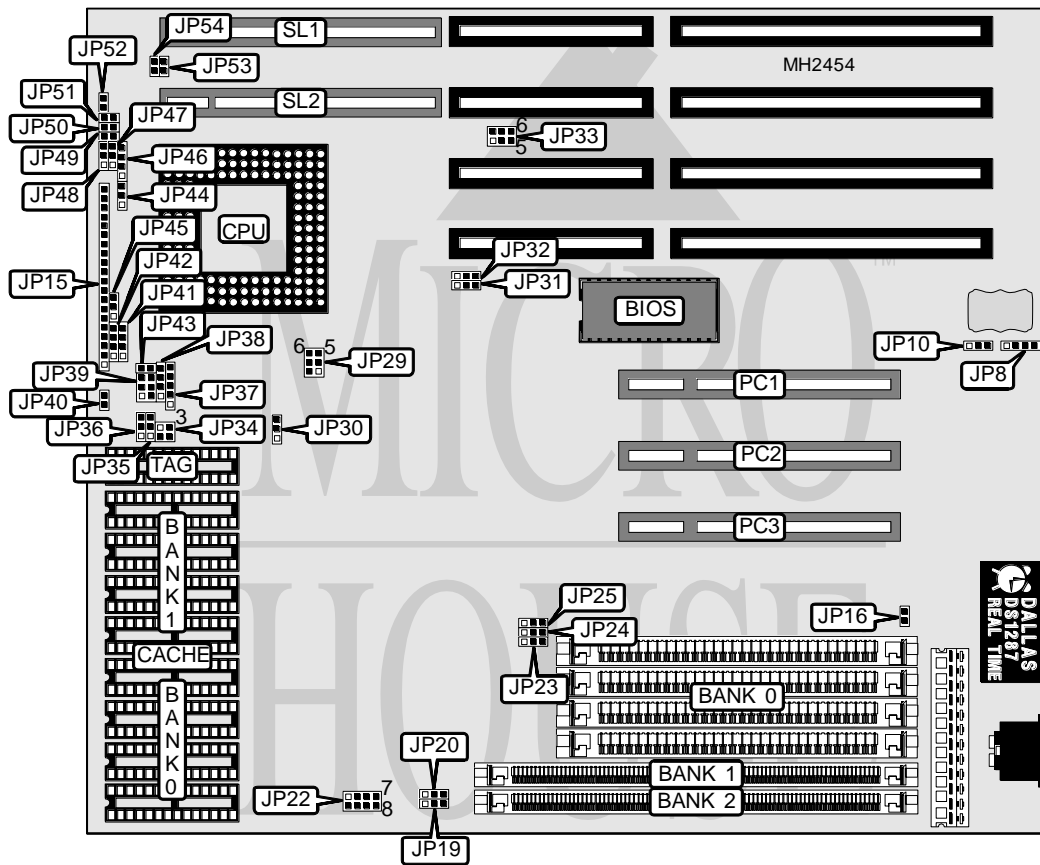


BIOSTAR MICROTECH INTERNATIONAL CORPORATION

MB-1425/1433/1440/1450UHT VER. 3 & ABOVE

Processor	CX486M6/80486SX/CX486M7/UMC486/80486DX/80486DX2/80486DX4/ Pentium Overdrive
Processor Speed	25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	128MB
Cache	128/256/512/1024KB
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2), 32-bit PCI slots (3)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP8	Turbo switch	JP15 (pins 14 - 15)
Speaker	JP15 (pins 1 - 4)	+5v ground	JP15 (pins 17 - 18)
Power LED & keylock	JP15 (pins 5 - 9)	32-bit PCI slots	PC1 - PC3
Turbo LED	JP15 (pins 10 - 11)	32-bit VESA local bus slots	SL1 & SL2
Reset switch	JP15 (pins 12 - 13)		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP8	Open
CMOS memory clear	JP8	pins 3 & 4 closed
Battery type select external	JP8	Closed
í Power good signal detect from power supply	JP10	pins 1 & 2 closed
Power good signal detect from board	JP10	pins 2 & 3 closed
í Password select normal	JP16	Open
Password select clear	JP16	Closed
í Factory configured - do not alter	JP23	Open
í Factory configured - do not alter	JP24	Open
í Factory configured - do not alter	JP25	Open
í Factory configured - do not alter	JP30	Open
í Factory configured - do not alter	JP31	Open
í Factory configured - do not alter	JP32	Open
í Hardware management select normal operation	JP40	Open
Hardware management select trigger for SMM mode	JP40	Closed
í Cyrix CPU clock speed select 2x	JP43	Open
Cyrix CPU clock speed select 1x	JP43	Closed
í CPU type select all	JP60	Open
CPU type select AMD 80486DX2-66/80 only	JP60	Closed

Note: The location of JP60 is unidentified.

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
2MB	(4) 512K x 9	NONE	NONE
2MB	NONE	(1) 512K x 36	NONE
2MB	NONE	NONE	(1) 512K x 36
4MB	(4) 1M x 9	NONE	NONE
4MB	NONE	(1) 512K x 36	(1) 512K x 36
4MB	NONE	(1) 1M x 36	NONE
4MB	NONE	NONE	NONE
6MB	(4) 1M x 9	(1) 512 x 36	NONE
6MB	(4) 1M x 9	NONE	(1) 512 x 36
8MB	(4) 1M x 9	(1) 512K x 36	(1) 512K x 36
8MB	(4) 1M x 9	(1) 1M x 36	NONE
8MB	(4) 1M x 9	NONE	(1) 1M x 36
8MB	NONE	(1) 1M x 36	(1) 1M x 36
8MB	NONE	(1) 2M x 36	NONE
8MB	NONE	NONE	(1) 2M x 36
16MB	(4) 4M x 9	NONE	NONE
16MB	NONE	(1) 2M x 36	(1) 2M x 36
16MB	NONE	(1) 4M x 36	NONE
16MB	NONE	NONE	(1) 4M x 36
24MB	(4) 4M x 9	(1) 1M x 36	(1) 1M x 36

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DRAM CONFIGURATION (CON'T)			
Size	Bank 0	Bank 1	Bank 2
24MB	(4) 4M x 9	(1) 2M x 36	NONE
24MB	(4) 4M x 9	NONE	(1) 2M x 36
32MB	(4) 4M x 9	(1) 2M x 36	(1) 2M x 36
32MB	(4) 4M x 9	(1) 4M x 36	NONE
32MB	(4) 4M x 9	NONE	(1) 4M x 36
32MB	(4) 8M x 9	NONE	NONE
32MB	NONE	(1) 4M x 36	(1) 4M x 36
32MB	NONE	(1) 8M x 36	NONE
32MB	NONE	NONE	(1) 8M x 36
64MB	(4) 16M x 9	NONE	NONE
64MB	NONE	(1) 8M x 36	(1) 8M x 36
128MB	(4) 16M x 9	(1) 8M x 36	(1) 8M x 36

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
512KB	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8	(1) 64K x 8

CACHE JUMPER CONFIGURATION				
Size	JP22	JP34	JP35	JP36
128KB	1 & 2	Open	1 & 2	2 & 3
256KB	1 & 2, 3 & 4	Open	2 & 3	1 & 2
512KB	1 & 2, 3 & 4, 5 & 6	3 & 4	2 & 3	1 & 2
1MB	1 & 2, 3 & 4, 5 & 6, 7 & 8	1 & 2, 3 & 4	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION		
Type	JP19	JP20
AMD	pins 2 & 3 closed	pins 1 & 2 closed
Cyrix	pins 1 & 2 closed	pins 2 & 3 closed
Intel	pins 1 & 2 closed	pins 1 & 2 closed

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CPU TYPE CONFIGURATION					
Type	JP37	JP38	JP39	JP41	JP42
CX486M6	1 & 2, 3 & 4	1 & 2, 3 & 4	3 & 4	2 & 3	2 & 3
AM486	Open	2 & 3	1 & 2	1 & 2	Open
80486SX	Open	2 & 3	1 & 2	Open	Open
CX486M7	1 & 2, 3 & 4	1 & 2, 3 & 4	3 & 4	2 & 3	2 & 3
UMC486	Open	2 & 3	1 & 2	1 & 2	Open
SL80486	1 & 2, 3 & 4	1 & 2, 3 & 4	3 & 4	2 & 3	2 & 3
80486DX	Open	1 & 2, 3 & 4	1 & 2	2 & 3	Open
80486DX2	Open	1 & 2, 3 & 4	1 & 2	2 & 3	Open
80486DX4	2 & 3, 4 & 5	1 & 2, 3 & 4	3 & 4	2 & 3	1 & 2, 3 & 4
Pentium Overdrive	2 & 3, 4 & 5	1 & 2, 3 & 4	3 & 4, 5 & 6	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION				
Type	JP44	JP45	JP46	JP47
CX486M6	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
AM486	Open	pins 2 & 3 closed	pins 3 & 4 closed	Open
80486SX	Open	Open	Open	Open
CX486M7	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
UMC486	Open	pins 2 & 3 closed	pins 3 & 4 closed	Open
SL80486	Open	pins 1 & 2 closed	Open	Open
80486DX	Open	Open	Open	Open
80486DX2	Open	Open	Open	Open
80486DX4	pins 3 & 4 closed	pins 1 & 2 closed	Open	Open
Pentium Overdrive	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed

CPU SPEED CONFIGURATION	
Speed	JP33
25MHz	pins 1 & 2 closed
33MHz	pins 1 & 2, 3 & 4, 5 & 6 closed
40MHz	pins 1 & 2, 3 & 4 closed
50iMHz	pins 1 & 2 closed
50MHz	pins 5 & 6 closed
66iMHz	pins 1 & 2, 3 & 4, 5 & 6 closed
75iMHz	pins 1 & 2 closed
100iMHz	pins 1 & 2, 3 & 4, 5 & 6 closed

CPU CLOCK CONFIGURATION	
Speed	JP48
2x	pins 2 & 3 closed
2.5x	pins 1 & 2 closed
3x	Open

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CPU VOLTAGE CONFIGURATION					
Voltage	JP29	JP49	JP50	JP51	JP52
3.3v	Open	Open	Open	Closed	Closed
5v	pins 1 & 2, 3 & 4 closed	Closed	Closed	Open	Open

VESA WAIT STATE CONFIGURATION	
Wait states	JP53
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	JP54
<= 33MHz	Open
> 33MHz	Closed