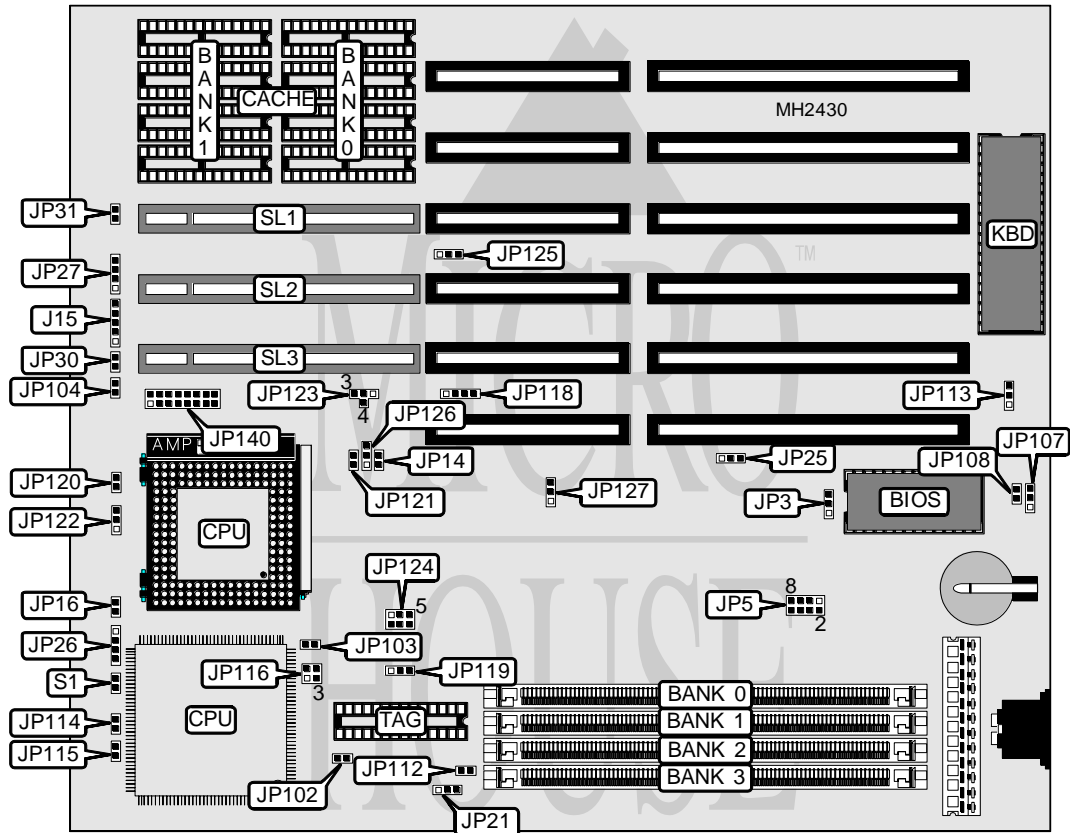


SEANIX TECHNOLOGY, INC. S 8 9 5 (V E R . 1 . 0)

Processor	80486SX/80486DX/80486DX2/80486DX4/Pentium Overdrive
Processor Speed	25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	128MB
Cache	128/256KB
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (3)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J15	Chassis fan power	JP118
Speaker	JP27	PAL socket for VL master option	JP140
Turbo switch	JP114	Reset switch	S1
Turbo LED	JP115	32-bit VESA local bus slots	SL1 - SL3

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Flash BIOS write protect enabled	JP3	pins 1 & 2 closed
Flash BIOS write protect disabled	JP3	pins 2 & 3 closed
í Factory configured - do not alter	JP5	pins 1 & 2 closed
í Cache type select write through	JP14	Open
Cache type select write back	JP14	Closed
í Pentium Overdrive level 1 cache select burst write	JP16	Closed
Pentium Overdrive level 1 cache select disable blen#	JP16	Open
í Factory configured - do not alter	JP21	pins 2 & 3 closed
í Programming voltage select +12v	JP107	pins 1 & 2 closed
Programming voltage select +5v	JP107	pins 2 & 3 closed
í 1Mbit flash select 512KB bit file at 00000	JP108	Closed
1Mbit flash select 512KB bit file at 10000	JP108	Open
í Factory configured - do not alter	JP112	Open
í Factory configured - do not alter	JP113	Open
í CPU type select Intel/AMD	JP119	pins 1 & 2 closed
CPU type select Cyrix M7	JP119	pins 2 & 3 closed
í CPU type select Intel/AMD	JP120	Open
CPU type select Cyrix 2X	JP120	Closed
í Factory configured - do not alter	JP121	Open
í Factory configured - do not alter	JP122	Open
í Factory configured - do not alter	JP123	Open
í Factory configured - do not alter	JP127	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 512K x 36	NONE	NONE	NONE
4MB	(1) 512K x 36	(1) 512K x 36	NONE	NONE
4MB	(1) 1M x 36	NONE	NONE	NONE
6MB	(1) 512K x 36	(1) 512K x 36	(1) 256K x 36	(1) 256K x 36
6MB	(1) 512K x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 2M x 36	NONE	NONE	NONE
10MB	(1) 512K x 36	(1) 2M x 36	NONE	NONE
12MB	(1) 512K x 36	(1) 512K x 36	(1) 1M x 36	(1) 1M x 36
14MB	(1) 512K x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	(1) 2M x 36	NONE	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	NONE	NONE	NONE
18MB	(1) 512K x 36	(1) 2M x 36	(1) 1M x 36	(1) 1M x 36
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
20MB	(1) 1M x 36	(1) 2M x 36	(1) 1M x 36	(1) 1M x 36
24MB	(1) 2M x 36	(1) 2M x 36	(1) 1M x 36	(1) 1M x 36
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	NONE
32MB	(1) 2M x 36	(1) 2M x 36	(1) 4M x 36	NONE

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DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
32MB	(1) 8M x 36	NONE	NONE	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	NONE
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
48MB	(1) 2M x 36	(1) 2M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	(1) 8M x 36	NONE	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 16M x 36	NONE	NONE	NONE
96MB	(1) 8M x 36	(1) 8M x 36	(1) 4M x 36	(1) 4M x 36
128MB	(1) 16M x 36	(1) 16M x 36	NONE	NONE

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION		
Size	JP125	JP126
128KB	pins 1 & 2 closed	pins 1 & 2 closed
256KB	pins 2 & 3 closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION	
Type	JP26
80486SX	pins 2 & 3 closed
80486DX	pins 1 & 2, 3 & 4 closed
80486DX2	pins 1 & 2, 3 & 4 closed
80486DX4	pins 1 & 2, 3 & 4 closed
Pentium Overdrive	pins 1 & 2, 3 & 4 closed

CPU TYPE CONFIGURATION		
Type	JP25	JP116
Non - SL enhanced CPU	Open	Open
SL enhanced CPU	Closed	pins 1 & 2, 3 & 4 closed

CPU SPEED CONFIGURATION	
Speed	JP124
25MHz	pins 1 & 2, 5 & 6 closed
33MHz	pins 1 & 2, 3 & 4 closed
40MHz	pins 5 & 6 closed
50iMHz	pins 1 & 2, 5 & 6 closed
50MHz	pins 3 & 4 closed
66iMHz	pins 1 & 2, 3 & 4 closed
75iMHz	pins 1 & 2, 5 & 6 closed
100iMHz	pins 1 & 2, 3 & 4 closed

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CPU VOLTAGE CONFIGURATION				
Voltage	JP101	JP102	JP103	JP104
3.45v	Open	Open	Open	Open
5v	Closed	Closed	Closed	Closed
Note: The location of JP101 is unidentified.				

VESA WAIT STATE CONFIGURATION	
Wait states	JP31
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	JP30
<= 33MHz	Open
> 33MHz	Closed