



**SOYO COMPUTER CO., LTD.**  
**486 P C I**

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome	JP3	Open
Monitor type select color	JP3	Closed
í CMOS memory normal operation	JP5	pins 1 & 2 closed
CMOS memory clear	JP5	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 512K x 36	NONE	NONE	NONE
4MB	(1) 1M x 36	NONE	NONE	NONE
4MB	(1) 512K x 36	(1) 512K x 36	NONE	NONE
6MB	(1) 512K x 36	(1) 1M x 36	NONE	NONE
6MB	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36	NONE
8MB	(1) 2M x 36	NONE	NONE	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36
10MB	(1) 512K x 36	(1) 1M x 36	NONE	NONE
12MB	(1) 1M x 36	(1) 2M x 36	NONE	NONE
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE
12MB	(1) 512K x 36	(1) 512K x 36	(1) 2M x 36	NONE
14MB	(1) 512K x 36	(1) 1M x 36	(1) 2M x 36	NONE
16MB	(1) 2M x 36	(1) 2M x 36	NONE	NONE
16MB	(1) 4M x 36	NONE	NONE	NONE
16MB	(1) 2M x 36	(1) 2M x 36	NONE	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
18MB	(1) 512K x 36	(1) 2M x 36	(1) 2M x 36	NONE
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
20MB	(1) 1M x 36	(1) 2M x 36	(1) 2M x 36	NONE
20MB	(1) 512K x 36	(1) 512K x 36	(1) 2M x 36	(1) 2M x 36
22MB	(1) 512K x 36	(1) 1M x 36	(1) 2M x 36	(1) 2M x 36
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	NONE
24MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	NONE
24MB	(1) 1M x 36	(1) 1M x 36	(1) 2M x 36	(1) 2M x 36
26MB	(1) 512K x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
28MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 1M x 36
28MB	(1) 1M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
32MB	(1) 8M x 36	NONE	NONE	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 2M x 36	(1) 2M x 36	(1) 4M x 36	NONE
32MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	NONE
40MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	NONE
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
48MB	(1) 2M x 36	(1) 2M x 36	(1) 8M x 36	NONE

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DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
48MB	(1) 2M x 36	(1) 2M x 36	(1) 4M x 36	(1) 4M x 36
52MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 16M x 36	NONE	NONE	NONE
64MB	(1) 8M x 36	(1) 8M x 36	NONE	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
68MB	(1) 1M x 36	(1) 8M x 36	(1) 4M x 36	(1) 4M x 36
72MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36
80MB	(1) 2M x 36	(1) 2M x 36	(1) 8M x 36	(1) 8M x 36
84MB	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36	(1) 4M x 36
96MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	NONE
96MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	(1) 8M x 36
100MB	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
128MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8 or (1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP22	JP24	JP27	JP30
128KB	Closed	Open	Open	pins 1 & 2 closed
256KB	Closed	Closed	Open	pins 2 & 3 closed
512KB	Closed	Closed	Closed	pins 1 & 2 closed

CPU TYPE CONFIGURATION							
Type	JP6	JP7	JP8	JP9	JP10	JP11	JP12
80486SX	Open	2 & 3	Open	Open	Open	Open	Open
CX486M7(sync)	1 & 2	Open	Open	Open	Open	Open	Open
CX486M7(async)	1 & 2	Open	Open	Open	Open	Open	Open
80486DX	Open	2 & 3	Open	Open	Open	Open	Open
SL80486DX	Open	1 & 2	Open	Open	Open	Open	Closed
80486DX2	Open	2 & 3	Open	Open	Open	Open	Open
SL80486DX2	Open	1 & 2	Open	Open	Open	Open	Closed
80486DX4	Open	1 & 2	Open	Open	Open	Open	Closed
P24D	2 & 3	1 & 2	Open	2 & 3	Closed	Closed	Closed
P24T	2 & 3	1 & 2	Open	1 & 2	Open	Open	Closed

Note: Pins designated should be in the closed position.

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CPU TYPE CONFIGURATION (CON'T)							
Type	JP13	JP14	JP15	JP16	JP17	JP18	JP20
80486SX	Open	Open	Open	Open	2 & 3	2 & 3	2 & 3
CX486M7(sync)	Open	Open	Closed	1 & 2	Open	2 & 3	1 & 2, 3 & 4
CX486M7(async)	Open	Open	Closed	1 & 2	1 & 2	2 & 3	1 & 2, 3 & 4
80486DX	Open	Open	Open	Open	1 & 2	2 & 3	1 & 2, 3 & 4
SL80486DX	Closed	Closed	Open	Open	1 & 2	1 & 2	1 & 2, 3 & 4
80486DX2	Open	Open	Open	Open	1 & 2	2 & 3	1 & 2, 3 & 4
SL80486DX2	Open	Open	Open	Open	1 & 2	2 & 3	1 & 2, 3 & 4
80486DX4	Closed	Closed	Open	Open	1 & 2	1 & 2	1 & 2, 3 & 4
P24D	Closed	Closed	Open	Open	1 & 2	1 & 2	1 & 2, 3 & 4
P24T	Closed	Closed	Open	Open	1 & 2	1 & 2	1 & 2, 3 & 4

Note: Pins designated should be in the closed position.

CPU SPEED CONFIGURATION			
Speed	JP34	JP36	JP37
25MHz	Open	Open	Open
33MHz	Open	Closed	Closed
40MHz	Open	Closed	Open
50iMHz	Open	Open	Open
50MHz	Open	Open	Closed
66iMHz	Open	Closed	Closed
75iMHz	Open	Open	Open
80iMHz	Open	Closed	Open
100iMHz	Open	Closed	Closed

CPU VOLTAGE CONFIGURATION	
Voltage	JP21
3.3v	pins 1 & 2, 4 & 5, 7 & 8 closed
5.0v	pins 2 & 3, 5 & 6, 8 & 9 closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP2
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	JP1
<= 33MHz	Open
> 33MHz	Closed

PCI BUS CLOCK CONFIGURATION		
Mode	JP31	JP32
Asynchronous	pins 2 & 3 closed	pins 2 & 3 closed
Synchronous	pins 1 & 2 closed	pins 1 & 2 closed