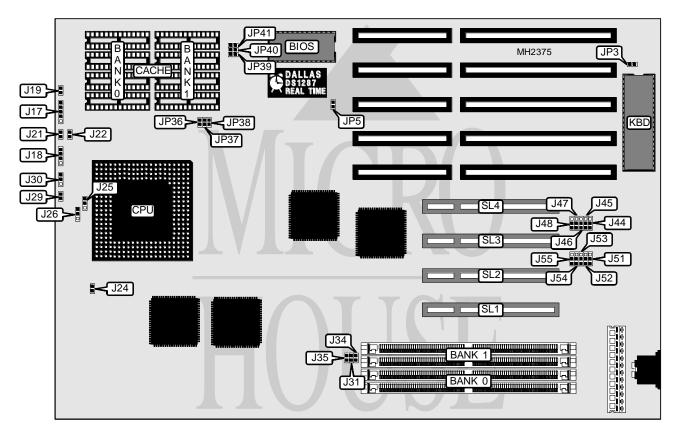
Processor Processor Speed Chip Set Max. Onboard DRAM Cache BIOS Dimensions I/O Options NPU Options Pentium 90/100MHz Unidentified 512MB 256/512KB Award 330mm x 218mm 32-bit PCI bus slots (4), green PC connector None



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J17	Green PC connector	J21
Speaker	J18	Green PC LED	J22
Reset switch	J19	32-bit PCI bus slots	SL1 - SL4

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Ppeline address disabled	J24	Open
Pipeline address enabled	J24	Closed
í Cache type select write back	J29	Open
Cache type select write through	J29	Closed
í Monitor type select color	JP3	Closed
Monitor type select monochrome	JP3	Open
í CMOS memory normal operation	JP5	Open
CMOS memory clear	JP5	Closed
í Factory configured - do not alter	JP36	Closed
í Factory configured - do not alter	JP37	Closed
í Factory configured - do not alter	JP38	Closed
í Factory configured - do not alter	JP39	Open
í Factory configured - do not alter	JP40	Open
í Factory configured - do not alter	JP41	Open

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	NONE
4MB	(2) 256K x 36	(2) 256K x 36
4MB	(2) 512K x 36	NONE
8MB	(2) 512K x 36	(2) 512K x 36
8MB	(2) 1M x 36	NONE
10MB	(2) 256K x 36	(2) 1M x 36
12MB	(2) 512K x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
16MB	(2) 1M x 36	(2) 1M x 36
20MB	(2) 512K x 36	(2) 2M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
32MB	(2) 2M x 36	(2) 2M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE
64MB	(2) 4M x 36	(2) 4M x 36
68MB	(2) 512K x 36	(2) 8M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	NONE
256MB	(2) 16M x 36	(2) 16M x 36
256MB	(2) 32M x 36	NONE
512MB	(2) 32M x 36	(2) 32M x 36

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CACHE CONFIGURATION		
Size	Bank 0	Bank 1
256KB	(4) 32K x 8	(4) 32K x 8
512KB	(4) 64K x 8	(4) 64K x 8

CACHE JUMPER CONFIGURATION			
Size	J25	J26	
256KB	pins 2 & 3 closed	pins 1 & 2 closed	
512KB	pins 2 & 3 closed	pins 2 & 3 closed	

CPU SPEED CONFIGURATION			
Speed	JP31	JP34	JP35
90MHz	Closed	Open	Open
100MHz	Open	Closed	Closed

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	PCI IRQ CONFIGURATION (IRQ 10)	
PIRQ	J44	J51
PIRQ 0	Open	pins 1 & 2 closed
PIRQ 1	Open	pins 2 & 3 closed
PIRQ 2	pins 1 & 2 closed	Open
PIRQ 3	pins 2 & 3 closed	Open

	PCI IRQ CONFIGURATION (IRQ 11)	
PIRQ	J45	J52
PIRQ 0	Open	pins 1 & 2 closed
PIRQ 1	Open	pins 2 & 3 closed
PIRQ 2	pins 1 & 2 closed	Open
PIRQ 3	pins 2 & 3 closed	Open

	PCI IRQ CONFIGURATION (IRQ 12)	
PIRQ	J46	J53
PIRQ 0	Open	pins 1 & 2 closed
PIRQ 1	Open	pins 2 & 3 closed
PIRQ 2	pins 1 & 2 closed	Open
PIRQ 3	pins 2 & 3 closed	Open

PCI IRQ CONFIGURATION (IRQ 14)		
PIRQ	J47	J54
PIRQ 0	Open	pins 1 & 2 closed
PIRQ 1	Open	pins 2 & 3 closed
PIRQ 2	pins 1 & 2 closed	Open
PIRQ 3	pins 2 & 3 closed	Open

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	PCI IRQ CONFIGURATION (IRQ 15)	
PIRQ	J48	J55
PIRQ 0	Open	pins 1 & 2 closed
PIRQ 1	Open	pins 2 & 3 closed
PIRQ 2	pins 1 & 2 closed	Open
PIRQ 3	pins 2 & 3 closed	Open

HOST BUS PREQUENCY CONFIGURATION		
Setting J30		
Full speed	Open	
2/3 speed	pins 1 & 2 closed	
1/2 speed	pins 2 & 3 closed	