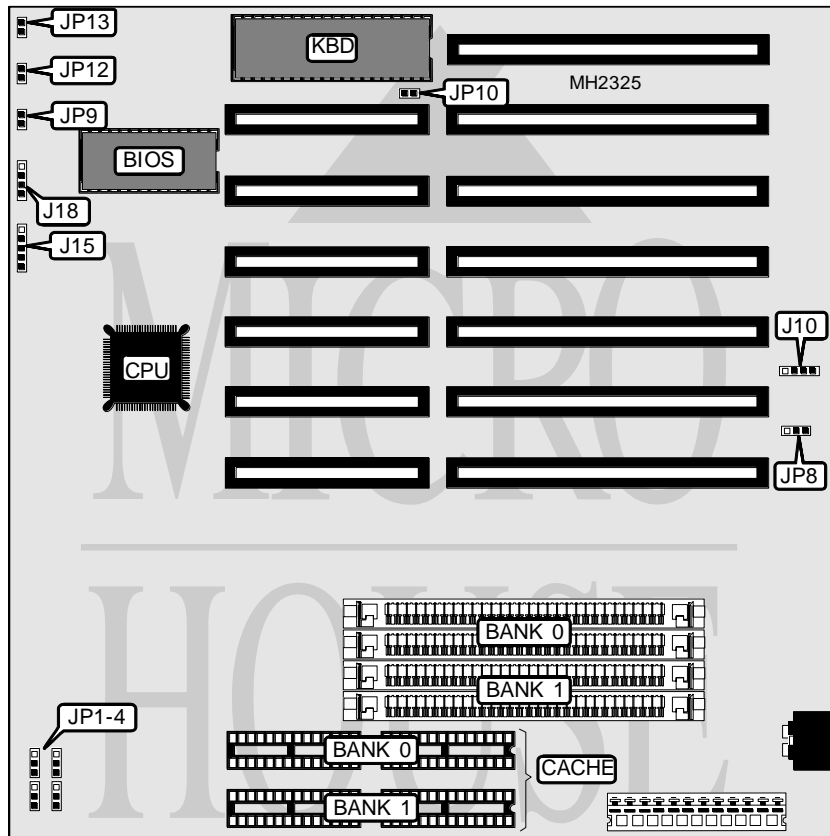


UNIDENTIFIED A - B 333SX

Processor	AM386SX/80386SX
Processor Speed	25/33MHz
Chip Set	Unidentified
Max. Onboard DRAM	16MB
Cache	16/32/64/128KB
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387SX (exact location unidentified)



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J10	Turbo switch	JP9
Power LED & keylock	J15	Turbo LED	JP12
Speaker	J18	Reset switch	JP13

Continued on next page. . .

UNIDENTIFIED

A - B 333SX

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation (internal battery)	JP8	pins 2 & 3 closed
CMOS memory normal operation (external battery)	JP8	Open
CMOS memory clear	JP8	pins 1 & 2 closed
í Monitor type select color	JP10	Closed
Monitor type select monochrome	JP10	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
2.5MB	(2) 1M x 9	(2) 256K x 9
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
16KB	(2) 8K x 8	NONE
32KB	(2) 8K x 8	(2) 8K x 8
64KB	(2) 32K x 8	NONE
128KB	(2) 32K x 8	(2) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP1	JP2	JP3	JP4
16KB	pins 2 & 3 closed	Open	Open	pins 2 & 3 closed
32KB	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
64KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
128KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed

Note: Jumper location is approximated, check board for actual location.