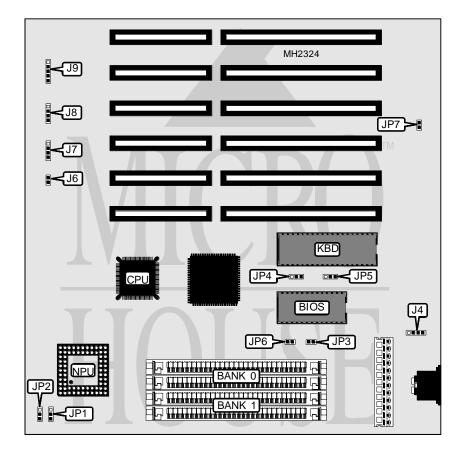
## UNIDENTIFIED A-B 340SX / A-B 341SX

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
I/O Options
NPU Options

AM386SX/80386SX 25/33/40MHz ALI 16MB None AMI 220mm x 170mm None 80387SX



CONNECTIONS				
Purpose	Location	Purpose	Location	
External battery	J4	Turbo switch	J8	
Reset switch	JG	Turbo LED	J8	
Speaker	J7	Power LED & keylock	J9	

Continued on next page. . .

## UNIDENTIFIED A-B 340SX / A-B 341SX

... continued from previous page

USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í NPU synchronous with CPU	JP2	pins 1 & 2 closed		
NPU asynchronous with CPU	JP2	pins 2 & 3 closed		
í Factory configured - do not alter	JP3	Open		
í Monitor type select color	JP4	pins 2 & 3 closed		
Monitor type select monochrome	JP4	pins 1 & 2 closed		
í Parity check enabled	JP6	Open		
Parity check disabled	JP6	Closed		
í Factory configured - do not alter	JP7	Open		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
512KB	(2) 256K x 9	NONE		
1MB	(2) 256K x 9	(2) 256K x 9		
2MB	(2) 1M x 9	NONE		
4MB	(2) 1M x 9	(2) 1M x 9		
8MB	(2) 4M x 9	NONE		
16MB	(2) 4M x 9	(2) 4M x 9		

	BUS SPEED SELECT	
CPU Speed	JP1	JP5
25MHz	pins 1 & 2 closed	pins 2 & 3 closed
33MHz	pins 2 & 3 closed	pins 2 & 3 closed
40MHz	pins 1 & 2 closed	pins 1 & 2 closed