OGIVAR, INC.
INTERSYSTEM 386/25

Processor	80386DX
Processor Speed	25MHz
Chip Set	Unidentified
Max. Onboard DRAM	16MB (on external memory card)
Cache	None
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card (2)
NPU Options	80287/80387DX
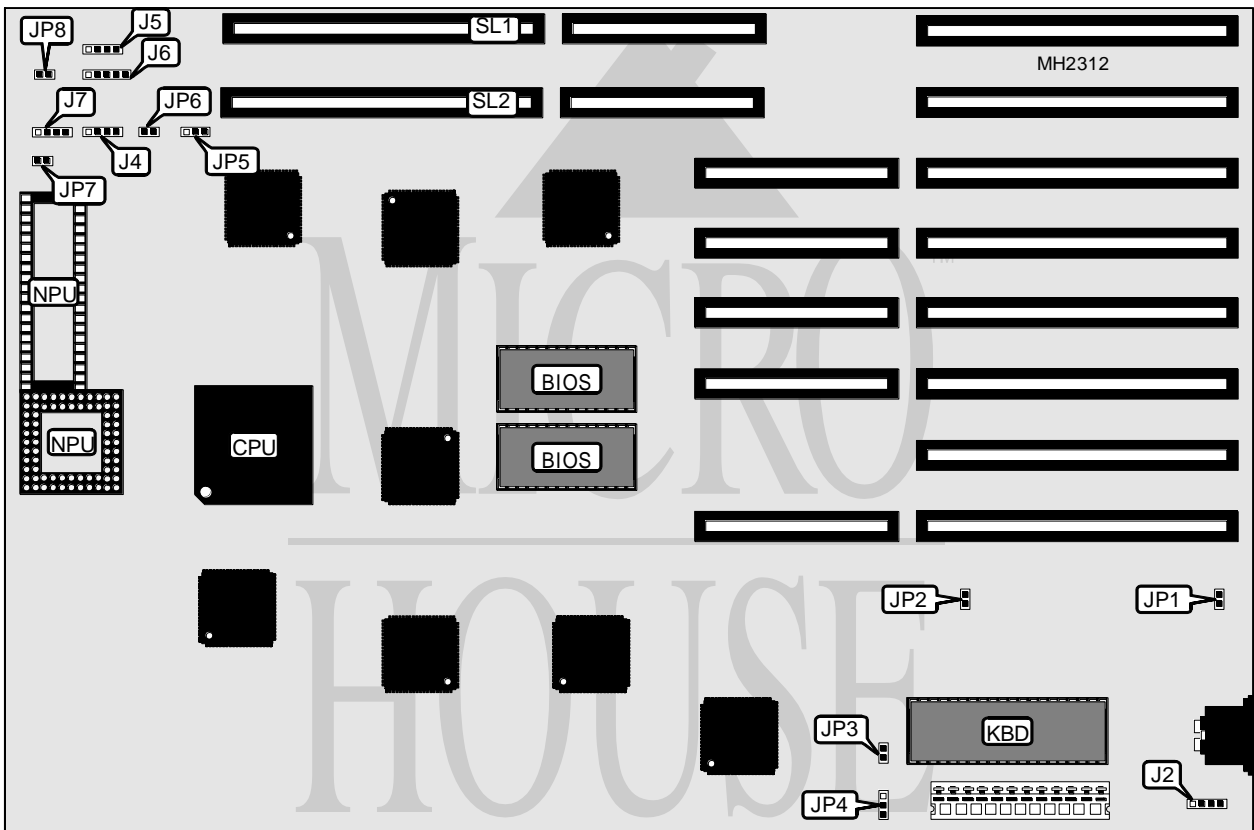
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Reset switch	JP3 or JP8
Speaker	J5	32-bit external memory card	SL1 & SL2
Power LED & keylock	J6		

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J4	N/A
í Factory configured - do not alter	J7	N/A
í Battery type select external	JP1	Open
Battery type select internal	JP1	Closed
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open
í Power good signal detect from power supply	JP4	pins 2 & 3 closed
Power good signal detect from board	JP4	pins 1 & 2 closed
í NPU synchronous with CPU	JP5	pins 1 & 2 closed
NPU asynchronous with CPU	JP5	pins 1 & 2 closed

DRAM CONFIGURATION (EXTERNAL MEMORY CARD)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE	NONE
6MB	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9

NPU TYPE SELECT		
Type	JP6	JP7
í None	Closed	Open
80287	Closed	Open
80387	Open	Closed
Note: Exact location of JP6 & JP7 is approximate. Both jumpers are labeled on the board.		