LEADING TECHNOLOGY
1 2 / 2 8 6 M X

Processor	80286
Processor Speed	12MHz
Chip Set	Unidentified
Max. Onboard DRAM	2MB
Cache	None
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80287 (exact location unidentified)
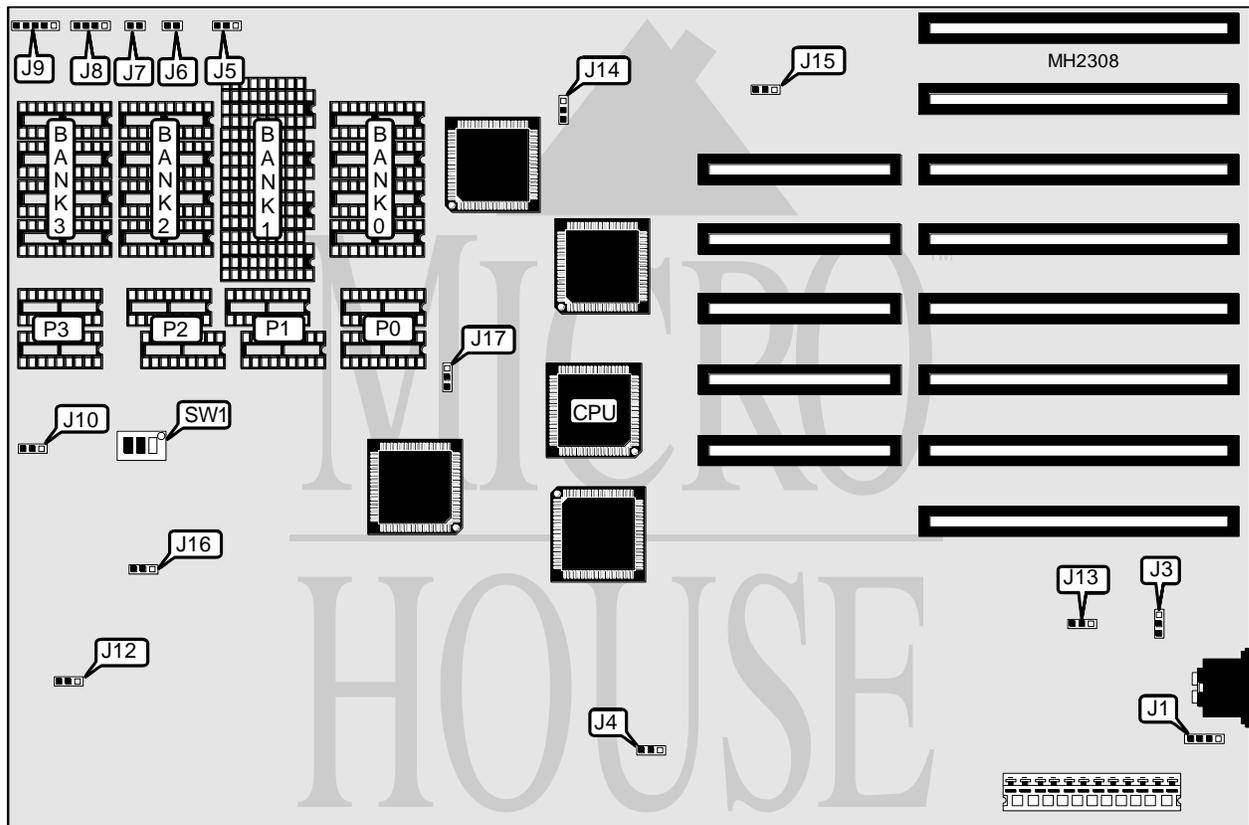
CONNECTIONS			
Purpose	Location	Purpose	Location
External Battery	J1	Speaker	J8
Turbo LED	J6	Power LED & keylock	J9
Reset switch	J7		

LEADING TECHNOLOGY
1 2 / 2 8 6 M X

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	J3	pins 2 & 3 closed
Monitor type select monochrome	J3	pins 1 & 2 closed
í Parity check disabled	J4	pins 1 & 2 closed
Parity check enabled	J4	pins 2 & 3 closed
í Turbo enabled	J5	pins 1 & 2 closed
Turbo disabled	J5	pins 2 & 3 closed
í Wait state select 0 wait state	J10	pins 1 & 2 closed
Wait state select 1 wait state	J10	pins 2 & 3 closed
í Factory configured - do not alter	J12	N/A
í Reset select power good	J13	pins 1 & 2 closed
Reset select RC circuit	J13	pins 2 & 3 closed
í BIOS type select 64K	J14	pins 1 & 2 closed
BIOS type select 32K	J14	pins 2 & 3 closed
í Factory configured - do not alter	J15	Open
í Factory configured - do not alter	J16	N/A
í NPU speed select 24MHz	J17	pins 2 & 3 closed
NPU speed select 12MHz	J17	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
640KB	(4) 44256	(4) 4464	NONE	NONE
1MB	(4) 44256	(4) 44256	NONE	NONE
1152KB	(4) 44256	(4) 4464	(4) 44256	NONE
1536KB	(4) 44256	(4) 44256	(4) 44256	NONE
1664KB	(4) 44256	(4) 4464	(4) 44256	(4) 44256
2MB	(4) 44256	(4) 44256	(4) 44256	(4) 44256

DRAM PARITY CONFIGURATION				
Size	P0	P1	P2	P3
640KB	(2) 41256	(2) 4164	NONE	NONE
1MB	(2) 41256	(2) 41256	NONE	NONE
1152KB	(2) 41256	(2) 4164	(2) 41256	NONE
1536KB	(2) 41256	(2) 41256	(2) 41256	NONE
1664KB	(2) 41256	(2) 4164	(2) 41256	(2) 41256
2MB	(2) 41256	(2) 41256	(2) 41256	(2) 41256

LEADING TECHNOLOGY
12/286MX

DRAM SWITCH CONFIGURATION			
Size	SW1/1	SW1/2	SW1/3
640KB	On	On	On
1MB	On	Off	Off
1152KB	On	On	Off
1536KB	Off	On	On
1664KB	On	Off	On
2MB	Off	On	Off