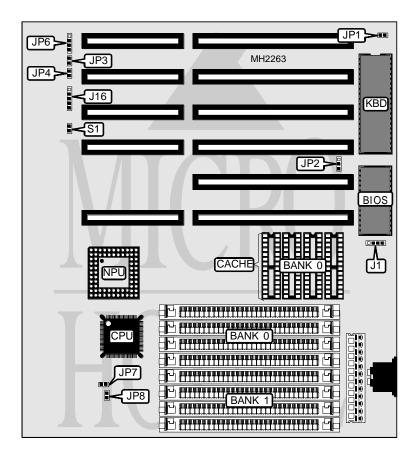
UNIDENTIFIED 386DX CACHE MAINBOARD

Processor Processor Speed Chip Set Max. Onboard DRAM Cache BIOS Dimensions I/O Options NPU Options 80386DX 33/40MHz OPTI 32MB 128KB AMI 220mm x 170mm None 80387DX



| CONNECTIONS | | | | |
|---------------------|----------|--------------|----------|--|
| Purpose | Location | Purpose | Location | |
| External battery | J1 | Turbo switch | JP4 | |
| Power LED & keylock | J16 | Speaker | JP6 | |
| Turbo LED | JP3 | Reset switch | S1 | |

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| USER CONFIGURABLE SETTINGS | | | | |
|----------------------------------|--------|-------------------|--|--|
| Function | Jumper | Position | | |
| í Monitor type select monochrome | JP1 | Open | | |
| Monitor type select color | JP1 | Closed | | |
| í CMOS memory normal operation | JP2 | pins 1 & 2 closed | | |
| CMOS memory clear | JP2 | pins 2 & 3 closed | | |

| DRAM CONFIGURATION | | | | |
|--------------------|--------------|--------------|--|--|
| Size | Bank 0 | Bank 1 | | |
| 1MB | (4) 256K x 9 | NONE | | |
| 2MB | (4) 256K x 9 | (4) 256K x 9 | | |
| 4MB | (4) 1M x 9 | NONE | | |
| 8MB | (4) 1M x 9 | (4) 1M x 9 | | |
| 16MB | (4) 4M x 9 | NONE | | |
| 32MB | (4) 4M x 9 | (4) 4M x 9 | | |

| CACHE CONFIGURATION | | | |
|---------------------|-------------|--|--|
| Size | Bank 0 | | |
| 128KB | (4) 32K x 8 | | |

| CPU SPEED CONFIGURATION | | | | |
|-------------------------|--------|--------|--|--|
| Speed | JP7 | JP8 | | |
| 50MHz | Closed | Open | | |
| 66MHz | Open | Closed | | |
| 80MHz | Open | Open | | |