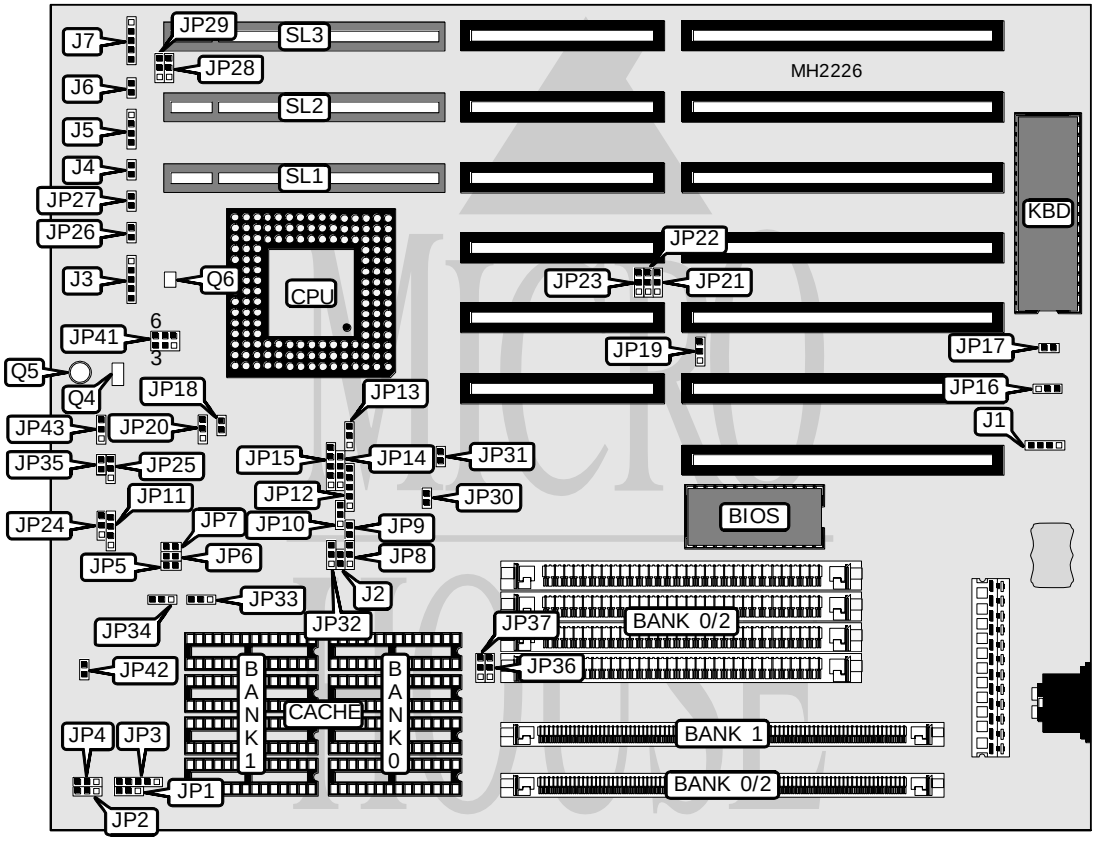
UNIDENTIFIED
SIS 486-471 VL BUS 5/3.3V

Processor	CX486S/80486SX/CX486DX/80486DX/80486DX2/80486DX4/Pentium Overdrive
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	128MB
Cache	64/128/256/512KB
BIOS	AMI
Dimensions	254mm x 220mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Reset switch	J6
Green PC connector	J2	Power LED & keylock	J7
Turbo switch	J3	Break switch connector	JP26
Turbo LED	J4	32-bit VESA local bus slots	SL1 - SL3
Speaker	J5		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Turbo switch enabled	J3	pins 1 & 2 closed
Turbo switch disabled	J3	pins 2 & 3 closed
Use for break switch	J3	pins 4 & 5 closed
í CMOS memory normal operation	JP16	pins 1 & 2 closed
CMOS memory clear	JP16	pins 2 & 3 closed
í Monitor type select VGA/EGA/monochrome	JP17	Open
Monitor type select CGA	JP17	Closed
í CX486S2 CPU not installed	JP18	Open
CX486S2 CPU installed	JP18	Closed
í RC pin for break switch	JP19	pins 1 & 2 closed
RC pin for RC	JP19	pins 2 & 3 closed
í Factory configured - do not alter	JP20	pins 2 & 3 closed
í Factory configured - do not alter	JP23	pins 1 & 2 closed
í J3 set with pins open	JP27	Closed
J3 set with pins 4 & 5 closed	JP27	Open
í Factory configured - do not alter	JP43	pins 1 & 2 closed

DRAM CONFIGURATION (TABLE 1)			
Size	Bank 0	Bank 1	Bank 2
1MB	(4) 256K x 9	NONE	NONE
2MB	(4) 256K x 9	(1) 256K x 36	NONE
4MB	(4) 1M x 9	NONE	NONE
5MB	(4) 256K x 9	(1) 1M x 36	NONE
6MB	(4) 256K x 9	(1) 256K x 36	(1) 1M x 36
8MB	(4) 1M x 9	(1) 1M x 36	NONE
12MB	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36
16MB	(4) 4M x 9	NONE	NONE
17MB	(4) 256K x 9	(1) 4M x 36	NONE
18MB	(4) 256K x 9	(1) 256K x 36	(1) 4M x 36
20MB	(4) 1M x 9	(1) 4M x 36	NONE
24MB	(4) 1M x 9	(1) 1M x 36	(1) 4M x 36
32MB	(4) 4M x 9	(1) 4M x 36	NONE
34MB	(4) 256K x 9	(1) 256K x 36	(1) 8M x 36
36MB	(4) 1M x 9	(1) 4M x 36	(1) 4M x 36
48MB	(4) 4M x 9	(1) 8M x 36	NONE
48MB	(4) 4M x 9	(1) 8M x 36	NONE
52MB	(4) 1M x 9	(1) 4M x 36	(1) 8M x 36
64MB	(4) 4M x 9	(1) 4M x 36	(1) 8M x 36
64MB	(4) 16M x 9	NONE	NONE
68MB	(4) 1M x 9	(1) 8M x 36	(1) 8M x 36
80MB	(4) 4M x 9	(1) 8M x 36	(1) 8M x 36
96MB	(4) 16M x 9	(1) 8M x 36	NONE
128MB	(4) 16M x 9	(1) 8M x 36	(1) 8M x 36

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DRAM CONFIGURATION (TABLE 2)			
Size	Bank 0	Bank 1	Bank 2
1MB	(1) 256K x 36	NONE	NONE
2MB	(1) 256K x 36	(1) 256K x 36	NONE
4MB	(1) 1M x 36	NONE	NONE
5MB	(1) 256K x 36	(1) 1M x 36	NONE
6MB	(1) 256K x 36	(1) 256K x 36	(4) 1M x 9
8MB	(1) 2M x 36	NONE	NONE
12MB	(1) 1M x 36	(1) 1M x 36	(4) 1M x 9
16MB	(1) 2M x 36	(1) 2M x 36	NONE
16MB	(1) 4M x 36	NONE	NONE
17MB	(1) 256K x 36	(1) 4M x 36	NONE
18MB	(1) 256K x 36	(1) 256K x 36	(4) 4M x 9
20MB	(1) 1M x 36	(1) 4M x 36	NONE
24MB	(1) 1M x 36	(1) 1M x 36	(4) 4M x 9
32MB	(1) 4M x 36	(1) 4M x 36	NONE
32MB	(1) 8M x 36	NONE	NONE
36MB	(1) 1M x 36	(1) 4M x 36	(4) 4M x 9
36MB	(1) 1M x 36	(1) 8M x 36	NONE
48MB	(1) 4M x 36	(1) 4M x 36	(4) 4M x 9
48MB	(1) 4M x 36	(1) 8M x 36	NONE
64MB	(1) 8M x 36	(1) 8M x 36	NONE
72MB	(1) 1M x 36	(1) 1M x 36	(4) 16M x 9
96MB	(1) 4M x 36	(1) 4M x 36	(4) 16M x 9
128MB	(1) 8M x 36	(1) 8M x 36	(4) 16M x 9

DRAM JUMPER CONFIGURATION		
Configuration	JP36	JP37
Table 1	pins 1 & 2 closed	pins 2 & 3 closed
Table 2	pins 2 & 3 closed	pins 1 & 2 closed

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 4K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION							
Size	JP1	JP2	JP3	JP4	JP33	JP34	JP35
64KB	2 & 3	1 & 2	2 & 3, 4 & 5	1 & 2	1 & 2	1 & 2	Open
128KB	1 & 2	1 & 2	1 & 2, 3 & 4	1 & 2	2 & 3	2 & 3	Open
256KB	2 & 3	Open	2 & 3, 4 & 5	Open	2 & 3	2 & 3	Closed
256KB	2 & 3	2 & 3	2 & 3, 4 & 5	1 & 2	2 & 3	2 & 3	Open
512KB	1 & 2	2 & 3	1 & 2, 3 & 4	2 & 3	2 & 3	2 & 3	Open
Note: Pins designated should be in the closed position.							

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CPU TYPE CONFIGURATION		
Type	JP30	JP31
P24D	Closed	Closed
All other CPU	Open	Open

CPU TYPE CONFIGURATION					
Type	JP8	JP9	JP10	JP11	JP12
CX486SX	2 & 3	Closed	2 & 3	Open	2 & 3, 4 & 5
80486SX	2 & 3	Open	2 & 3	Open	Open
SLE486SX	2 & 3	Open	2 & 3	Open	3 & 4
CX486DX	1 & 2	Closed	2 & 3	3 & 4	2 & 3
SLE486DX	1 & 2	Closed	2 & 3	3 & 4	3 & 4
80486DX	1 & 2	Closed	2 & 3	3 & 4	Open
SLE486DX2	1 & 2	Closed	2 & 3	3 & 4	3 & 4
80486DX4	1 & 2	Closed	2 & 3	3 & 4	3 & 4
P24D	1 & 2	Closed	1 & 2	3 & 4	1 & 2, 3 & 4
P24T	1 & 2	Closed	1 & 2	2 & 3	3 & 4
Note: Pins designated should be in the closed position.					

CPU TYPE CONFIGURATION (CON'T)					
Type	JP13	JP14	JP15	JP21	JP22
CX486SX	Open	1 & 2, 3 & 4	2 & 3	1 & 2	2 & 3
80486SX	Open	Open	Open	2 & 3	1 & 2
SLE486SX	1 & 2	2 & 3	4 & 5	2 & 3	1 & 2
CX486DX	Open	1 & 2, 3 & 4	2 & 3	1 & 2	2 & 3
SLE486DX	1 & 2	2 & 3	4 & 5	2 & 3	1 & 2
80486DX	Open	Open	Open	2 & 3	1 & 2
SLE486DX2	1 & 2	2 & 3	4 & 5	2 & 3	1 & 2
80486DX4	1 & 2	2 & 3	4 & 5	2 & 3	1 & 2
P24D	2 & 3	2 & 3	4 & 5	2 & 3	1 & 2
P24T	Open	2 & 3	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.					

CPU TYPE CONFIGURATION (CON'T)					
Type	JP24	JP25	JP32	JP41	JP42
CX486SX	2 & 3	2 & 3	2 & 3	2 & 3, 5 & 6	Closed
80486SX	Open	Open	1 & 2	2 & 3, 5 & 6	Closed
SLE486SX	Open	Open	2 & 3	2 & 3, 5 & 6	Closed
CX486DX	2 & 3	2 & 3	2 & 3	2 & 3, 5 & 6	Closed
SLE486DX	Open	Open	2 & 3	2 & 3, 5 & 6	Closed
80486DX	Open	Open	1 & 2	2 & 3, 5 & 6	Closed
SLE486DX2	Open	Open	2 & 3	2 & 3, 5 & 6	Closed
80486DX4	Open	Open	2 & 3	1 & 2, 4 & 5	Open
P24D	Open	1 & 2	2 & 3	2 & 3, 5 & 6	Closed
P24T	1 & 2	1 & 2	2 & 3	2 & 3, 5 & 6	Closed
Note: Pins designated should be in the closed position.					

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CPU VOLTAGE CONFIGURATION	
Voltage	JP41
3.3v	pins 1 & 2, 4 & 5 closed
5.0v (DX4)	pins 2 & 3, 5 & 6 closed
Note: If Q4, Q5, and Q6 are installed or Q6 only installed, set JP41 with pins 1 & 2, 4 & 5 closed.	

CPU SPEED CONFIGURATION (U16-AVASEM/AV9107-03/KTS0147C)			
Speed	JP5	JP6	JP7
20MHz	Closed	Open	Closed
25MHz	Closed	Closed	Open
33MHz	Open	Closed	Closed
40MHz	Closed	Open	Open
50iMHz	Closed	Closed	Open
50MHz	Open	Open	Closed
66iMHz	Open	Closed	Closed

CPU SPEED CONFIGURATION (U17-MX8315)			
Speed	JP5	JP6	JP7
20MHz	Open	Open	Open
25MHz	Open	Closed	Open
33MHz	Closed	Closed	Closed
40MHz	Open	Closed	Closed
50iMHz	Open	Closed	Open
50MHz	Closed	Open	Open
66iMHz	Closed	Closed	Closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP28
0 wait states	pins 1 & 2 closed
1 wait state	pins 2 & 3 closed

BUS SPEED CONFIGURATION	
CPU speed	JP29
<= 33MHz	pins 1 & 2 closed
> 33MHz	pins 2 & 3 closed