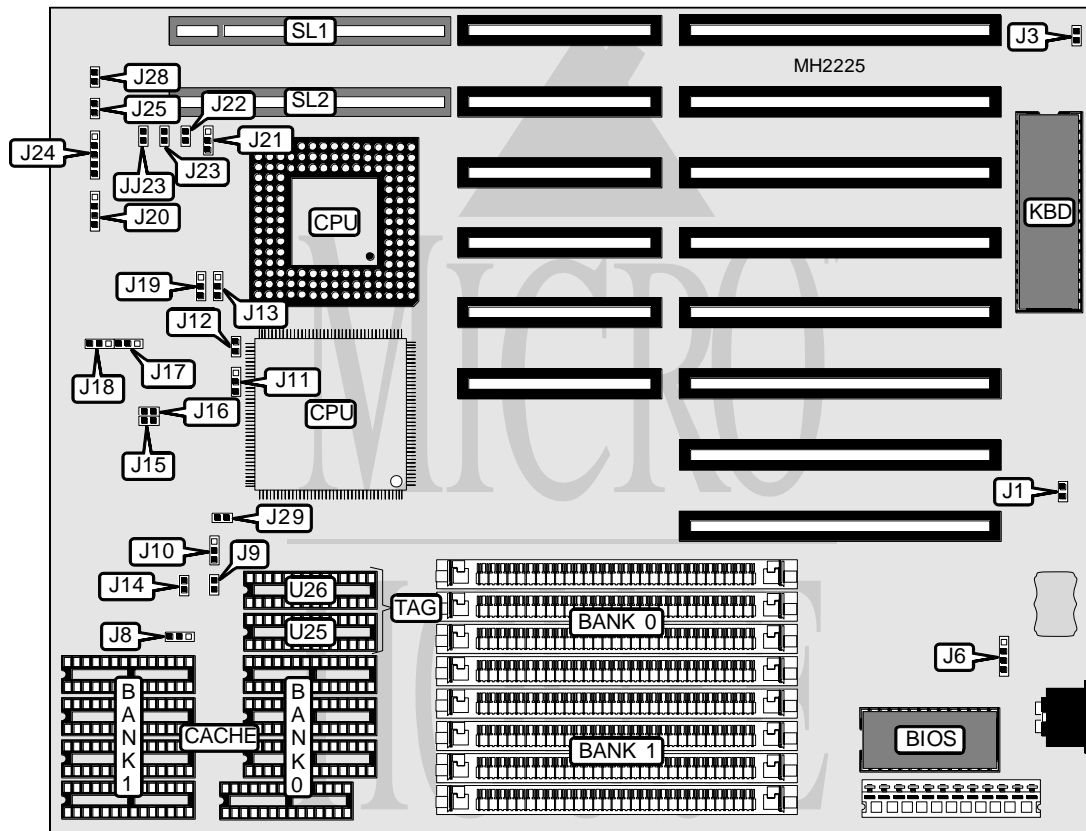


UNIDENTIFIED 486NL

Processor	CX486S/80486SX/CX486DX/80486DX/CX486DX2/80486DX2/Pentium Overdrive
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	254mm x 220mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J6	Speaker	J24
Power LED & keylock	J20	Turbo switch	JJ23
Reset switch	J22	32-bit VESA local bus slots	SL1 & SL2
Turbo LED	J23		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	J1	pins 2 & 3 closed
CMOS memory clear	J1	pins 1 & 2 closed
í Monitor type select color	J3	Closed
Monitor type select monochrome	J3	Open
í Cyrix CPU type select CX486DX2	J19	pins 1 & 2 closed
Cyrix CPU type select CX486DX	J19	pins 2 & 3 closed
í CPU type select Intel	J21	pins 2 & 3 closed
CPU type select Cyrix	J21	pins 1 & 2 closed
í CPU type select PGA	J29	Closed
CPU type select PQFP	J29	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG (U25)	TAG (U26)
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	NONE
128KB	(4) 32K x 8	NONE	(1) 8K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8	(1) 8K x 8	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	NONE

CACHE JUMPER CONFIGURATION				
Size	J8	J9	J10	J14
64KB	pins 2 & 3 closed	Open	pins 1 & 2 closed	Open
128KB	pins 1 & 2 closed	Closed	pins 2 & 3 closed	Open
256KB	pins 2 & 3 closed	Closed	pins 1 & 2 closed	Closed

CPU TYPE CONFIGURATION			
Type	J11	J12	J13
80486SX	pins 1 & 2 closed	Open	pins 2 & 3 closed
80486DX	pins 2 & 3 closed	Closed	pins 1 & 2 closed

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CPU SPEED CONFIGURATION				
Speed	J15	J16	J17	J18
25MHz	Closed	Open	pins 1 & 2 closed	pins 2 & 3 closed
33MHz	Open	Closed	pins 1 & 2 closed	pins 2 & 3 closed
40MHz	Closed	Closed	pins 2 & 3 closed	pins 2 & 3 closed
50iMHz	Closed	Open	pins 1 & 2 closed	pins 2 & 3 closed
50MHz	Closed	Open	pins 2 & 3 closed	pins 1 & 2 closed
66iMHz	Open	Closed	pins 1 & 2 closed	pins 2 & 3 closed

VESA WAIT STATE CONFIGURATION	
Wait states	J28
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	J25
<= 33MHz	Open
> 33MHz	Closed