ROBOTECH, INC. GMB-486UNL

80486SX/80487SX/80486DX/80486DX2 **Processor Processor Speed** 25/33/40/50(internal)/50/66(internal)MHz

Chip Set Toshiba Max. Onboard DRAM 32MB

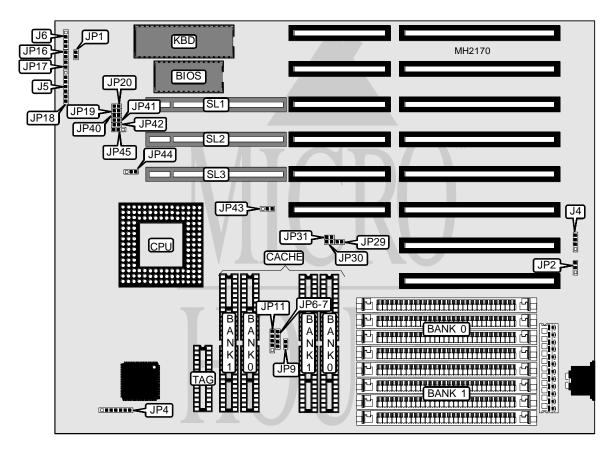
Cache 64/128/256KB

BIOS AMI

Dimensions 260mm x 220mm

32-bit VESA local bus slots (3) I/O Options

NPU Options None



CONNECTIONS				
Purpose Location Purpose Location				
External battery	J4	Turbo switch	JP17	
Power LED & keylock	J5	Reset switch	JP18	
Speaker	J6	32-bit VESA local bus slots	SL1 - SL3	
Turbo LED	JP16			

Continued on next page. . .

ROBOTECH, INC. GMB-486UNL

. . . continued from previous page

USER CONFIGURABLE SETTINGS				
Function Jumper Position				
í Monitor type select color	JP1	Closed		
Monitor type select monochrome	JP1	Open		
í CMOS memory normal operation	JP2	pins 2 & 3 closed		
CMOS memory clear	JP2	pins 1 & 2 closed		

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION					
Size Bank 0 Bank 1 TAG					
32KB	(4) 8K x 8	NONE	(1) 8K x 8		
64KB (4) 8K x 8 (4) 8K x 8 (1) 8K x 8					
128KB	(4) 32K x 8	NONE	(1) 8K x 8		
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8		

CACHE JUMPER CONFIGURATION					
Size	JP6	JP7	JP9	JP11	
32KB	Open	Open	Open	pins 2 & 3 closed	
64KB	Open	Closed	Open	pins 1 & 2 closed	
128KB	Open	Closed	Closed	pins 1 & 2, 3 & 4 closed	
256KB	Closed	Closed	Closed	pins 1 & 2, 4 & 5 closed	

CPU TYPE CONFIGURATION			
Type JP4			
80486SX	pins 5 & 6 closed		
80487SX	pins 2 & 3, 4 & 5, 6 & 7 closed		
80486DX/DX2	pins 1 & 2, 4 & 5, 6 & 7 closed		

CPU SPEED CONFIGURATION					
Speed	Speed JP29 JP30 JP31				
25MHz	Closed	Open	Closed		
33MHz	Closed	Closed	Open		
40MHz	Open	Open	Closed		
50iMHz	Closed	Open	Closed		
50MHz	Open	Closed	Open		
66iMHz	Closed	Closed	Open		

Continued on next page. . .

ROBOTECH, INC. GMB-486UNL

. . . continued from previous page

VESA CLOCK SPEED CONFIGURATION			
CPU speed JP43			
Same phase as CPU	pins 2 & 3 closed		
Same phase as Toshiba U4800 chipset	pins 1 & 2 closed		

BUS SPEED CONFIGURATION			
CPU speed JP19			
<=33MHz Open			
>33MHz	Closed		

VESA WAIT STATE CONFIGURATION			
Wait states JP20			
0 wait states Open			
1 wait state Closed			

VESA MASTER/SLAVE CONFIGURATION					
Setting JP40 JP41 JP42 JP44 JP45					JP45
PAL/GAL installed	Open	Open	Open	pins 1 & 2 closed	pins 2 & 3 closed
PAL/GAL not installed	Closed	Closed	Closed	pins 2 & 3 closed	pins 2 & 3 closed