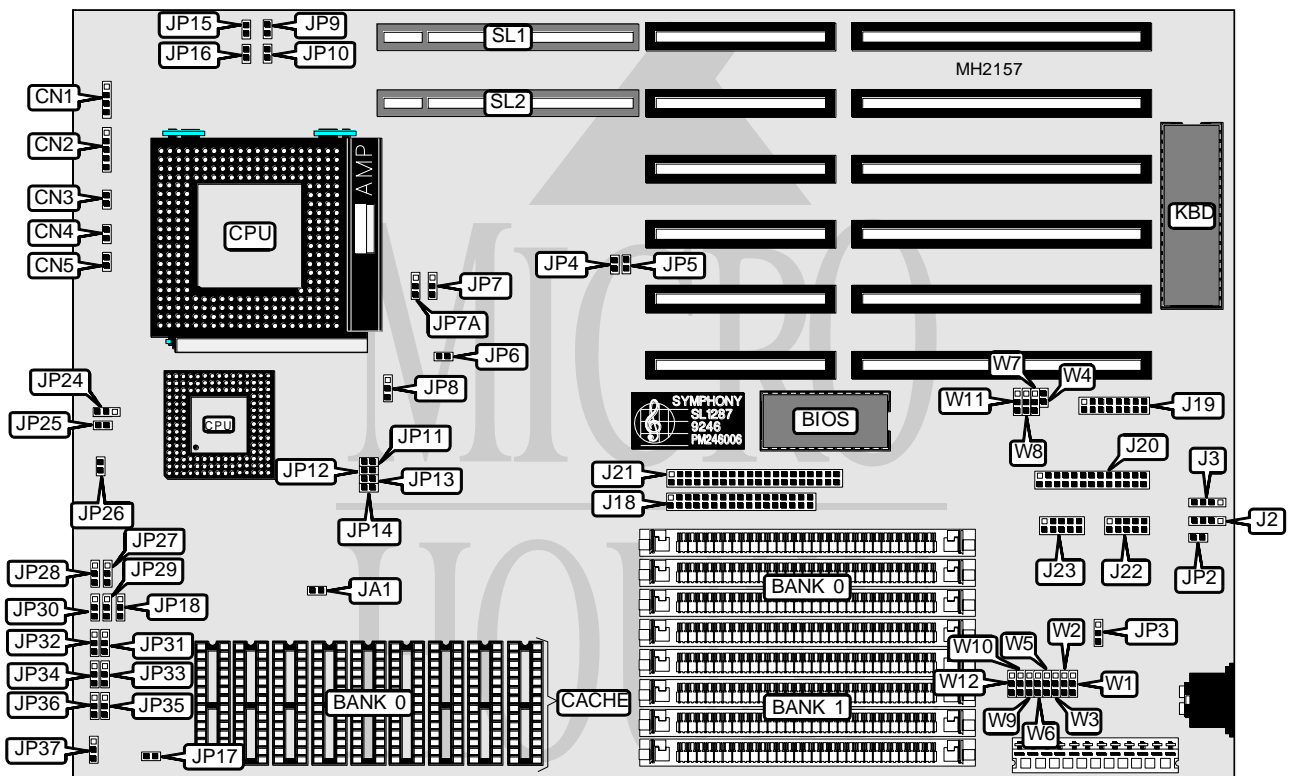


# YOUNG MICRO SYSTEMS, INC.

## VEGA 3486AIO-2VL

<b>Processor</b>	80386DX/80486SX/80486DX/ODP486SX/80486DX2/Pentium Overdrive
<b>Processor Speed</b>	25/33/40/50(internal)/50/66(internal)MHz
<b>Chip Set</b>	Symphony
<b>Max. Onboard DRAM</b>	64MB
<b>Cache</b>	64/256/1024KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	32-bit VESA local bus slots (2), floppy drive interface, game port, IDE interface, parallel port, serial ports (2)
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	CN1	Game interface	J19
Power LED & keylock	CN2	Parallel port	J20
Reset switch	CN3	IDE interface	J21
Turbo LED	CN4	Serial port 1	J22
Turbo switch	CN5	Serial port 2	J23
External battery	J2	32-bit VESA Local bus slots	SL1 & SL2
Floppy drive interface	J18		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	J3	pins 1 & 2 closed
CMOS memory clear	J3	pins 2 & 3 closed
í IDE interface enabled	JA1	Closed
IDE interface disabled	JA1	Open
í Battery type select internal	JP2	Closed
Battery type select external	JP2	Open
í BIOS type select Flash	JP3	pins 1 & 2 closed
BIOS type select normal	JP3	pins 2 & 3 closed
í Cache wait state select 1 wait state	JP11	Open
Cache wait state select 0 wait states	JP11	Closed
í VESA sample select T2	JP12	Open
VESA sample select T3	JP12	Closed
í Cache burst select <=33MHz	JP13	Open
Cache burst select >33MHz	JP13	Closed
í Factory configured - do not alter	JP32	pins 2 & 3 closed
í Parallel port IRQ select IRQ5	W7	pins 1 & 2 closed
Parallel port IRQ select IRQ7	W7	pins 2 & 3 closed
í Floppy drive interface enabled	W12	pins 1 & 2 closed
Floppy drive interface disabled	W12	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE

CACHE CONFIGURATION	
Size	Bank 0
64KB	(8) 8K x 8
256KB	(8) 32K x 8
1MB	(8) 128K x 8

CACHE JUMPER CONFIGURATION					
Size	JP27	JP28	JP29	JP30	JP31
64KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
256KB	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3
1MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

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CPU TYPE CONFIGURATION										
Type	JP6	JP9	JP10	JP17	JP18	JP33	JP34	JP35	JP36	JP37
80386	Open	Open	Closed	Closed	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
80486	Closed	Closed	Open	Open	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION			
Type	JP24	JP25	JP26
80486SX	pins 2 & 3 closed	Open	Open
80486DX	pins 1 & 2 closed	Closed	Closed

CPU SPEED CONFIGURATION		
Type	JP4	JP5
25MHz	Open	Closed
33MHz	Closed	Open
40MHz	Open	Open
50MHz	Open	Closed
50iMHz	Open	Closed
66iMHz	Closed	Open

SERIAL PORT 1 CONFIGURATION		
Port 1	W1	W2
COM1 (3F8)	pins 1 & 2 closed	pins 1 & 2 closed
COM2 (2F8)	pins 1 & 2 closed	pins 2 & 3 closed
COM3 (2E8)	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

SERIAL PORT 2 CONFIGURATION		
Port 2	W3	W5
COM1 (3F8)	pins 1 & 2 closed	pins 1 & 2 closed
COM2 (2F8)	pins 1 & 2 closed	pins 2 & 3 closed
COM4 (2E8)	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

SERIAL PORT 1 INTERRUPT CONFIGURATION	
Port 1	W11
IRQ3	pins 1 & 2 closed
IRQ4	pins 2 & 3 closed

SERIAL PORT 2 INTERRUPT CONFIGURATION	
Port 2	W8
IRQ4	pins 1 & 2 closed
IRQ3	pins 2 & 3 closed

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PARALLEL PORT CONFIGURATION		
Port 1	W6	W9
LPT1 (378)	pins 1 & 2 closed	pins 1 & 2 closed
LPT2 (278)	pins 1 & 2 closed	pins 2 & 3 closed
LPT3 (3BC)	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

IDE INTERFACE CONFIGURATION		
Setting	W4	W10
Enabled	Closed	pins 1 & 2 closed
Disabled	Open	pins 2 & 3 closed

BUS SPEED CONFIGURATION		
CPU speed	JP15	
<=33MHz	Closed	
> 33MHz	Open	

VESA WAIT STATE CONFIGURATION		
Wait states	JP16	
0 wait states	Closed	
1 wait state	Open	

PHASE CONFIGURATION		
Setting	JP8	JP14
1X	pins 2 & 3 closed	Closed
2X	pins 1 & 2 closed	Open

VESA PHASE CONFIGURATION		
Setting	JP7	JP7A
1X	pins 2 & 3 closed	pins 2 & 3 closed
2X	pins 1 & 2 closed	pins 1 & 2 closed