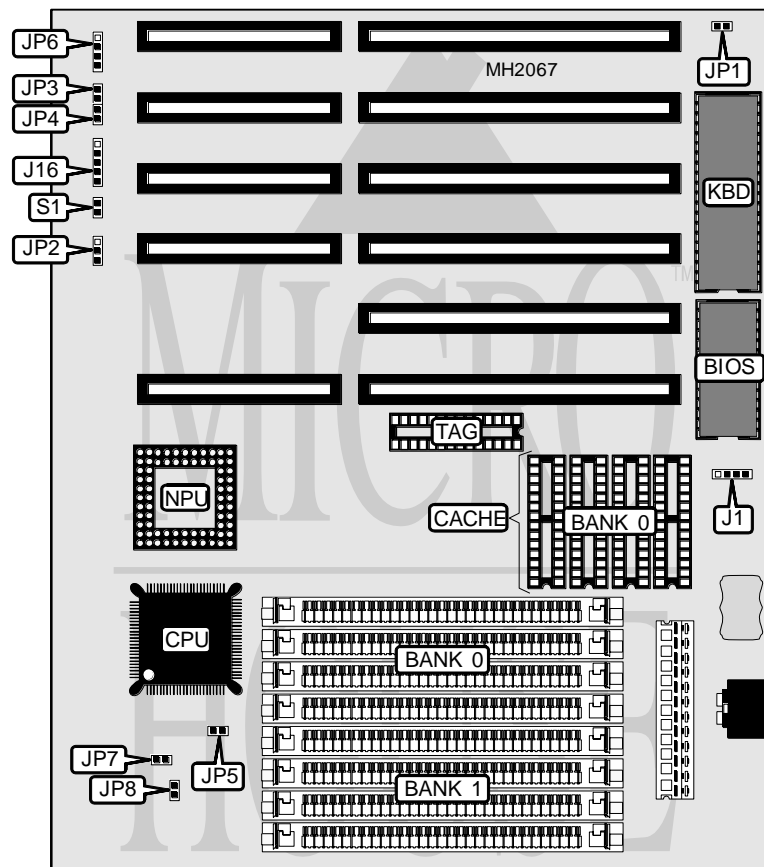


UNIDENTIFIED

OPTI-495SLC 386 WB

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	OPTI
Max. Onboard DRAM	64MB
Cache	128KB
BIOS	AMI
Dimensions	220mm x 165mm
I/O Options	None
NPU Options	80387DX



CONNECTIONS

Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	JP4
Power LED & keylock	J16	Speaker	JP6
Turbo LED	JP3	Reset switch	S1

Continued on next page . . .

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OPTI-495SLC 386WB

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome	JP1	Open
Monitor type select color	JP1	Closed
í CMOS memory normal operation	JP2	pins 2 & 3 closed
CMOS memory clear	JP2	pins 1 & 2 closed
í Turbo mode disabled	JP4	Open
Turbo mode enabled	JP4	Closed
í PQFP CPU enabled	JP5	Open
PQFP CPU disabled	JP5	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE

CACHE CONFIGURATION		
Size	Bank 0	TAG
128KB	(4) 32K x 8	(1) 8K x 8

CPU SPEED CONFIGURATION		
Speed	JP7	JP8
33MHz	Open	Closed
40MHz	Open	Open