DATAEXPERT CORPORATION
A L I - 1 4 2 9 G (E X P 4 0 4 9)

Processor	CXM6/80486SXSL/80486SX/80487SX/CXM7/AMD486DX/AMD486DXL
	80486DX/AMD486DX2/80486DX2SL/80486DX2/Pentium Overdrive
Processor Speed	20/25/33/40/50(internal)/66(internal)MHz
Chip Set	ALI
Max. Onboard DRAM	80MB
Cache	32/64/128/256/512KB
BIOS	AMI
Dimensions	230mm x 218mm
I/O Options	32-bit VESA local bus slots (3)
NPU Options	None
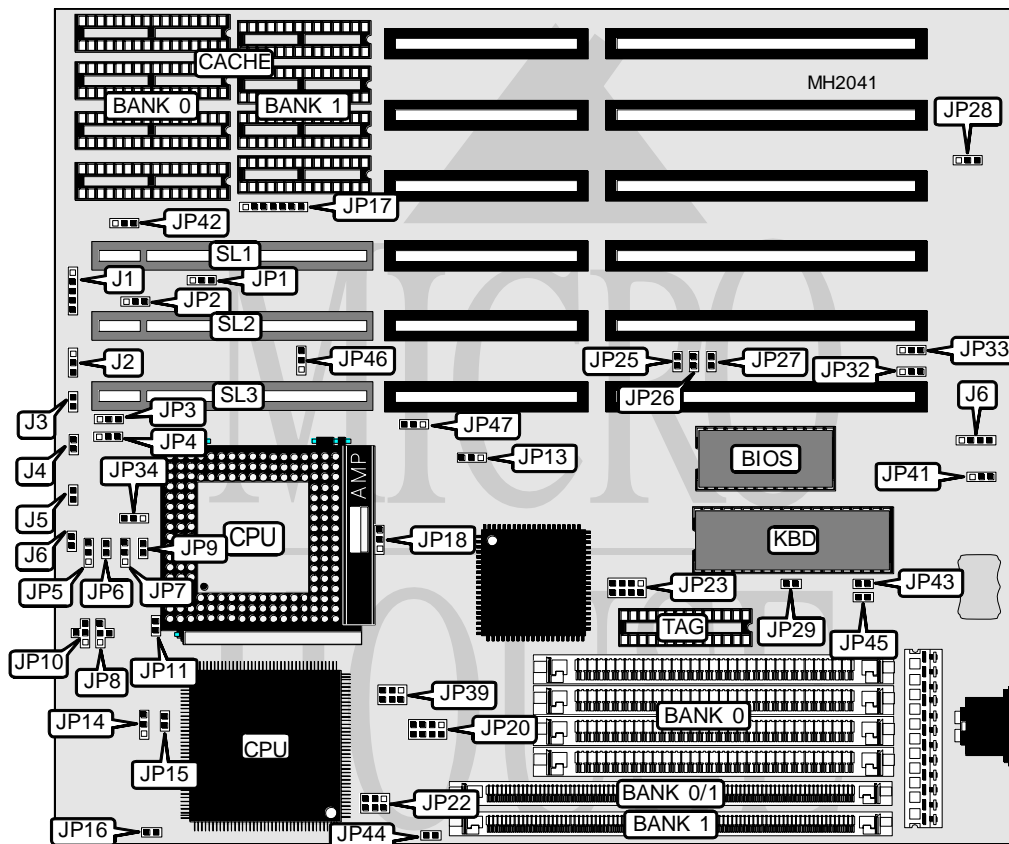
CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Turbo LED	J4
Speaker	J2	Turbo switch	J5
Reset switch	J3	32-bit VESA Local bus slots	SL 1, 2, 3

DATAEXPERT CORPORATION
A L I - 1 4 2 9 G (E X P 4 0 4 9)

USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í CMOS normal operation	J6	pins 2 & 3 closed
CMOS memory clear	J6	pins 1 & 2 closed
í Factory configured - do not alter	JP3	pins 1 & 2 closed
í Factory configured - do not alter	JP4	pins 1 & 2 closed
í CPU installed other than INTEL-S CPU	JP11	Open
INTEL-S CPU installed	JP11	Closed
í 486 PQFP CPU disabled	JP16	Closed
486 PQFP CPU enabled	JP16	Open
Green control connector standby mode	JP20	pins 1 & 3 closed
Green control connector suspend mode	JP20	pins 5 & 7 closed
í Factory configured - do not alter	JP23	N/A
í Monitor type select color	JP29	Open
Monitor type select monochrome	JP29	Closed
í Modem ring connector disabled	JP32	Open
Modem ring connector enabled	JP32	pins 1 & 2 closed
í Green wake-up connector disabled	JP33	Open
Green wake-up connector enabled	JP33	pins 1 & 2 closed
í Lithium battery enabled	JP41	pins 2 & 3 closed
Ni-Cad battery enabled	JP41	pins 1 & 2 closed
í VESA bus cycle with 1 wait state	JP42	pins 2 & 3 closed
VESA bus cycle with 0 wait state	JP42	pins 1 & 2 closed
í Control green VGA H-sync connector enabled (U31 removed)	JP43	Closed
Control green VGA H-sync connector disabled	JP43	Open
í Factory configured - do not alter	JP44	Open
í Control green VGA V-sync connector enabled (U31 removed)	JP45	Closed
Control green VGA V-sync connector disabled	JP45	Open
í Factory configured - do not alter	JP46	pins 1 & 2 closed
í Use 74F244 TTL	JP47	pins 2 & 3 closed
Use 74F245 TTL	JP47	pins 1 & 2 closed

DRAM CONFIGURATION			
Size	Bank 0	Bank 0/1	Bank 1
1MB	(4) 256K x 9	NONE	NONE
1MB	NONE	(1) 256K x 36	NONE
2MB	(4) 256K x 9	NONE	(1) 256K x 36
4MB	(4) 1M x 9	NONE	NONE
4MB	NONE	(1) 1M x 36	NONE
5MB	(4) 256K x 9	NONE	(1) 1M x 36
8MB	(4) 1M x 9	NONE	(1) 1M x 36
8MB	NONE	(1) 1M x 36	(1) 1M x 36
16MB	(4) 4M x 9	NONE	NONE
16MB	NONE	(1) 4M x 36	NONE

DATAEXPERT CORPORATION
A L I - 1 4 2 9 G (E X P 4 0 4 9)

DRAM CONFIGURATION			
Size	Bank 0	Bank 0/1	Bank 1
20MB	(4) 4M x 9	NONE	(4) 1M x 36
20MB	NONE	(1) 4M x 36	(4) 1M x 36
32MB	NONE	(1) 4M x 36	(1) 4M x 36
32MB	(4) 4M x 9	(1) 4M x 36	NONE
64MB	(4) 16M x 9	NONE	NONE
68MB	(4) 16M x 9	NONE	(1) 1M x 36
80MB	(4) 16M x 9	NONE	(1) 4M x 36

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(1) 8K x 8
64KB	(4) 16K x 8	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
í256KB	(4) 64K x 8	NONE	(1) 16K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8 / (1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8

CACHE CONFIGURATION		
Size	JP17	JP22
32KB	pins 6 & 7 closed	Open
64KB	pins 4 & 5, 6 & 7	pins 1 & 2 closed
64KB	pins 5 & 6 closed	pins 1 & 2 closed
128KB	pins 2 & 3, 4 & 5, 6 & 7 closed	pins 1 & 2, 3 & 4 closed
256KB	pins 2 & 3, 4 & 5, 6 & 7 closed	pins 1 & 2, 3 & 4 closed
í256KB	pins 1 & 2, 3 & 4, 5 & 6 closed	pins 1 & 2, 3 & 4 closed
512KB	pins 2 & 3, 4 & 5, 6 & 7 closed	pins 1 & 2, 3 & 4, 5 & 6 closed

CPU TYPE CONFIGURATION					
Type	JP5	JP6	JP7	JP8	JP9
CXM6	Open	Closed	pins 2 & 3	pins 2 & 4	Open
80486SXSL	Open	Open	pins 1 & 2	pins 1 & 2	Closed
80486SX	Open	Open	pins 2 & 3	Open	Closed
80487SX	pins 1 & 2	Open	pins 2 & 3	Open	Closed
CXM7	pins 2 & 3	Closed	pins 2 & 3	pins 2 & 4	Open
AMD DXL	pins 2 & 3	Open	pins 2 & 3	pins 2 & 3	Closed
80486DX	pins 2 & 3	Open	pins 2 & 3	Open	Closed
80486DX2SL	pins 2 & 3	Open	pins 1 & 2	pins 1 & 2	Closed
P24T	pins 1 & 2	Open	pins 1 & 2	pins 1 & 2	Closed

DATAEXPERT CORPORATION
A L I - 1 4 2 9 G (E X P 4 0 4 9)

CPU TYPE CONFIGURATION (continued)					
Type	JP10	JP14	JP15	JP18	JP34
CXM6	pins 2 & 4	pins 1 &2	Open	pins 2 & 3	pins 1 & 2
80486SXSL	pins 2 & 3	pins 1 & 2	Closed	pins 2 & 3	pins 2 & 3
80486SX	Open	pins 1 & 2	Open	pins 2 & 3	Open
80487SX	Open	pins 2 & 3	Closed	pins 2 & 3	Open
CXM7	pins 2 & 4	pins 2 & 3	Closed	pins 2 & 3	pins 1 & 2
AMD DXL	pins 1 & 2	pins 2 & 3	Closed	pins 2 & 3	Open
80486DX	Open	pins 2 & 3	Closed	pins 2 & 3	Open
80486DX2SL	pins 2 & 3	pins 2 & 3	Closed	pins 2 & 3	pins 2 & 3
P24T	pins 2 & 3	pins 2 & 3	Closed	pins 1 & 2	pins 2 & 3

CPU TYPE CONFIGURATION (continued)			
Type	JP25	JP26	JP27
CXM6	Closed	Open	Closed
80486DLC	Closed	Closed	Closed
CXM7	Open	Open	Closed
80486DX	Closed	Open	Open
P24T	Open	Closed	Open

CPU SPEED CONFIGURATION			
Speed	JP28	JP13	JP39
20MHz	pins 1 & 2	pins 1 & 2	pins 3 & 4, 5 & 6
25MHz	pins 1 &2	pins 1 & 2	pins 1 & 2, 5 & 6
¡ 33MHz	pins 1 & 2	pins 1 & 2	pins 1 & 2, 5 & 6
40MHz	pins 1 & 2	pins 1 & 2	pins 5 & 6
50iMHz	pins 2 & 3	pins 2 & 3	pins 1 & 2, 5 & 6
66iMHz	pins 1 & 2	pins 1 & 2	pins 1 & 2, 5 & 6

VESA WAIT STATE/BUS SPEED (ID2 & ID3) CONFIGURATION			
CPU speed	Wait states	JP1 (ID2)	JP2 (ID3)
< 33MHz	0 wait states	pins 1 & 2 closed	pins 1 & 2 closed
> 33MHz	1 wait state	pins 2 & 3 closed	pins 2 & 3 closed

MISCELLANEOUS TECHNICAL NOTE
Note: Pins designated should be in the closed position.