Processor CX486S/80486SX/80486DX/80486DX2/80486DX4/Pentium Overdrive

Processor Speed 25/33/40/50(internal)/50MHz

**Chip Set** VIA **Max. Onboard DRAM** 96MB

**Cache** 64/128/256KB

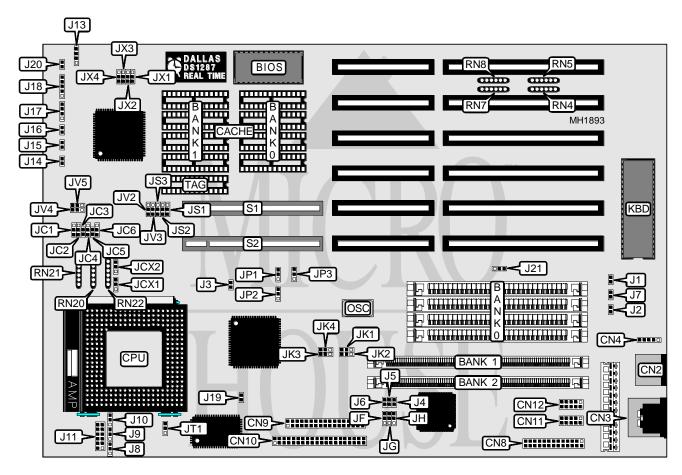
BIOS Award

**Dimensions** 330mm x 218mm

1/O Options 32-bit VESA local bus slots (2), floppy drive interface, IDE interface,

parallel port, PS/2 mouse port, serial ports (2)

NPU Options None



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CONNECTIONS						
Purpose	Purpose	Location				
PS/2 mouse port	CN2	External battery	J13			
AT keyboard connector	CN3	Turbo LED	J14			
PS/2 mouse pin connector	CN4	Turbo switch	J15			
Parallel port	CN8	Reset switch	J16			
Floppy drive interface	CN9	Speaker	J17			
IDE interface	CN10	Power LED & keylock	J18			
Serial port 1	CN11	Hardware sleep	J19			
Serial port 2	CN12	Green status LED	J20			
IDE interface LED	J6	32-bit VESA local bus slot	S1			
3.3V Daughterboard connector	J11	32-bit VESA local bus slot	S2			

USER CONFIGURABLE SETTINGS					
Function	Jumper	Position			
í Factory configured - do not alter	J1	N/A			
í Monitor type select color	J2	Closed			
Monitor type select monochrome, EGA & VGA	J2	Open			
<sup>1</sup> ISA master transfer rate for >5.7MB/sec.	J3	Closed			
ISA master transfer rate for 5.0MB/sec.	J3	Open			
í CMOS memory normal operation	J7	Open			
CMOS memory clear	J7	Closed			
1 IDE interface enabled	J10	pins 1 & 2 closed			
IDE interface disabled	J10	pins 2 & 3 closed			
í Factory configured - do not alter	J21	pins 1 & 2 closed			
1 DX4 clock mode - 2X	JC6	pins 1 & 2 closed			
DX4 clock mode - 2.5X	JC6	pins 2 & 3 closed			
DX4 clock mode - 3X	JC6	Open			
í Factory configured - do not alter	JF	pins 1 & 2 closed			
í Factory configured - do not alter	JP1	N/A			
í Factory configured - do not alter	JP3	pins 1 & 2 closed			
1 Pentium Overdrive write-back cache	JT1	pins 1 & 2 closed			
Pentium Overdrive write-through cache	JT1	pins 2 & 3 closed			
1 Factory configured - do not alter	JX1	pins 1 & 2 closed			

LOCAL ACTIVITY JUMPER CONFIGURATION					
Setting JV4 JV5					
í VGA or local IDE disabled	pins 1 & 2 closed	pins 1 & 2 closed			
SL13 enabled	pins 2 & 3 closed	N/A			
SL14 enabled	N/A	pins 2 & 3 closed			

PARALLEL PORT DIRECTION CONFIGURATION					
Direction JG JH					
Output	pins 1 & 2 closed	pins 2 & 3 closed			
Intput pins 1 & 2 closed pins 1 & 2 closed					
Bidirectional	pins 2 & 3 closed	N/A			

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DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2			
1MB	(4) 256K x 9	NONE	NONE			
1MB	NONE	(1) 1M x 36	NONE			
1MB	NONE	NONE	(1) 1M x 36			
2MB	(4) 256K x 9	(1) 1M x 36	NONE			
2MB	NONE	(1) 1M x 36	(1) 1M x 36			
2MB	(4) 256K x 9	NONE	(1) 1M x 36			
3МВ	(4) 256K x 9	(1) 1M x 36	(1) 1M x 36			
4MB	(4) 1M x 9	NONE	NONE			
4MB	NONE	(1) 1M x 36	NONE			
4MB	NONE	NONE	(1) 1M x 36			
5MB	(4) 256K x 9	(1) 4M x 36	NONE			
5MB	(4) 256K x 9	NONE	(1) 4M x 36			
5MB	(4) 1M x 9	(1) 4M x 36	NONE			
5MB	(4) 1M x 9	NONE	(1) 1M x 36			
5MB	NONE	(1) 1M x 36	(1) 4M x 36			
5MB	NONE	(1) 4M x 36	(1) 1M x 36			
6MB	(4) 256K x 9	(1) 4M x 36	(1) 1M x 36			
6MB	(4) 256K x 9	(1) 1M x 36	(1) 4M x 36			
6MB	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36			
8MB	(4) 1M x 9	(1) 4M x 36	NONE			
8MB	(4) 1M x 9	NONE	(1) 4M x 36			
8MB	NONE	(1) 4M x 36	(1) 4M x 36			
9MB	(4) 256K x 9	(1) 4M x 36	(1) 4M x 36			
9MB	(4) 1M x 9	(1) 1M x 36	(1) 4M x 36			
9MB	(4) 1M x 9	(1) 4M x 36	(1) 1M x 36			
12MB	(4) 1M x 9	(1) 4M x 36	(1) 4M x 36			
16MB	(4) 4M x 9	NONE	NONE			
16MB	NONE	(1) 16M x 36	NONE			
16MB	NONE	NONE	(1) 16M x 36			
17MB	(4) 256K x 9	(1) 16M x 36	NONE			
17MB	(4) 256K x 9	NONE	(1) 16M x 36			
17MB	NONE	(1) 1M x 36	(1) 16M x 36			
17MB	NONE	(1) 16M x 36	(1) 1M x 36			
17MB	(4) 4M x 9	(1) 1M x 36	NONE			
17MB	(4) 4M x 9	NONE	(1) 1M x 36			
18MB	(4) 256K x 9	(1) 1M x 36	(1) 16M x 36			
18MB	(4) 256K x 9	(1) 16M x 36	(1) 1M x 36			
18MB	(4) 4M x 9	(1) 1M x 36	(1) 1M x 36			
20MB	(4) 1M x 9	(1) 16M x 36	NONE			
20MB	(4) 1M x 9	NONE	(1) 16M x 36			
20MB	(4) 4M x 9	(1) 4M x 36	NONE			
20MB	(4) 4M x 9	NONE	(1) 4M x 36			
20MB	NONE	(1) 4M x 36	(1) 16M x 36			
20MB	NONE	(1) 16M x 36	(1) 4M x 36			

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DRAM CONFIGURATION (CONT.)						
Size	Bank 0	Bank 1	Bank 2			
21MB	(4) 256K x 9	(4) 4M x 9	(1) 16M x 36			
21MB	(4) 256K x 9	(1) 16M x 36	(4) 4M x 9			
21MB	(4) 1M x 9	(1) 1M x 36	(1) 16M x 36			
21MB	(4) 1M x 9	(1) 16M x 36	(1) 1M x 36			
21MB	(4) 4M x 9	(1) 1M x 36	(4) 4M x 9			
21MB	(4) 4M x 9	(4) 4M x 9	(1) 1M x 36			
24MB	(4) 1M x 9	(1) 4M x 36	(1) 16M x 36			
24MB	(4) 1M x 9	(1) 16M x 36	(1) 4M x 36			
24MB	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36			
32MB	(4) 4M x 9	(1) 16M x 36	NONE			
32MB	(4) 4M x 9	NONE	(1) 16M x 36			
32MB	NONE	(1) 16M x 36	(1) 16M x 36			
32MB	NONE	(1) 32M x 36	NONE			
32MB	NONE	NONE	(1) 16M x 36			
33MB	(4) 256K x 9	(1) 16M x 36	(1) 16M x 36			
33MB	(4) 4M x 9	(1) 1M x 36	(1) 16M x 36			
33MB	(4) 4M x 9	(1) 16M x 36	(1) 1M x 36			
36MB	(4) 1M x 9	(1) 16M x 36	(1) 16M x 36			
36MB	(4) 4M x 9	(1) 4M x 36	(1) 16M x 36			
36MB	(4) 4M x 9	(1) 16M x 36	(1) 4M x 36			
48MB	(4) 4M x 9	(1) 16M x 36	(1) 16M x 36			
64MB	(4) 16M x 9	NONE	NONE			
64MB	NONE	(1) 32M x 36	(1) 32M x 36			
65MB	(4) 16M x 9	(1) 1M x 36	NONE			
65MB	(4) 16M x 9	NONE	(1) 1M x 36			
66MB	(4) 16M x 9	(1) 1M x 36	(1) 1M x 36			
68MB	(4) 16M x 9	(1) 4M x 36	NONE			
68MB	(4) 16M x 9	NONE	(1) 4M x 36			
69MB	(4) 16M x 9	(1) 1M x 36	(1) 4M x 36			
69MB	(4) 16M x 9	(1) 4M x 36	(1) 1M x 36			
72MB	(4) 16M x 9	(1) 4M x 36	(1) 4M x 36			
80MB	(4) 16M x 9	(1) 16M x 36	NONE			
80MB	(4) 16M x 9	NONE	(1) 16M x 36			
81MB	(4) 16M x 9	(1) 1M x 36	(1) 16M x 36			
81MB	(4) 16M x 9	(1) 16M x 36	(1) 1M x 36			
84MB	(4) 16M x 9	(1) 4M x 36	(1) 16M x 36			
84MB	(4) 16M x 9	(1) 16M x 36	(1) 4M x 36			
96MB	(4) 16M x 9	(1) 16M x 36	(1) 16M x 36			

	IDE CONFIGURATION	
Setting	J4	J5
Pin 27 link to IOCHRDY signal	Closed	Closed
Pin 27 open	Open	Open

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KEYBOARD CONTROLLER SELECTION							
Type	JX3	JX4	RN4	RN5	RN7	RN8	
Internal w/ PS/2 mouse	2 & 3	2 & 3	Not installed	Not installed	Not installed	Installed	
Internal w/out PS/2	2 & 3	1 & 2	Not installed	Installed	Not installed	Not installed	
External	1 & 2	1 & 2	Installed	Not installed	Installed	Not installed	
Note: Pins designated should be in the closed position.							

CACHE CONFIGURATION						
Size Bank 0 Bank 1 TAG						
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8			
128KB	(4) 32K x 8	NONE	(1) 8K x 8			
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8			

CACHE JUMPER CONFIGURATION						
Size JS1 JS2 JS3						
64KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed			
128KB	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed			
256KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed			

CPU TYPE CONFIGURATION									
Туре	JC1	JC2	JC3	JC4	JC5	JC7	RN20	RN21	RN22
CX486S	2 & 3	2 & 3	Open	1 & 2	Open	Open	Closed	Open	Open
CX486S & CX487S	1 & 2	1 & 2	Open	1 & 2	N/A	Open	N/A	N/A	N/A
CX486DX	1 & 2	1 & 2	Open	1 & 2	Open	Open	Closed	Open	Open
80486SX	2 & 3	2 & 3	Open	1 & 2	Open	Closed	Open	Open	Open
80486DX	1 & 2	1 & 2	Open	1 & 2	Open	Closed	Closed	Open	Open
80486DX2	1 & 2	1 & 2	Open	1 & 2	Open	Closed	Closed	Open	Open
AMD486DXL	1 & 2	1 & 2	Open	1 & 2	Open	Closed	Open	Open	Closed
80486DX4	1 & 2	1 & 2	Open	1 & 2	Closed	Closed	Open	Open	Closed
Pentium Overdrive	1 & 2	1 & 2	Closed	2 & 3	Closed	Closed	Open	Open	Open
Note: Pins designate	ed should b	e in the cl	osed positi	on.					

	CPU TYPE CONFIGURATION (CONT.)						
Туре	Type JCX1 JCX2 JP2 JX2						
Intel	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed			
Cyrix	Cyrix pins 2 & 3 closed						
Any other	N/A	N/A	pins 1 & 2 closed	pins 1 & 2 closed			

CPU SPEED CONFIGURATION						
Speed	JK1	JK2	JK3	JK4		
25MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed		
33MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed		
40MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed		
50iMHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed		
50MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed		

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LOCAL IDE SPEED CONFIGURATION						
Speed	J8	J9				
Normal	pins 2 & 3 closed	pins 2 & 3 closed				
Medium	pins 1 & 2 closed	pins 2 & 3 closed				
Fast	pins 1 & 2 closed	pins 1 & 2 closed				

VESA WAIT STATE/BUS SPEED CONFIGURATION						
Speed	Wait states	JV2	JV3			
≤ <b>33MHz</b>	0 wait states	pins 2 & 3 closed	pins 2 & 3 closed			
> 33MHz	1 wait state	pins 1 & 2 closed	pins 1 & 2 closed			