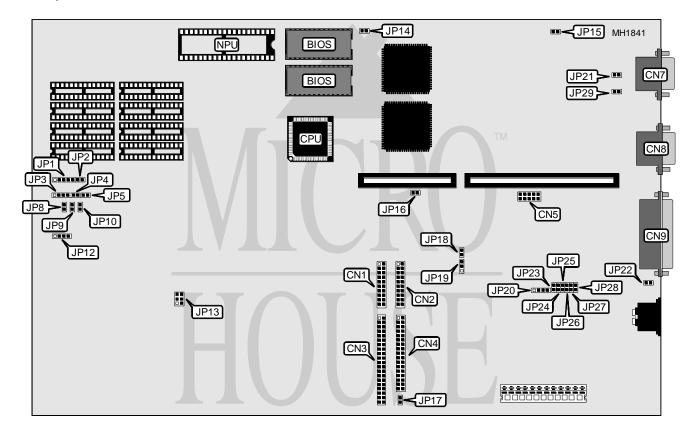
## DTK COMPUTERS, INC. PTM-1260B

80286 **Processor Processor Speed** 12MHz **Chip Set** VLSI Max. Onboard DRAM 1MB Cache None **BIOS** DTK

**Dimensions** 330mm x 218mm

Floppy drive interface, hard drive interface, parallel port, serial ports (2), video port I/O Options

**NPU Options** 80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Hard drive 1 data cable connector	CN1	Parallel port	CN9
Hard drive 1 data cable connector	CN2	Power LED & keylock	JP1
Hard drive interface	CN3	Speaker	JP3
Floppy drive interface	CN4	Turbo LED	JP4
Serial port 2	CN5	Turbo switch	JP5
Video port	CN7	IDE interface LED	JP12
Serial port 1	CN8	External battery	JP20

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Factory configured - do not alter	JP2	N/A	
í Zero wait state enabled	JP8	Closed	
One wait state enabled	JP8	Open	
f Primary hard drive address is 1F0h - 1F7h	JP13	pins 2 & 3 closed	
Secondary hard drive address is 170h - 177h	JP13	pins 1 & 2 closed	
f Primary floppy drive address is 3F0h - 3F7h	JP13	pins 5 & 6 closed	
Secondary floppy drive address is 370h - 37Fh	JP13	pins 4 & 5 closed	
f BIOS type select 32KB	JP14	Open	
BIOS type select 64KB	JP14	Closed	
í Factory configured - do not alter	JP15	N/A	
Monochrome display primary	JP22	Open	
CGA display primary	JP22	Closed	

FLOPPY DRIVE CONFIGURATION			
Drive	JP16	JP17	JP18
í Enabled	Closed	Closed	Closed
Disabled	Open	Open	Open

PRIMARY SERIAL PORT CONFIGURATION				
Port 1 (CN8)	JP24	JP26		
COM1	Closed	Closed		
COM3	Open	Open		
Disabled	Closed	Open		
Disabled	Open	Closed		

SECONDARY SERIAL PORT CONFIGURATION			
Port 2 (CN5)	JP25	JP27	
COM2	Closed	Closed	
COM4	Open	Open	
Disabled	Open	Closed	
Disabled	Closed	Open	

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PARALLEL PORT CONFIGURATION			
LPT	JP19	JP23	JP28
LPT1	pins 1 & 2 closed	Closed	Closed
LPT2	pins 2 & 3 closed	Open	Open
Disabled	N/A	Open	Closed
Disabled	N/A	Closed	Open

		DRAM CONFIGURATION	N	
Size	Bank 0	Parity 0	Bank 1	Parity 1
512KB	(4) 44256	(2) 41256	NONE	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(2) 4164
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256
Note: The exact locations of bank 0 and bank 1 are unknown.				

DRAM JUMPER CONFIGURATION				
Size	JP9	JP10		
512KB	Closed	Closed		
640KB	Open	Closed		
1MB	Open	Open		