GENOA SYSTEMS CORPORATION
T U R B O E X P R E S S   4 8 6 V L

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2/80486DX3/ Pentium Overdrive
Processor Speed	20/25/33/40/50(internal)/50/66(internal)/99(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (3)
NPU Options	None
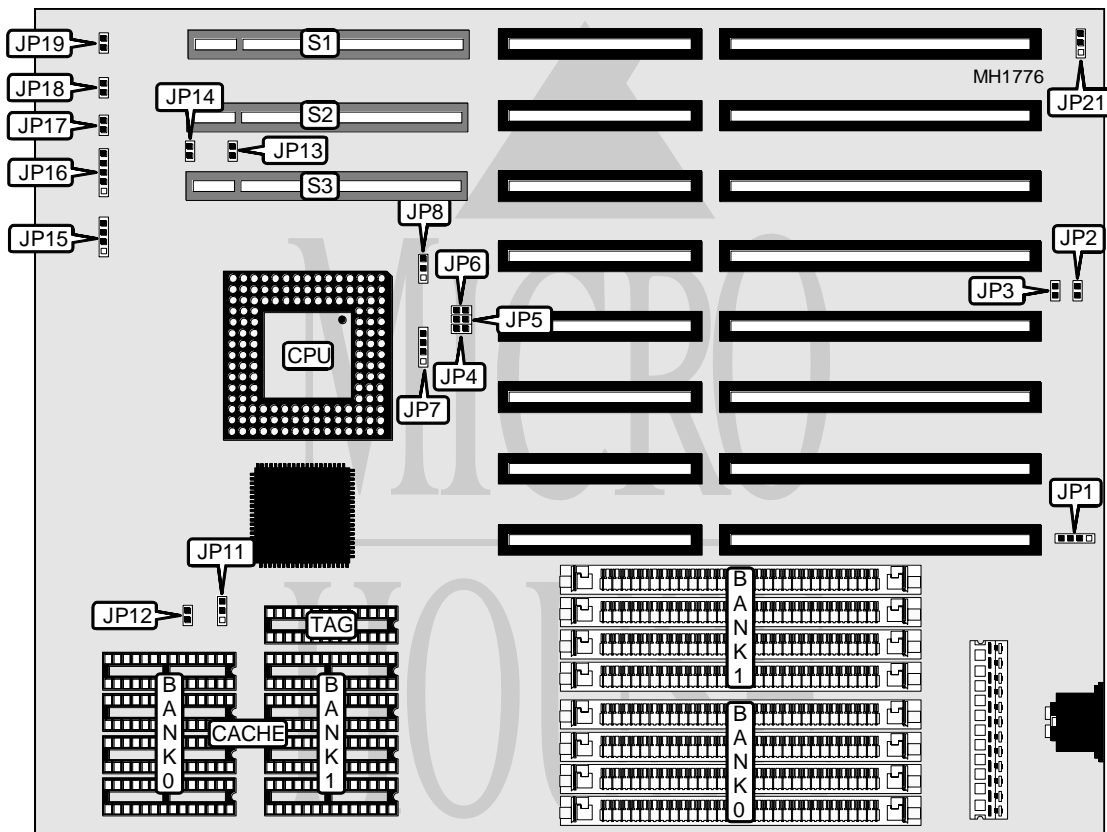
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Turbo switch	JP19
Speaker	JP15	32-bit VESA Local bus slot	S1
Power LED & keylock	JP16	32-bit VESA Local bus slot	S2
Reset switch	JP17	32-bit VESA Local bus slot	S3
Turbo LED	JP18		

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TURBOEXPRESS 486VL

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP2	N/A
í Factory configured - do not alter	JP3	N/A
í Factory configured - do not alter	JP21	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8/(1) 32K x 8

CACHE JUMPER CONFIGURATION		
Size	JP11	JP12
64KB	Open	Open
128KB	pins 1 & 2 closed	Closed
256KB	pins 2 & 3 closed	Closed

CPU TYPE CONFIGURATION		
Type	JP7	JP8
80486SX	pins 2 & 3 closed	Open
80487SX	pins 1 & 2 and 3 & 4 closed	pins 2 & 3 closed
80486DX	pins 1 & 2 and 3 & 4 closed	pins 1 & 2 closed
ODP486SX (168 pin)	pins 1 & 2 and 3 & 4 closed	pins 1 & 2 closed
ODP486SX (169 pin)	pins 1 & 2 and 3 & 4 closed	pins 2 & 3 closed
ODP486SX2	pins 1 & 2 and 3 & 4 closed	pins 2 & 3 closed
80486DX2	pins 1 & 2 and 3 & 4 closed	pins 1 & 2 closed
80486DX3	pins 1 & 2 and 3 & 4 closed	pins 1 & 2 closed
Pentium Overdrive	pins 1 & 2 and 3 & 4 closed	pins 2 & 3 closed

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CPU SPEED CONFIGURATION			
Speed	JP4	JP5	JP6
20MHz	Closed	Closed	Open
25MHz	Closed	Open	Closed
33MHz	Open	Closed	Closed
40MHz	Closed	Open	Open
50i MHz	Closed	Open	Closed
50MHz	Open	Closed	Open
66i MHz	Open	Closed	Closed
99i MHz	Open	Closed	Closed

VESA WAIT STATE/BUS SPEED CONFIGURATION			
CPU Speed	Wait states	JP13	JP14
≤ 33MHz	0 wait states	Open	Open
> 33MHz	1 wait state	Closed	Closed