## UNIDENTIFIED N318 386SX/25/33/40

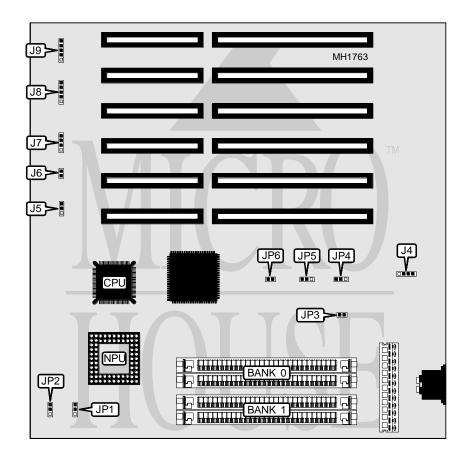
80386SX/CX486SLC **Processor Processor Speed** 25/33/40MHz

**Chip Set** ALI Max. Onboard DRAM 16MB Cache None **BIOS** AMI

**Dimensions** 220mm x 170mm

I/O Options None

**NPU Options** 80387SX/CX83S87



CONNECTIONS					
Purpose	Location	Purpose	Location		
External battery	J4	Turbo switch & Turbo LED	J8		
Reset switch	J6	Power LED & keylock	J9		
Speaker	J7				

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í Select 0 wait states	J5	pins 2 & 3 closed		
Select 1 wait state	J5	pins 1 & 2 closed		
í NPU asynchronous	JP2	pins 2 & 3 closed		
NPU synchronous with CPU	JP2	pins 1 & 2 closed		
í CMOS memory normal operation	JP3	Open		
Real time clock enabled	JP3	Closed		
í Monitor type select color	JP4	pins 2 & 3 closed		
Monitor type select monochrome	JP4	pins 1 & 2 closed		
í Parity check enabled	JP6	Open		
Parity check disabled	JP6	Closed		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
512KB	(2) 256K x 9	NONE		
1MB	(2) 256K x 9	(2) 256K x 9		
2MB	(2) 1M x 9	NONE		
4MB	(2) 1M x 9	(2) 1M x 9		
8MB	(2) 4M x 9	NONE		
16MB	(2) 4M x 9	(2) 4M x 9		

CPU SPEED CONFIGURATION				
Speed	JP1	JP5		
25MHz	pins 1 & 2 closed	pins 2 & 3 closed		
33MHz	pins 2 & 3 closed	pins 2 & 3 closed		
40MHz	pins 1 & 2 closed	pins 1 & 2 closed		