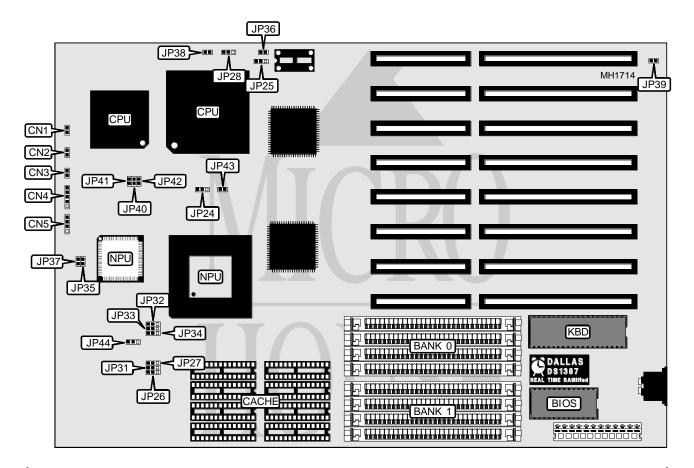
YOUNG MICRO SYSTEMS, INC. V S 3 4 8 6 F / S - 3 V L

Processor	80386DX/80486DLC/80486SX/80486DX/80486DX2
Processor Speed	25/33/40/50(internal)/50/66MHz
Chip Set	Symphony
Max. Onboard DRAM	32MB
Cache	64/256/1024KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387DX/80487SX



CONNECTIONS							
Purpose Location Purpose Location							
Turbo switch CN1 Power LED & keylock CN4							
Turbo LED	Speaker	CN5					
Reset switch	CN3						

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	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CPU TYPE CONFIGURATION								
Туре	JP24	JP25	JP28	JP32	JP33	JP34	JP35	JP36
80386DX	2&3	1&2	1&2	1&2	1&2	1&2	Closed	Closed
80486SX	2&3	2&3	2&3	2&3	2&3	2&3	Open	Open
80486DLC	2&3	1&2	1&2	1&2	1&2	1&2	Closed	Closed
Note: Pins des	Note: Pins designated should be in the closed position.							

CPU TYPE CONFIGURATION								
Туре	JP37	JP38	JP39	JP40	JP41	JP42	JP43	JP44
80386DX	Open	Open	Closed	Open	Open	Open	Open	1&2
80486SX	Open	Open	Open	Open	Open	Open	Closed	2&3
80486DLC	Open	Open	Closed	Open	Open	Open	Open	1&2
Note: Pins des	Note: Pins designated should be in the closed position.							

CPU SPEED CONFIGURATION								
Speed	JP24	JP25	JP28	JP32	JP33	JP34	JP35	JP36
25MHz	2&3	2&3	2&3	2&3	2&3	2&3	Open	Open
33MHz	2&3	2&3	2&3	2&3	2&3	2&3	Open	Open
50i MHz	2&3	2&3	2&3	2&3	2&3	2&3	Open	Open
50MHz	1&2	2&3	2&3	2&3	2&3	2&3	Open	Open
66i MHz	2&3	2&3	2&3	2&3	2&3	2&3	Open	Open
Note: Pins des	Note: Pins designated should be in the closed position.							

CPU SPEED CONFIGURATION								
Speed	JP37	JP38	JP39	JP40	JP41	JP42	JP43	JP44
25MHz	Open	Open	Open	Closed	Closed	Closed	Open	2&3
33MHz	Open	Open	Open	Closed	Closed	Closed	Open	2&3
50I MHz	Open	Open	Open	Closed	Closed	Closed	Open	2&3
50MHz	Closed	Closed	Open	Open	Closed	Closed	Open	2&3
66I MHz	Open	Open	Open	Closed	Closed	Closed	Open	2&3
Note: Pins des	Note: Pins designated should be in the closed position.							

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CACHE CONFIGURATION						
Size Cache						
64KB	(9) 8K x 8					
256KB (9) 32K x 8						

CACHE JUMPER CONFIGURATION							
Size JP26 JP27 JP31							
64KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed				
256KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed				