LION COMPUTERS, INC.
LOCAL ISA 486 USA REV.1

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2/ODP586SX
Processor Speed	20/25/33/40(Internal)/50(Internal)/50/66(Internal)MHz
Chip Set	C & T
Max. onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	220mm x 218mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None
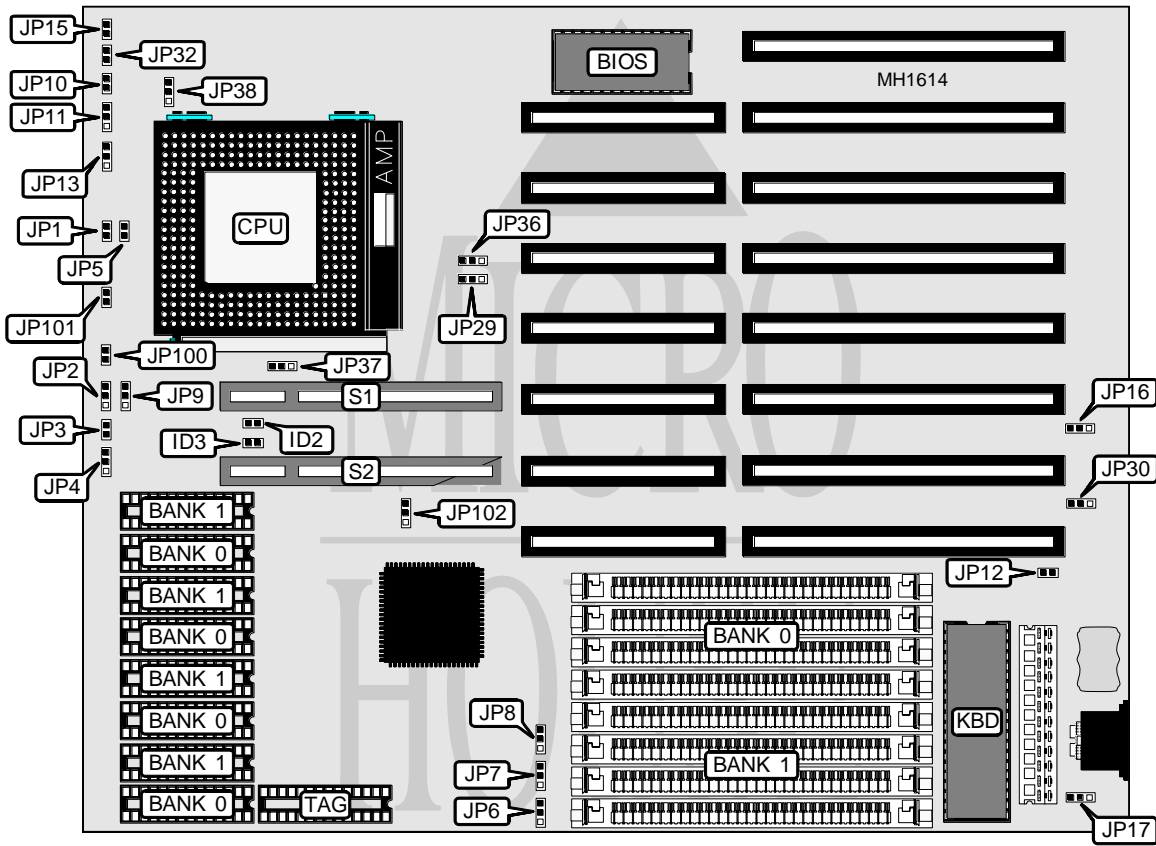
CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	JP10	External battery	JP17
Speaker	JP11	Turbo switch	JP32
Power LED & keylock	JP13	32-bit VESA local bus slot	S1
Reset switch	JP15	32-bit VESA local bus slot	S2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP1	Closed
í Factory configured - do not alter	JP9	pins 2 & 3 closed
í Monitor type select color	JP12	Closed
Monitor type select monochrome	JP12	Open
í CMOS memory normal operation	JP16	pins 1 & 2 closed
CMOS memory clear	JP16	pins 2 & 3 closed
í Factory configured - do not alter	JP29	pins 2 & 3 closed
í Factory configured - do not alter	JP36	pins 1 & 2 closed
í VESA video card chipset type select S3 or Western Digital	JP37	pins 1 & 2 closed
VESA video card chipset type select other	JP37	pins 2 & 3 closed
í Factory configured - do not alter	JP38	pins 1 & 2 closed
í Factory configured - do not alter	JP100	Open
í Factory configured - do not alter	JP101	Open
í Factory configured - do not alter	JP102	pins 2 & 3 closed
Note: JP37 is present only on the Rev.1 system boards.		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	JP6	JP7	JP8
64KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 4 closed
128KB	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
256KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed

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VESA WAIT STATE (ID2) CONFIGURATION		
Wait states	CPU speed	ID2
0 wait states	≤ 33MHz	Closed
1 wait state	> 33MHz	Open

VESA BUS SPEED (ID3) CONFIGURATION	
CPU speed	ID3
≤ 33MHz	Open
> 33MHz	Closed

CPU SPEED CONFIGURATION	
Speed	JP30
20MHz	pins 2 & 3 closed
25MHz	pins 2 & 3 closed
33MHz	pins 1 & 2 closed
40i MHz	pins 2 & 3 closed
50i MHz	pins 2 & 3 closed
50MHz	pins 1 & 2 closed
66i MHz	pins 1 & 2 closed

CPU TYPE CONFIGURATION				
Type	JP2	JP3	JP4	JP5
80486SX	pins 1 & 2 closed	Open	Open	Open
80487SX	pins 2 & 3 closed	Closed	pins 1 & 2 closed	Closed
80486DX	pins 2 & 3 closed	Closed	pins 2 & 3 closed	Open
ODP486SX	pins 2 & 3 closed	Closed	pins 1 & 2 closed	Closed
80486DX2	pins 2 & 3 closed	Closed	pins 2 & 3 closed	Open
ODP586SX	pins 2 & 3 closed	Closed	pins 1 & 2 closed	Closed