ORCHID TECHNOLOGY
SUPERBOARD 486/VLB (VERSION 2)

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	20/25/33/50(internal)/50/66(internal)MHz
Chip Set	OPTI
Max. onboard DRAM	32MB
Cache	128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA card slots (2)
NPU Options	None
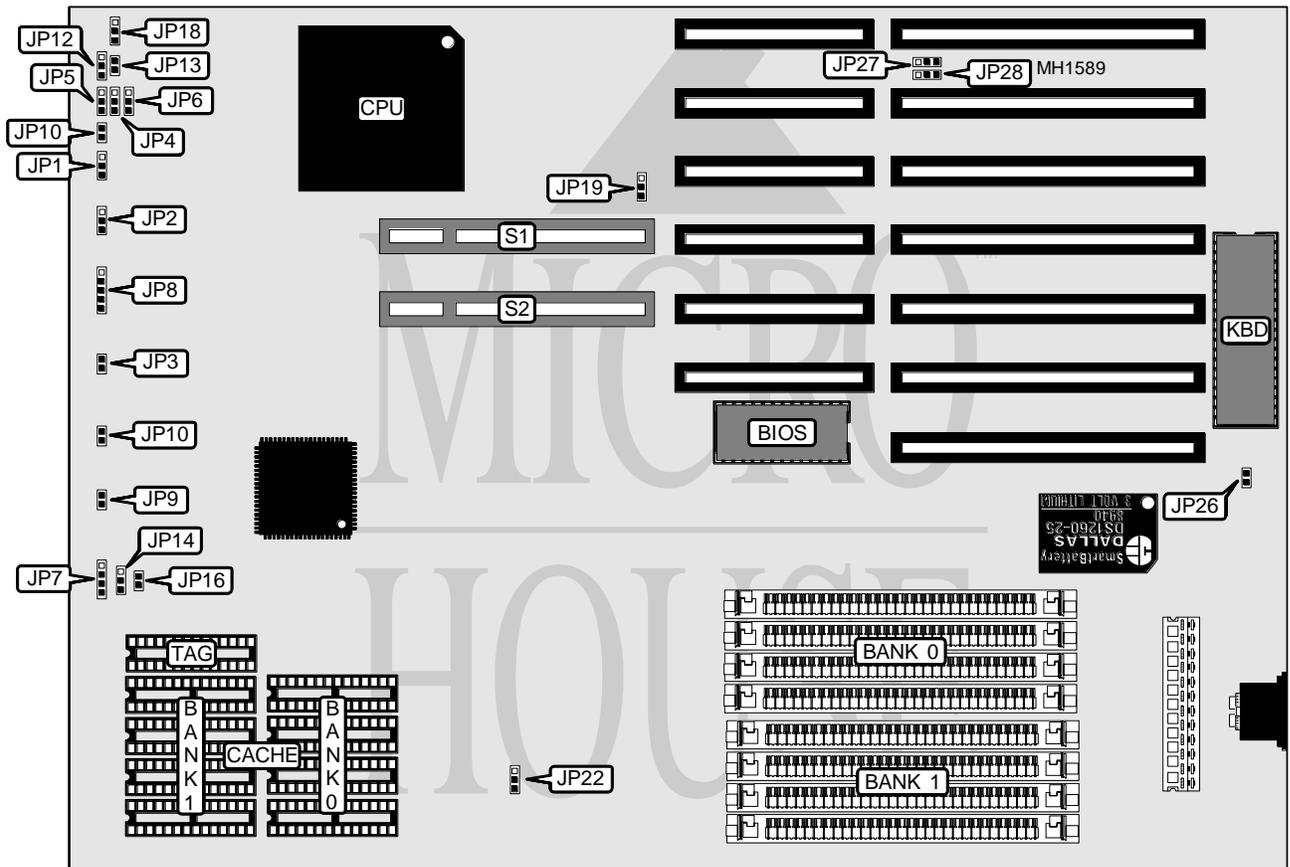
CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	JP8	32-bit VESA cards	S1 & S2
Speaker	JP7	Reset switch	JP9
Turbo LED	JP3	Turbo switch	JP10

ORCHID TECHNOLOGY
S U P E R B O A R D   4 8 6 / V L B   (V E R S I O N   2)

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP26	Closed
Monitor type select monochrome	JP26	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	JP14	JP16	JP22
128KB	pins 2 & 3 closed	Open	pins 2 & 3 closed
256KB	pins 1 & 2 closed	Closed	pins 1 & 2 closed

CPU TYPE CONFIGURATION					
CPU Type	Oscillator	JP12	JP13	JP18	JP19
80486SX - 20MHz	20MHz	pins 2 & 3	Open	Open	pins 2 & 3
80486SX - 20MHz	40MHz	pins 2 & 3	Open	Open	pins 1 & 2
80486SX - 25MHz	25MHz	pins 2 & 2	Open	Open	pins 2 & 3
80486SX - 25MHz	50MHz	pins 2 & 3	Open	Open	pins 1 & 2
80486DX - 25MHz	25MHz	pins 1 & 2	Closed	pins 2 & 3	pins 2 & 3
80486DX - 33MHz	66MHz	pins 1 & 2	Closed	pins 2 & 3	pins 1 & 2
80486DX - 50MHz	50MHz	pins 1 & 2	Closed	pins 2 & 3	pins 2 & 3
ODP486SX - 25MHz	25MHz	pins 2 & 3	Open	Open	pins 2 & 3
ODP486SX - 25MHz	50MHz	pins 2 & 3	Open	pins 2 & 3	Open
80486DX2 - 50MHz	25MHz	pins 1 & 2	Closed	pins 2 & 3	pins 2 & 3
80486DX2 - 50MHz	50MHz	pins 1 & 2	Closed	pins 2 & 3	pins 1 & 2
80486DX2 - 66MHz	66MHz	pins 1 & 2	Closed	pins 2 & 3	pins 1 & 2

ORCHID TECHNOLOGY
SUPERBOARD 486/VLB (VERSION 2)

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VESA CPU TYPE (ID0 & ID1) CONFIGURATION		
CPU Type	JP1 (ID0)	JP2 (ID1)
80386	pins 2 & 3 closed	pins 1 & 2 closed
80486	pins 1 & 2 closed	pins 2 & 3 closed

VESA WAIT STATE (ID2) CONFIGURATION		
Wait states	CPU speed	JP4 (ID2)
0 wait states	$\leq$ 33MHz	pins 1 & 2 closed
1 wait state	$>$ 33MHz	pins 2 & 3 closed

VESA BUS SPEED (ID3) CONFIGURATION	
CPU speed	JP5 (ID3)
$\leq$ 33MHz	pins 1 & 2 closed
$>$ 33MHz	pins 2 & 3 closed

VESA ID4 CONFIGURATION	
Setting	JP6 (ID4)
Reserved for future use	pins 2 & 3 closed