SHUTTLE COMPUTER INTERNATIONAL, INC.
H O T 4 0 9 4 8 6 V L B

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	25/33/50(internal)/50/66(internal)MHz
Chip Set	OPTI
Max. onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	260mm x 220mm
I/O Options	32-bit VESA slots (2)
NPU Options	None
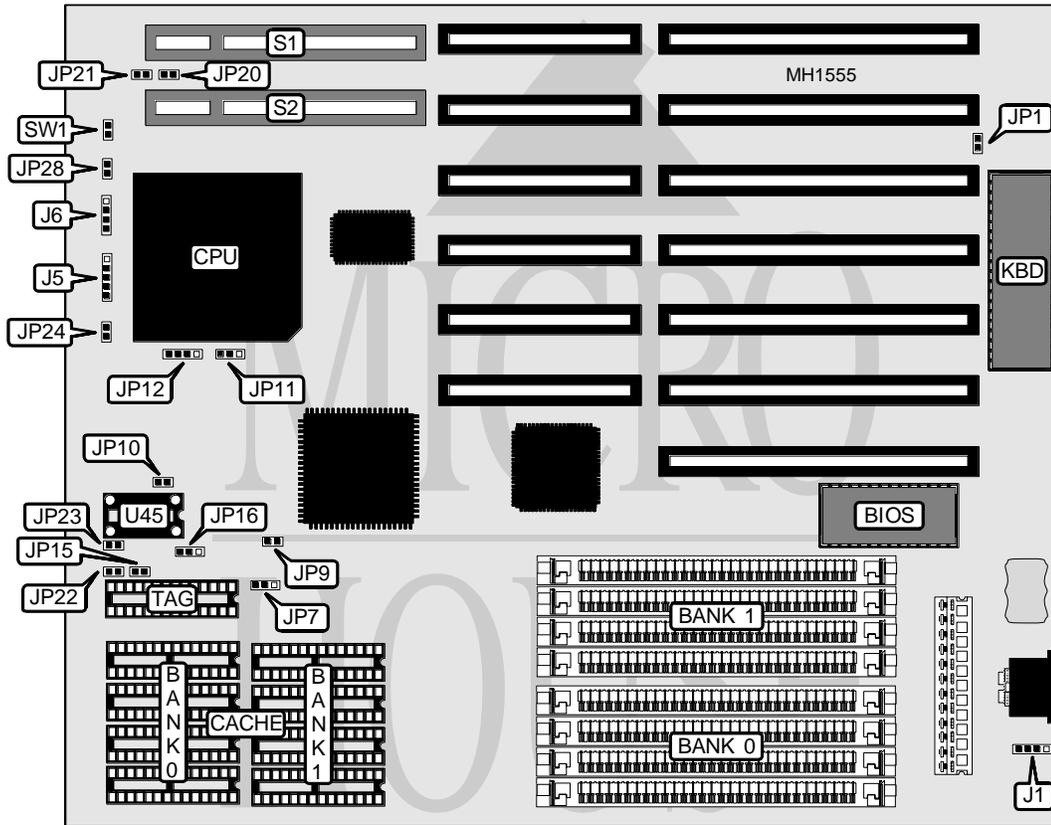
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	JP28
Power LED & keylock	J5	32-bit VESA slots (2)	S1 & S2
Speaker	J6	Reset switch	SW1
Turbo LED	JP24		

SHUTTLE COMPUTER INTERNATIONAL, INC.
HOT 409 486 VLB

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery select internal	J1	pins 1 & 2 closed
Battery select external	J1	pins 2 & 3 closed
í Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CPU JUMPER CONFIGURATION/CLOCK GENERATOR IN U45					
CPU Type	JP10	JP11	JP12	JP16	JP23
80486SX-25	Open	Open	pins 2 & 3	pins 2 & 3	Closed
80486SX-33	Closed	Open	pins 2 & 3	pins 2 & 3	Open
80487SX-25	Open	pins 2 & 3	pins 1 & 2, 3 & 4	pins 2 & 3	Closed
80486DX-25	Open	pins 1 & 2	pins 1 & 2. 3 & 4	pins 2 & 3	Closed
80486DX-33	Closed	pins 1 & 2	pins 1 & 2, 3 & 4	pins 2 & 3	Open
80486DX-50	Open	pins 1 & 2	pins 1 & 2, 3 & 4	pins 1 & 2	Closed
80486DX2-50	Open	pins 1 & 2	pins 1 & 2, 3 & 4	pins 2 & 3	Closed
80486DX2-66	Closed	pins 1 & 2	pins 1 & 2, 3 & 4	pins 2 & 3	Open
Note: Pins designated should be in the closed position.					

CPU JUMPER CONFIGURATION/OSCILLATOR IN U45			
CPU Type	JP11	JP12	JP16
80486SX-25	Open	pins 2 & 3	pins 2 & 3
80486SX-33	Open	pins 2 & 3	pins 2 & 3
80487SX-25	pins 2 & 3	pins 1 & 2, 3 & 4	pins 2 & 3
80486DX-25	pins 1 & 2	pins 1 & 2. 3 & 4	pins 2 & 3
80486DX-33	pins 1 & 2	pins 1 & 2, 3 & 4	pins 2 & 3
80486DX-50	pins 1 & 2	pins 1 & 2, 3 & 4	pins 1 & 2
80486DX2-50	pins 1 & 2	pins 1 & 2, 3 & 4	pins 2 & 3
80486DX2-66	pins 1 & 2	pins 1 & 2, 3 & 4	pins 2 & 3
Note: Pins designated should be in the closed position.			

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HOT 409 486 VLB

CACHE CONFIGURATION			
Size	Cache	Location	TAG
64KB	(8) 8K x 8	Banks 0 & 1	(1) 8K x 8
128KB	(4) 32K x 8	Bank 0	(1) 8K x 8
256KB	(8) 32K x 8	Banks 0 & 1	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP7	JP9	JP15	JP22
64KB	pins 2 & 3 closed	Open	Open	Open
128KB	pins 1 & 2 closed	Closed	Closed	Open
256KB	pins 2 & 3 closed	Closed	Closed	Closed

VESA BUS SPEED AND WAIT STATE CONFIGURATION			
CPU speed	Wait states	J20	J21
$\leq$ 33MHz	0 wait states	Closed	Closed
$>$ 33MHz	1 wait state	Open	Open