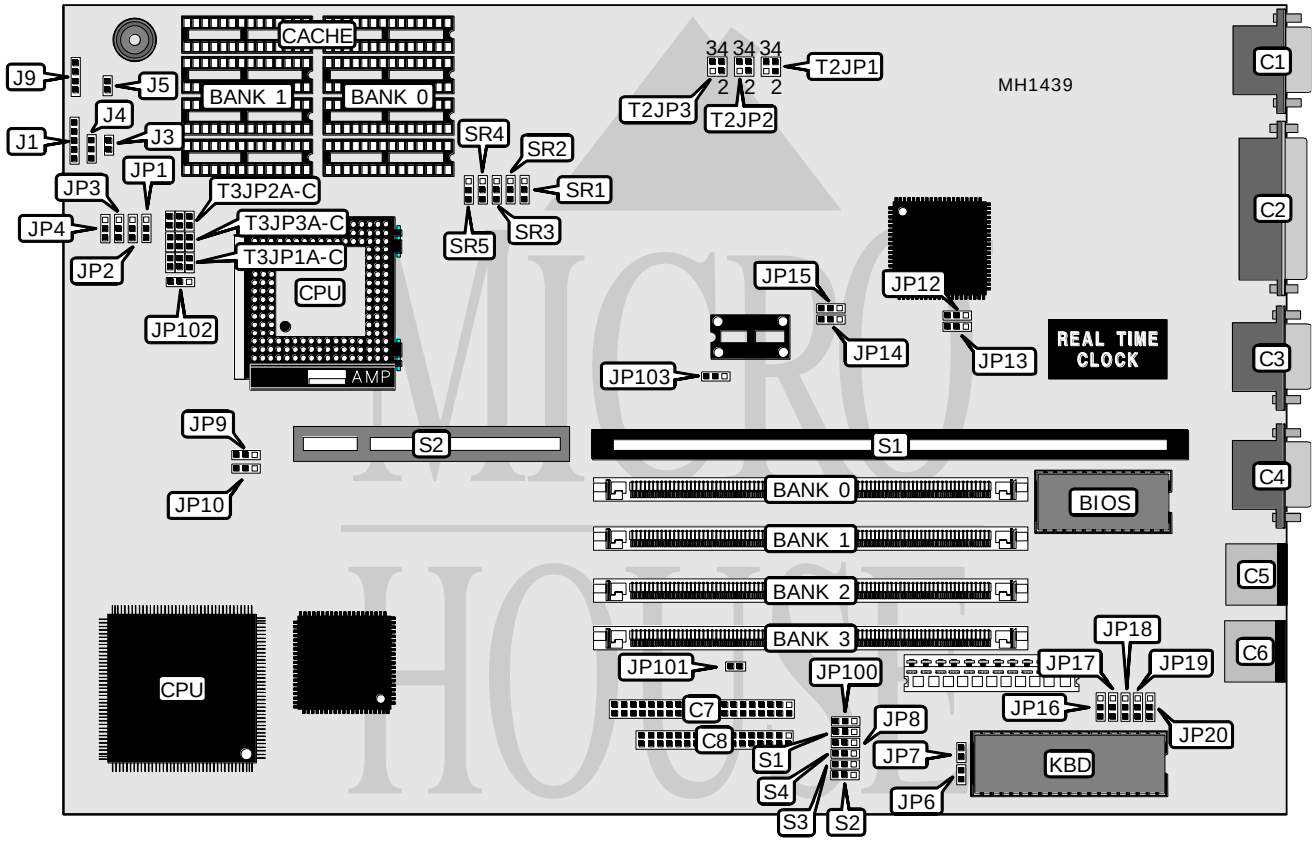
BCM ADVANCED RESEARCH, INC.

S I L V E R I I A S 5 0 0 L P

Processor	80486SX/80487SX/80486DX/80486DX2/ODP586SX
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	SIS
Max. onboard DRAM	64MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA riser card slot, floppy drive interface, ISA riser card slot, parallel port, PS/2 mouse port, PS/2 keyboard port, serial ports (2), VGA port
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
VGA port	C1	Power LED & keylock	J1
Parallel port	C2	Turbo LED	J3
Serial port	C3	Turbo switch	J4
Serial port	C4	Reset switch	J5
PS/2 mouse	C5	Speaker	J9
PS/2 keyboard	C6	ISA riser card slot	S1
IDE interface	C7	32-bit VESA riser card slot	S2
Floppy drive interface	C8		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP6	Open
Monitor type select monochrome	JP6	Closed
í CMOS memory normal operation	JP7	Open
CMOS memory clear	JP7	Closed
í Power good signal detect from board	JP8	pins 1 & 2 closed
Power good signal detect from power supply	JP8	pins 2 & 3 closed
í Factory configured - do not alter	JP14	pins 2 & 3 closed
í Factory configured - do not alter	JP15	pins 2 & 3 closed
í PQFP 80486SX disabled	JP102	pins 1 & 2 closed
PQFP 80486SX enabled	JP102	pins 2 & 3 closed
í PS/2 mouse port enabled	JP103	pins 1 & 2 closed
PS/2 mouse port disabled	JP103	pins 2 & 3 closed
í IDE interface enabled	S1	pins 1 & 2 closed
IDE interface disabled	S1	pins 2 & 3 closed
í Factory configured - do not alter	S2	pins 1 & 2 closed
í Factory configured - do not alter	S3	pins 1 & 2 closed
í Factory configured - do not alter	S4	pins 2 & 3 closed

I/O JUMPER CONFIGURATION								
Floppy(C8)	C3	C4	C2	JP20	JP19	JP18	JP17	JP16
Enabled	COM1	COM2	LPT2	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2
Enabled	COM1	COM2	LPT1	1 & 2	2 & 3	2 & 3	1 & 2	2 & 3
Enabled	COM3	COM4	LPT1	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2
Enabled	COM2	COM3	LPT2	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3
Disabled	COM1	COM2	LPT2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
Disabled	COM1	COM2	LPT1	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3
Disabled	COM3	COM4	LPT1	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2
Disabled	COM2	COM3	LPT2	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3
Enabled	Disabled	Disabled	Disabled	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2
Disabled	Disabled	Disabled	Disabled	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.								

ONBOARD VGA JUMPER CONFIGURATION			
Onboard VGA port	T2JP1	T2JP2	T2JP3
Enabled	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
Disabled	pins 3 & 4 closed	pins 3 & 4 closed	pins 3 & 4 closed

VGA WAIT STATE CONFIGURATION		
Wait State	JP12	JP13
0 (25MHz)	pins 2 & 3 closed	pins 2 & 3 closed
1 (33MHz)	pins 2 & 3 closed	pins 1 & 2 closed
2 (40MHz)	pins 1 & 2 closed	pins 2 & 3 closed
3 (50MHz)	pins 1 & 2 closed	pins 1 & 2 closed

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DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2	Bank3	JP100	JP101
1MB	256K x 36	NONE	NONE	NONE	Open	Open
2MB	256K x 36	256K x 36	NONE	NONE	Open	Open
2MB	512K x 36	NONE	NONE	NONE	pins 1 & 2	Open
4MB	1M x 36	NONE	NONE	NONE	Open	Open
6MB	256K x 36	256K x 36	1M x 36	NONE	Open	Open
6MB	512K x 36	NONE	1M x 36	NONE	pins 1 & 2	Open
8MB	1M x 36	1M x 36	NONE	NONE	Open	Open
8MB	2M x 36	NONE	NONE	NONE	pins 1 & 2	Open
10MB	256K x 36	256K x 36	1M x 36	1M x 36	Open	Open
10MB	512K x 36	NONE	1M x 36	1M x 36	pins 1 & 2	Open
12MB	1M x 36	1M x 36	1M x 36	NONE	Open	Open
12MB	2M x 36	NONE	1M x 36	NONE	pins 1 & 2	Open
16MB	1M x 36	1M x 36	1M x 36	1M x 36	Open	Open
16MB	2M x 36	NONE	1M x 36	1M x 36	pins 1 & 2	Open
16MB	2M x 36	2M x 36	NONE	NONE	pins 2 & 3	Closed
16MB	4M x 36	NONE	NONE	NONE	Open	Open
18MB	256K x 36	256K x 36	256K x 36	NONE	Open	Open
18MB	512K x 36	NONE	4M x 36	NONE	pins 1 & 2	Open
20MB	1M x 36	4M x 36	NONE	NONE	Open	Open
24MB	1M x 36	1M x 36	4M x 36	NONE	Open	Open
24MB	2M x 36	NONE	4M x 36	NONE	pins 1 & 2	Open
32MB	4M x 36	4M x 36	NONE	NONE	Open	Open
32MB	8M x 36	NONE	NONE	NONE	pins 1 & 2	Open
36MB	1M x 36	4M x 36	4M x 36	NONE	Open	Open
40MB	1M x 36	1M x 36	4M x 36	4M x 36	Open	Open
40MB	2M x 36	NONE	4M x 36	4M x 36	pins 1 & 2	Open
40MB	2M x 36	8M x 36	NONE	NONE	pins 2 & 3	Closed
48MB	4M x 36	4M x 36	4M x 36	NONE	Open	Open
48MB	8M x 36	NONE	4M x 36	NONE	pins 1 & 2	Open
64MB	4M x 36	4M x 36	4M x 36	4M x 36	Open	Open
64MB	8M x 36	8M x 36	NONE	NONE	pins 2 & 3	Closed
64MB	8M x 36	NONE	4M x 36	4M x 36	pins 1 & 2	Open
Note: Pins designated should be in the closed position.						

CACHE CONFIGURATION		
Size	Cache	Location
64KB	(8) 8K x 8	Banks 0 & 1
128KB	(4) 32K x 8	Bank 0
256KB	(8) 32K x 8	Banks 0 & 1

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CACHE JUMPER CONFIGURATION					
Size	SR1	SR2	SR3	SR4	SR5
64KB	pins 1 & 2	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 1 & 2
128KB	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 1 & 2	pins 2 & 3
256KB	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3

CPU TYPE CONFIGURATION			
Jumper	80486DX/80486DX2	80486SX	80487SX/ODP586SX
T3JP1A	Closed	Open	Open
T3JP1B	Open	Closed	Open
T3JP1C	Open	Open	Closed
T3JP2A	Closed	Open	Open
T3JP2B	Open	Closed	Open
T3JP2C	Open	Open	Closed
T3JP3A	Closed	Open	Open
T3JP3B	Open	Closed	Open
T3JP3C	Open	Open	Closed

CPU SPEED CONFIGURATION				
CPU speed	JP1	JP2	JP3	JP4
20MHz	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3
25/50i MHz	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 1 & 2
33/66i MHz	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 1 & 2
40MHz	pins 1 & 2	pins 1 & 2	pins 2 & 3	pins 2 & 3
50MHz	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 2 & 3

VESA LOCAL BUS CONFIGURATION			
CPU speed	Wait states	JP9	JP10
$\leq$ 33MHz	0 wait states	pins 1 & 2 closed	pins 1 & 2 closed
$>$ 33MHz	1 wait state	pins 2 & 3 closed	pins 2 & 3 closed