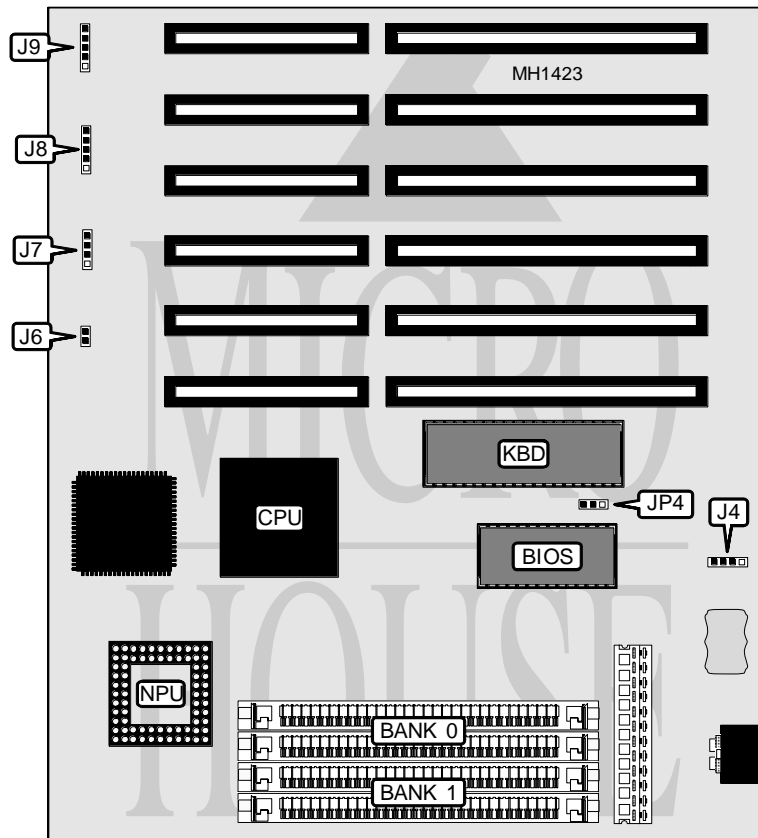


J-MARK COMPUTER CORPORATION 486SLC/386SX (VERSION III)

Processor 80386SX/CX486SLC
Processor Speed 25/33/40MHz
Chip Set ALI
Max. Onboard DRAM 16MB
SRAM Cache None
BIOS AMI/Award
Dimensions 220mm x 170mm
I/O Options None
NPU Options 80387SX/CX487SLC



CONNECTIONS			
Function	Location	Function	Location
External battery	J4	Turbo LED	J8/pins 1 & 2
Reset switch	J6	Turbo switch	J8/pins 4 & 5
Speaker	J7	Power LED & keylock	J9

Continued on next page . . .

J-MARK COMPUTER CORPORATION

486SLC/386SX (VERSION III)

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USER CONFIGURABLE SETTINGS		
Function	Location	Setting
í Monitor type select color	JP4	pins 2 & 3 closed
Monitor type select monochrome	JP4	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9