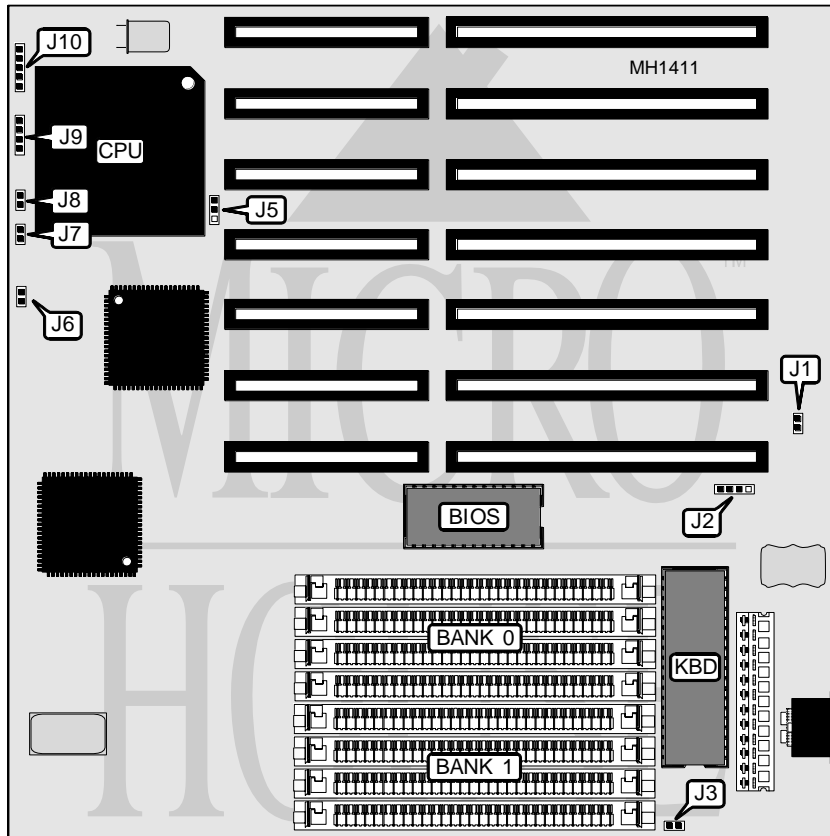


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PETER 486-3350 NON-CACHE (486-3350N)

Processor	80486SX/80487SX/80486DX
Processor Speed	33/50MHz
Chip Set	Symphony
Max. Onboard DRAM	32MB
Cache	None
BIOS	AMI/Award
Dimensions	220mm x 218mm
I/O Options	None
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Reset switch	J8
Turbo LED	J6	Speaker	J9
Turbo switch	J7	Power LED & keylock	J10

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PETER 486-3350 NON-CACHE (486-3350N)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	J1	open
CMOS memory clear	J1	closed
í Monitor type select color	J3	closed
Monitor type select monochrome	J3	open
í CPU type select 80486DX or 80487SX	J5	pins 2 & 3 closed
CPU type select 80486SX	J5	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1MB x 9	NONE
8MB	(4) 1MB x 9	(4) 1MB x 9
16MB	(4) 4MB x 9	NONE
32MB	(4) 4MB x 9	(4) 4MB x 9