UNIDENTIFIED
2 8 6 V

Processor	80286
Processor Speed	10/12MHz
Chip Set	VLSI
Max. onboard DRAM	4MB
SRAM Cache	None
BIOS	AMI
Dimensions	330mm x 220mm
I/O Options	None
NPU Options	80287
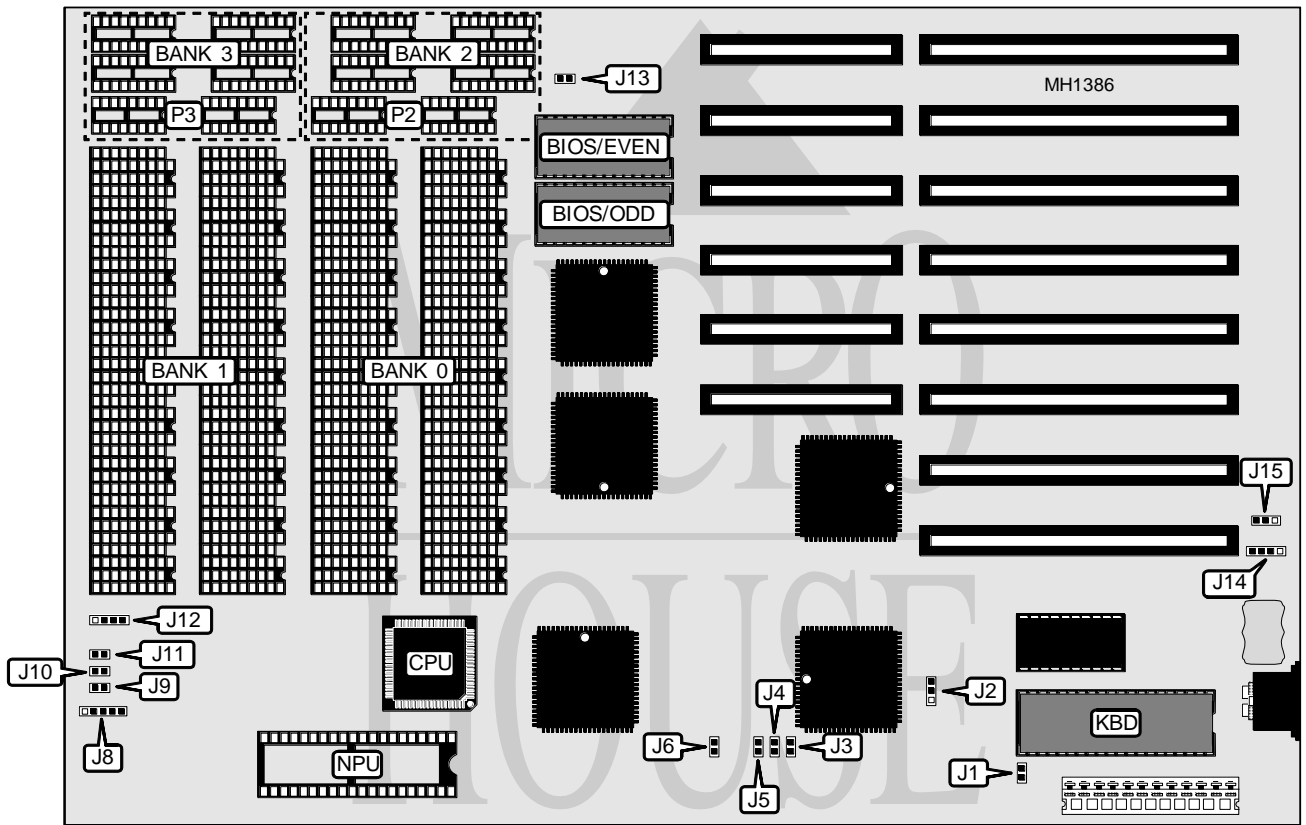
CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J8	Turbo switch	J11
Reset	J9	Speaker	J12
Turbo LED	J10	External battery	J14

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	J1	Closed
Monitor type select monochrome	J1	Open
í Power good signal detect from power supply	J2	pins 2 & 3 closed
Power good signal detect from board	J2	pins 1 & 2 closed
í Memory select 0 wait states	J6	Closed
Memory select 1 wait state	J6	Open
í BIOS type select 27256	J13	Closed
BIOS type select 27512	J13	Open
í CMOS memory normal operation	J15	pins 1 & 2 closed
CMOS memory clear	J15	pins 2 & 3 closed

DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2	Parity 2	Bank 3	Parity 3
512KB	(18) 41256	NONE	NONE	NONE	NONE	NONE
512KB	NONE	NONE	(4) 44256	(2) 41256	NONE	NONE
640KB	(18) 41256	(18) 4164	NONE	NONE	NONE	NONE
1MB	(18) 41256	(18) 41256	NONE	NONE	NONE	NONE
1MB	NONE	NONE	(4) 44256	(2) 41256	(4) 44256	(2) 41256
2MB	(18) 411000	NONE	NONE	NONE	NONE	NONE
4MB	(18) 411000	(18) 411000	NONE	NONE	NONE	NONE

DRAM JUMPER CONFIGURATION			
Size	J3	J4	J5
512KB	Closed	Closed	Closed
640KB	Open	Closed	Closed
1MB	Closed	Open	Closed
2MB	Open	Open	Closed
4MB	Closed	Closed	Open