

JC INFORMATION SYSTEMS CORPORATION

MODEL 2060

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP18	pins 2 & 3 closed
CMOS memory clear	JP18	pins 1 & 2 closed
í Monitor type select color	JP19	closed
Monitor type select monochrome	JP19	open
í BIOS type select EPROM	JP20	pins 2 & 3 closed
BIOS type select Flash ROM	JP20	pins 1 & 2 closed
í On-board VGA enabled	JP21	closed
On-board VGA disabled	JP21	open

VRAM CONFIGURATION	
Size	VRAM
512KB	NONE
1024KB	(4) 44256

Note: The base 512KB is soldered on board.

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(1) 256K x 36	NONE	NONE	NONE
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE
3MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	NONE
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
4MB	(1) 1M x 36	NONE	NONE	NONE
5MB	(1) 256K x 36	(1) 1M x 36	NONE	NONE
6MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	NONE
7MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
9MB	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36	NONE
10MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE
13MB	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	NONE	NONE	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36

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CPU TYPE CONFIGURATION					
Type	JP1	JP2	JP3	JP11	JP13
80486DX2	pins 1 & 2	closed	pins 1 & 2	closed	pins 2 & 3
80486DX	pins 1 & 2	closed	pins 1 & 2	closed	pins 2 & 3
80487SX	pins 1 & 2	closed	pins 2 & 3	closed	pins 2 & 3
80486SX	pins 2 & 3	open	open	closed	pins 2 & 3
80386DX	N/A	N/A	N/A	open	pins 1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED CONFIGURATION					
Speed	OSCI	JP12	S0	S1	S2
20MHz	40MHz	pins 2 & 3	closed	open	closed
25MHz	50MHz	pins 2 & 3	open	open	closed
33MHz	66MHz	pins 2 & 3	closed	closed	open
40MHz	80MHz	pins 2 & 3	open	closed	open
50(internal)MHz	50MHz	pins 2 & 3	open	open	closed
50MHz	50MHz	pins 1 & 2	open	open	closed
66(internal)MHz	66MHz	pins 2 & 3	closed	closed	open

Note: Pins designated should be in the closed position.

SRAM JUMPER CONFIGURATION				
Size	JP4	JP5	JP6	JP7
64KB	open	open	open	open
128KB	closed	open	open	closed
256KB	closed	closed	closed	closed

SRAM CONFIGURATION			
Size	Cache SRAM	Location	TAG
64KB	(8) 8K x 8	Banks 0 & 1	(1) 16K x 4
128KB	(4) 32K x 8	Bank 0	(1) 16K x 4
256KB	(8) 32K x 8	Banks 0 & 1	(1) 16K x 4

PARALLEL & SERIAL PORT IRQ CONFIGURATION		
Function/IRQ	Jumper	Position
í Parallel port interrupt select IRQ7	JP14/PP	pins 1 & 2 closed
Parallel port interrupt select IRQ5	JP14/PP	pins 2 & 3 closed
í Serial port COM 1 interrupt select IRQ4	JP14/S1	pins 1 & 2 closed
Serial port COM 1 interrupt select IRQ3	JP14/S1	pins 2 & 3 closed
í Serial port COM 2 interrupt select IRQ3	JP14/S2	pins 2 & 3 closed
Serial port COM 2 interrupt select IRQ4	JP14/S2	pins 1 & 2 closed